

Supplemental Information

This notice describes the differences between the updated version (Version 3, dated February 6, 2009) and its previous version (Version 2, dated January 11, 2007) of the IDT82P2821 data sheet. It helps readers to identify the changes when the data sheet is upgraded.

Revision History

| Revision Date | PCN Number (if applicable) | Date Code | Changed Items |
|------------------|----------------------------|-----------|---------------|
| February 6, 2009 | - | - | 10 |
| January 11, 2007 | - | - | 9 |
| December 7, 2005 | - | - | 1-8 |

Changed Items

February 6, 2009

Item 9: Changed 'MCLK should be a jitter-free clock with ± 32 ppm (in T1/J1 mode) or ± 50 ppm (in E1 mode) accuracy' to 'MCLK should be a clock with ± 32 ppm (in T1/J1 mode) or ± 50 ppm (in E1 mode) accuracy'. (Page 22, 61, 66)

January 11, 2007

Item 9: Changed the Min and Max values of 'Output Pulse Amplitude' in 'T1/J1 TRANSMITTER ELECTRICAL CHARACTERISTICS' (Page 129)

December 7, 2005

Item 8: Changed the Pd value in '8.1 Absolute Maximum Ratings' (Page 121)

Item 7: Changed the thermal data table and the corresponding data in 3 equations (Page 120)

Item 6: Changed Figure-10, Figure-58 and Figure-59 (Page 31, 136)

Item 5: Changed test conditions for Analog LOS Level (Line Monitor Mode) (Page 126, 127)

Item 4: Added diodes to Figure-46, Figure-47 and Figure-48 (Page 71, 72, 73)

Item 3: Changed the description for the LLOS pin (Page 20, 44)

Item 2: Changed the description for the REF pin (Page 23)

Item 1: Changed data sheet status from 'Advance Information' to final. (Page 10)

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