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Silicon NPN Epitaxial

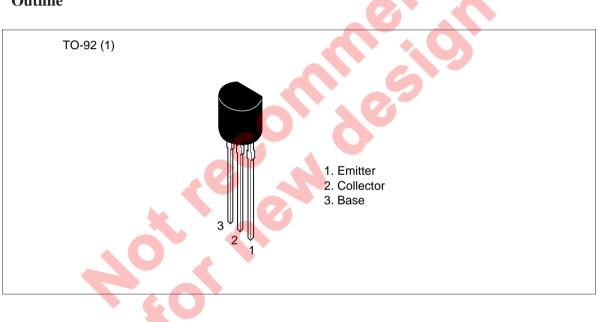
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ADE-208-1156A (Z) 2nd. Edition Mar. 2001

#### Application

• Low frequency power amplifier

#### Outline



#### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Collector to base voltage	V <sub>CBO</sub>	20	V	
Collector to emitter voltage	V <sub>CEO</sub>	16	V	
Emitter to base voltage	V <sub>EBO</sub>	6	V	
Collector current	Ι <sub>c</sub>	2	А	
Collector power dissipation	Pc	0.75	W	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

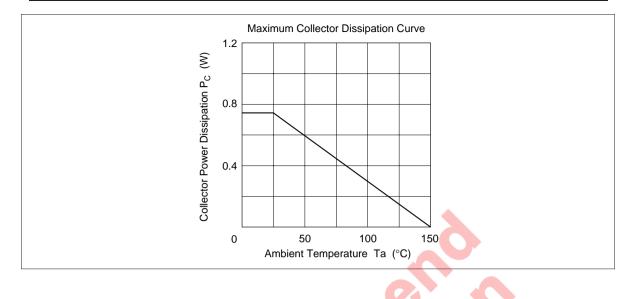
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#### **Electrical Characteristics** (Ta = 25°C)

ltem	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{\rm (BR)CBO}$	20	-	2	V	$I_{c} = 10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{\rm (BR)CEO}$	16	2		V	$I_c = 1 \text{ mA}, R_{BE} = \infty$
Emitter to base breakdown voltage	$V_{(\text{BR})\text{EBO}}$	6		X	V	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I <sub>CBO</sub>	-	_	2	μA	$V_{CB} = 16 \text{ V}, \text{ I}_{E} = 0$
Emitter cutoff current	IEBO		-	0.2	μΑ	$V_{EB} = 6 V, I_{C} = 0$
DC current transfer ratio	h <sub>FE</sub> *1	100		500		$V_{ce} = 2 \text{ V}, I_c = 0.1 \text{ A}$
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	-9	-	0.3	V	$I_{c} = 1 \text{ A}, I_{B} = 0.1 \text{ A}$
Gain bandwidth product	f <sub>T</sub>		80	—	MHz	$V_{ce} = 2 \text{ V}, I_c = 10 \text{ mA}$
Collector output capacitance	Cob	_	20	—	pF	$V_{\text{CB}}$ = 10 V, $I_{\text{E}}$ = 0, f = 1 MHz
Note: 1. The 2SD1489 is gro	ouped by h	<sub>FE</sub> as foll	ows.			
ВСО		_				
100 to 200 160 to 320 2	50 to 500					

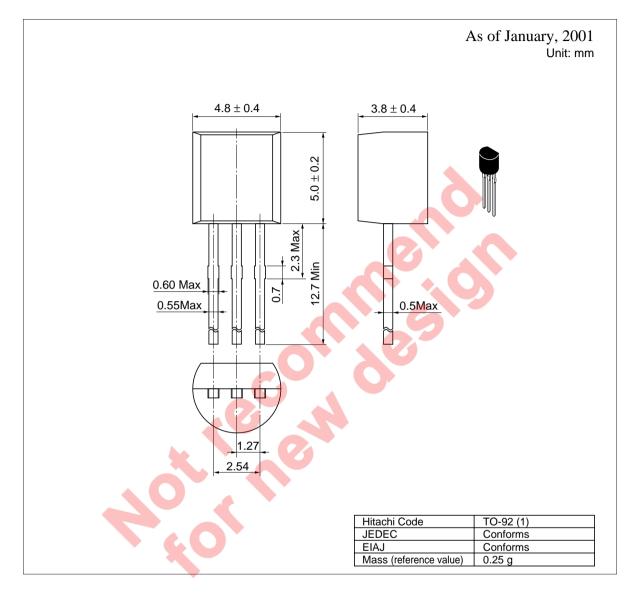
See characteristic curves of 2SD787.

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#### **Package Dimensions**



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