## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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#### Notice

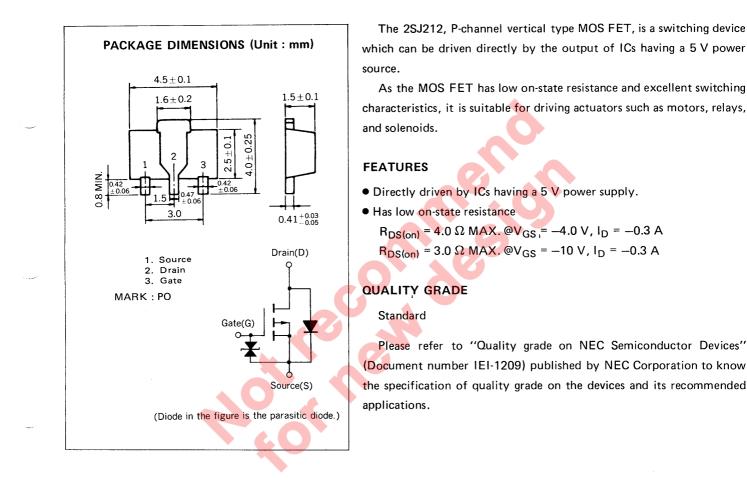
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## RENESAS

## DATA SHEET

# MOS FIELD EFFECT TRANSISTOR 2SJ212

## P-CHANNEL MOS FET FOR SWITCHING



#### ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub> = 25 $^{\circ}$ C)

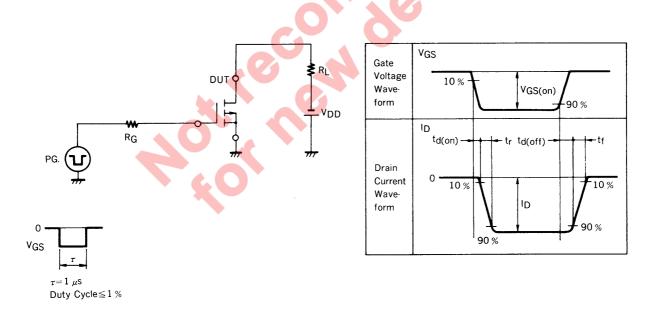
PARAMETER	SYMBOL	RATINGS	UNIT	TEST CONDITIONS
Drain to Source Voltage	V <sub>DSS</sub>	60	V	V <sub>GS</sub> = 0
Gate to Source Voltage	V <sub>GSS</sub>	∓20	V	V <sub>DS</sub> = 0
Drain Current	D(DC)	∓500	mA	
Drain Current	ID(pulse)	<b>∓1.0</b>	А	PW $\leq$ 10 ms, Duty Cycle $\geq$ 50 %
Total Power Dissipation	PT	2.0	W	when using ceramic board of 16 cm <sup>2</sup> x 0.7 mm
Channel Temperature	т <sub>ch</sub>	150	°C	
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C	

Document No. TC-2294B (0.D.No. TC-7748B) Date Published November 1994 M Printed in Japan

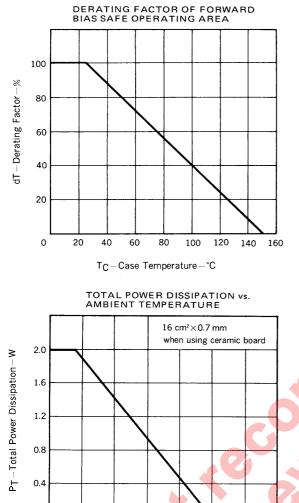
### ELECTRICAL CHARACTERISTICS (T<sub>a</sub> = 25 $^{\circ}$ C)

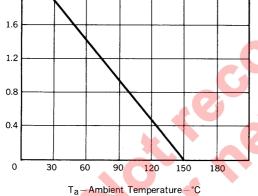
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	IDSS			-10	μA	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0
Gate Leakage Current	IGSS			∓10	μA	V <sub>GS</sub> = ∓20 V, V <sub>DS</sub> = 0
Gate Cut-off Voltage	V <sub>GS(off)</sub>	-1.0	-2.2	3.0	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Forward Transfer Admittance	y <sub>fs</sub>	0.4	0.54		S	V <sub>DS</sub> = -5 V, I <sub>D</sub> = -0.3 A
Drain to Source On-State Resistance	R <sub>DS(on)1</sub>		1.5	4.0	Ω	V <sub>GS</sub> = -4.0 V, I <sub>D</sub> = -0.3 A
Drain to Source On-State Resistance	R <sub>DS(on)2</sub>		0.8	3.0	Ω	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -0.5 A
Input Capacitance	Ciss		160		pF	V <sub>DS</sub> = -5.0 V, V <sub>GS</sub> = 0, f = 1 MHz
Output Capacitance	C <sub>OSS</sub>		100		pF	
Feedback Capacitance	C <sub>rss</sub>		25		pF	
Turn-On Delay Time	<sup>t</sup> d(on)		130		ns	V <sub>GS(on)</sub> = -4 V, R <sub>G</sub> = 10 Ω, V <sub>DD</sub> = -5 V, I <sub>D</sub> = -0.3 A, R <sub>L</sub> = 1.5 Ω
Rise Time	t <sub>r</sub>		380		ns	
Turn-Off Delay Time	<sup>t</sup> d(off)		95		ns	
Fall Time	tf		140		ns	

#### SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS

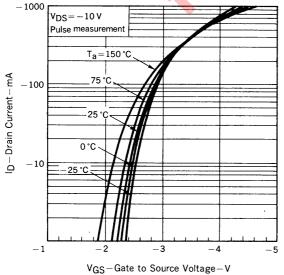


#### TYPICAL CHARACTERISTICS ( $T_a = 25$ °C)

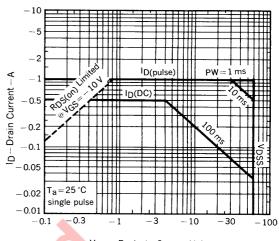






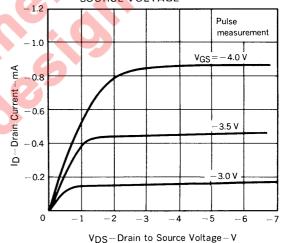




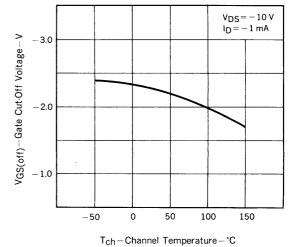


VDS-Drain to Source Voltage-V

DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

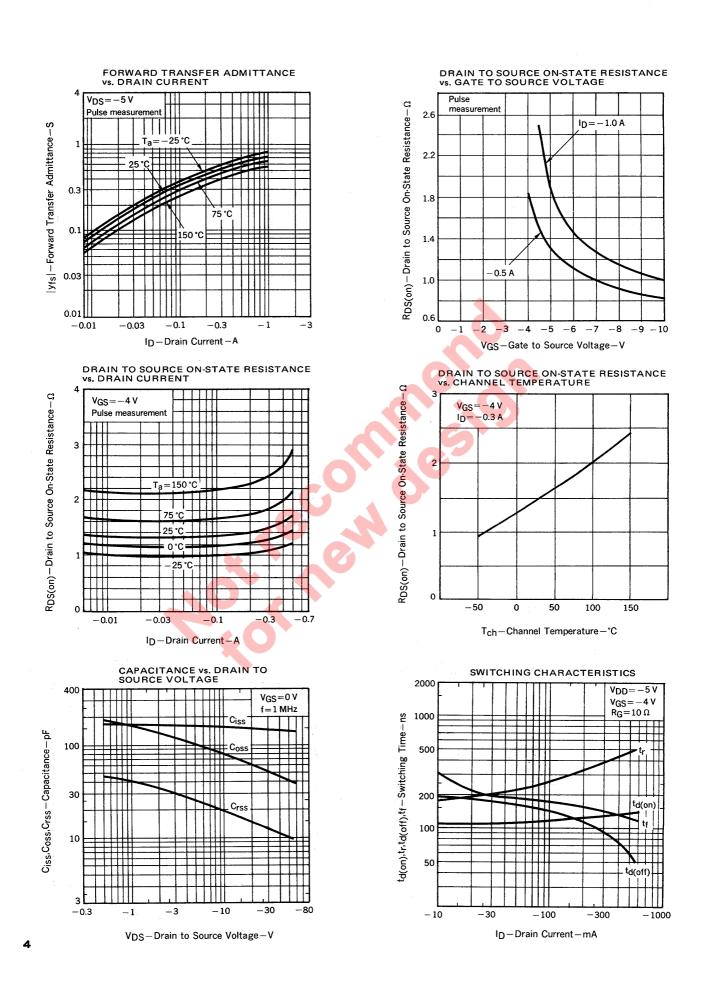


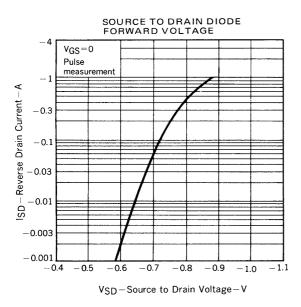
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE



## NEC

## 2SJ212





#### **RECOMMENDED SOLDERING CONDITIONS**

Mounting of this product by soldering should be done under the following conditions.

Please consult our representatives about soldering methods and conditions other than these.

#### SURFACE MOUNT TYPE

For details of the recommended soldering conditions, see the information document.

"Device Mounting Manual for Surface Mounting (IEI-1207)."

Soldering Method	Soldering Conditions	Symbol for Recommended Conditions	
Infrared Reflow	Package peak temp.: 230 °C Soldering time: within 30 sec (above 210 °C) Soldering times: 1, Days limitation: none*	IR30-00	
Vapor Phase Soldering	Package peak temp.: 215 °C Soldering time: within 40 sec (above 200 °C) Soldering times: 1, Days limitation: none*	VP15-00	
Wave Soldering	Soldering bath temp.: below 260 °C Soldering time: within 10 sec Soldering times: 1, Days limitation: none*	WS60-00	

\*: Stored days under storage conditions at 25 °C and below 65 % R.H. after the dry-pack has been opened.

Note 1 Combination of soldering methods should be avoided.

#### REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134

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