Old Company Name in Catalogs and Other Documents

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April 1st, 2010 **Renesas Electronics Corporation**

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SJ533 Silicon P Channel MOS FET

REJ03G0883-0400 (Previous: ADE-208-649B) Rev.4.00 Sep 07, 2005

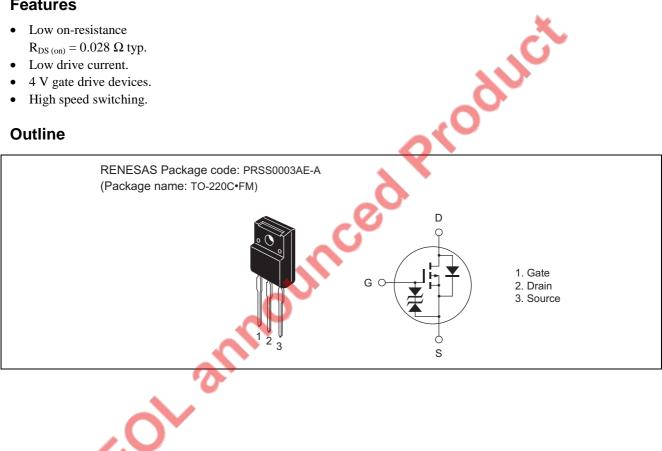
Description

High speed power switching

Features

- Low on-resistance $R_{DS (on)} = 0.028 \Omega$ typ.
- Low drive current.
- 4 V gate drive devices.
- High speed switching.

Outline





Absolute Maximum Ratings

			(Ta = 25°C)
Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	ID	-30	A
Drain peak current	I _{D (pulse)} Note 1	-120	A
Body to drain diode reverse drain current	I _{DR}	-30	A
Avalanche current	I _{AP} Note 3	-30	A
Avalanche energy	E _{AR} Note 3	77	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	۵°

Electrical Characteristics

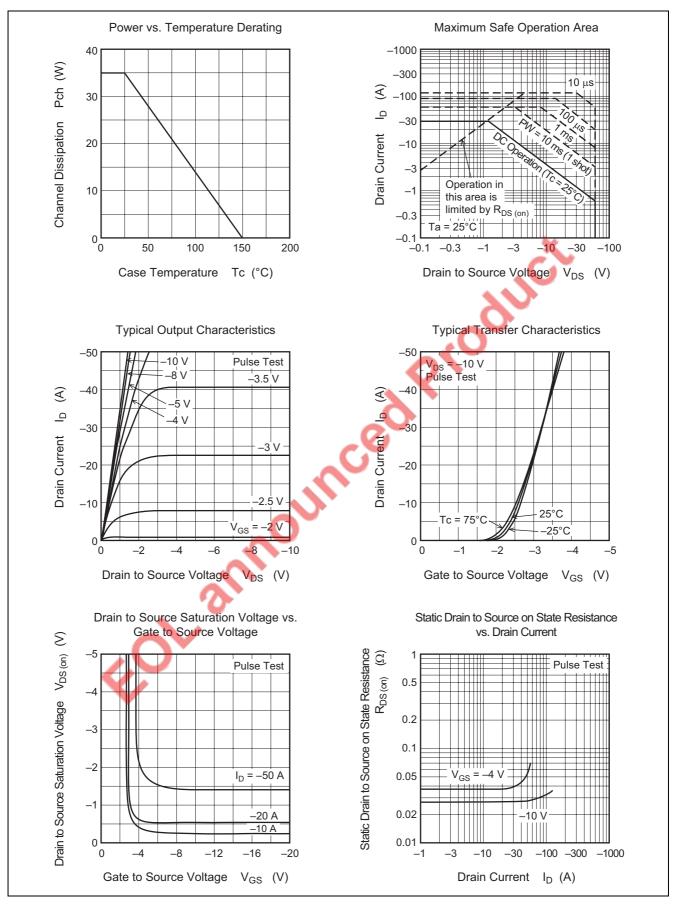
Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$ 2. Value at Tc = 25°C 3. Value at Tch = 25°C, Rg $\ge 50 \ \Omega$ Electrical Characteristics					2	(Ta = 25°C)
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60		Ċ	► V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20		ł	V	$I_G=\pm 100~\mu A,~V_{DS}=0$
Zero gate voltage drain current	I _{DSS}	_		-10	μΑ	$V_{DS} = -60 V, V_{GS} = 0$
Gate to source leak current	I _{GSS}	—		±10	μΑ	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	-1.0	9	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R _{DS (on)}		0.028	0.037	Ω	$I_D = -15 \text{ A}, V_{GS} = -10 \text{ V}^{Note 4}$
	R _{DS (on)}		0.038	0.055	Ω	$I_D = -15 \text{ A}, V_{GS} = -4 \text{ V}^{Note 4}$
Forward transfer admittance	y _{fs}	15	25		S	$I_D = -15 \text{ A}, V_{DS} = -10 \text{ V}^{Note 4}$
Input capacitance	Ciss	_	2500		pF	$V_{DS} = -10 V$
Output capacitance	Coss	_	1300		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	300		pF	f = 1 MHz
Turn-on delay time	t _{d (on)}	_	25		ns	$V_{GS} = -10 V$
Rise time	tr	_	150	_	ns	I _D = -15 A
Turn-off delay time	t _{d (off)}	_	350		ns	$R_L = 2 \Omega$
Fall time	t _f	_	220		ns	
Body to drain diode forward voltage	V_{DF}	_	-0.95		V	$I_F = -30 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	100		ns	$I_F = -30 \text{ A}, V_{GS} = 0$
						di⊧/dt = 50 A/µs

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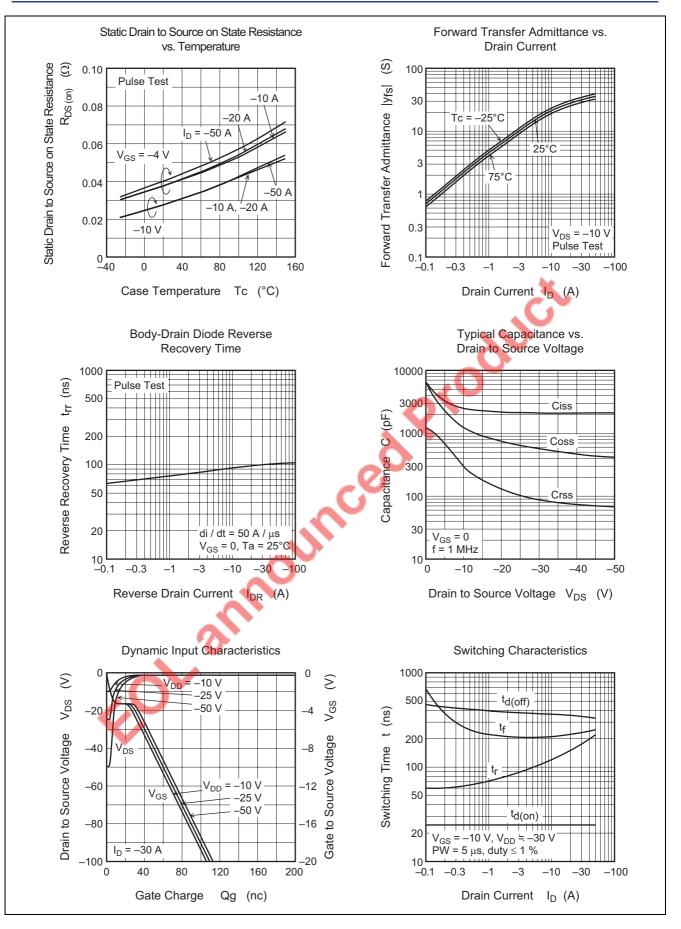
Note: 4. Pulse test



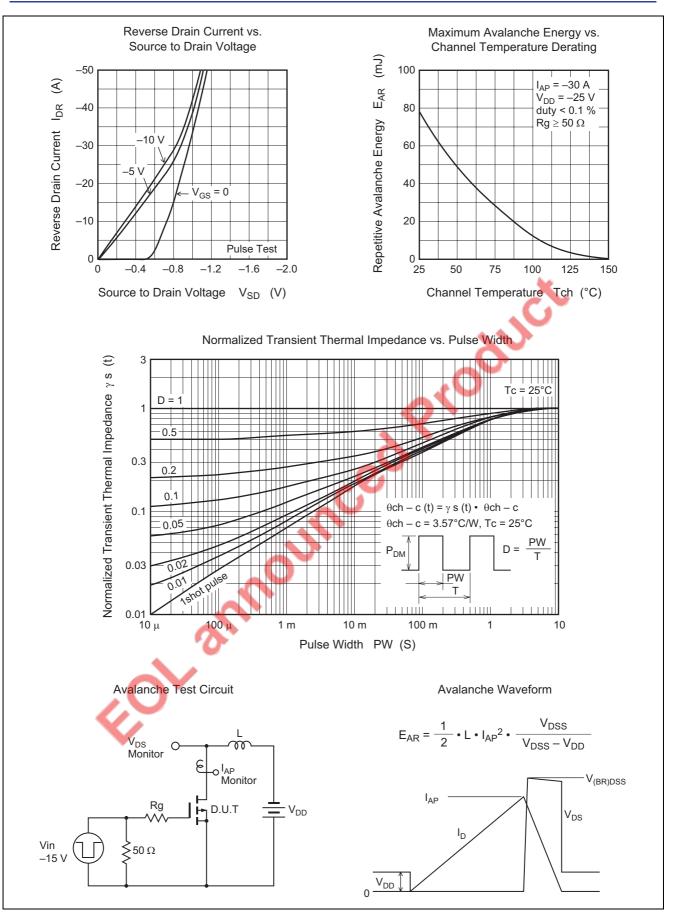
Main Characteristics



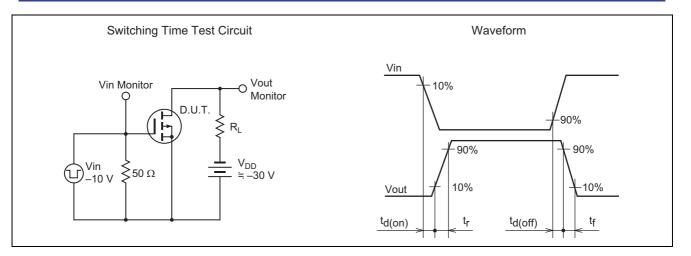




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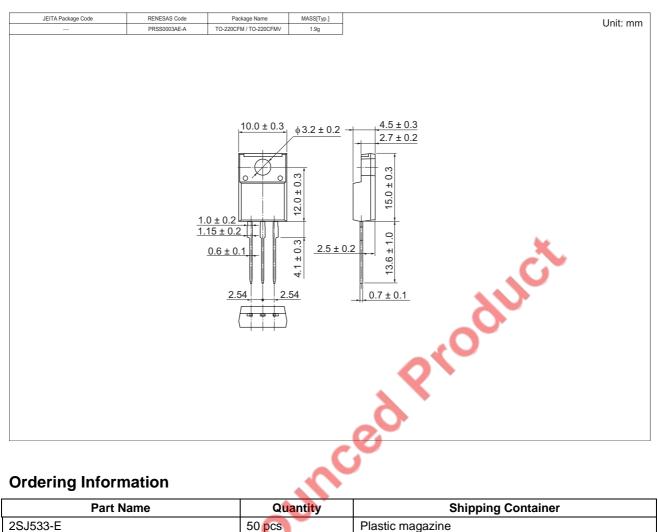








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SJ533-E	50 pcs	Plastic magazine

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