

2SK2054C

N-CHANNEL MOSFET FOR SWITCHING

R07DS1265EJ0200 Rev.2.00 Jun 12, 2015

Description

The 2SK2054C, N-channel vertical type MOSFET designed for general-purpose switch, is a device which can be driven directly by a 4.0 V power source.

Features

- Directly driven by a 4.0 V power source.
- Low on-state resistance

RDS(on)1 = 105 m Ω MAX. (VGS = 10 V, ID = 2.0 A)

 $R_{\text{DS(on)2}}$ = 150 $m\Omega$ MAX. (Vgs = 4.0 V, Ip = 2.0 A)

Ordering Information

Part Number	Lead Plating	Packing	Package	
2SK2054C-T1-AZ/AY	-AZ : Sn-Bi , -AY : Pure Sn	1000p/Reel	SC-84 (MP-2)	

Remark "-AZ/AY" indicates Pb-free. This product does not contain Pb in external electrode and other parts.

Marking XS1

Absolute Maximum Ratings (TA = 25°C)

Drain to Source Voltage (Ves = 0 V)	VDSS	60	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC)	I _{D(DC)}	±4.0	Α
Drain Current (pulse) Note	ID(pulse)	±16	Α
Total Power Dissipation	Рт	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

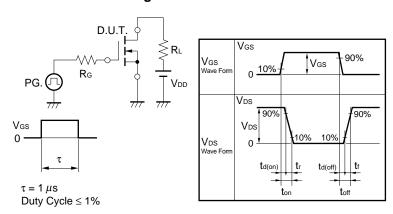
Note PW \leq 10 μ s, Duty Cycle \leq 1%

Electrical Characteristics (T_A = 25°C)

Characteristics	Symbol	Test Conditions	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V _{DS} = 60 V, V _{GS} = 0 V			10	μА
Gate Leakage Current	Igss	$V_{GS} = \pm 18 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μА
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	yfs	V _{DS} = 10 V, I _D = 2.0 A	2.0			S
Drain to Source On-state Resistance Note	R _{DS(on)1}	V _G S = 10 V, I _D = 2.0 A		85	105	mΩ
	R _{DS(on)2}	Vgs = 4.0 V, ID = 2.0 A		106	150	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V,		260		pF
Output Capacitance	Coss	V _G S = 0 V,		65		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		20		pF
Turn-on Delay Time	td(on)	V _{DD} = 30 V,		14		ns
Rise Time	t r	ID = 2 A,		5		ns
Turn-off Delay Time	td(off)	V _G S = 10 V,		80		ns
Fall Time	t _f	$R_G = 10 \Omega$		30		ns
Total Gate Charge	Q _G	ID = 4.0 A, VDD = 48 V, VGS = 10 V		6		nC
Body Diode Forward Voltage ^{Note}	V _{F(S-D)}	IF = 4.0 A, VGS = 0 V		0.9		V

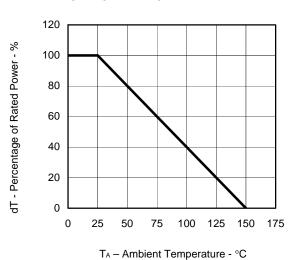
Note Pulsed

Test Circuit Switching Time

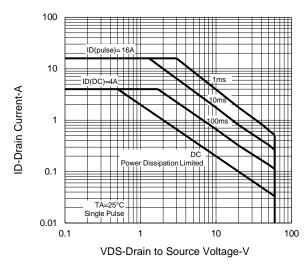


Typical Characteristics (T_A = 25°C)

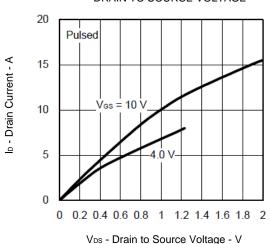
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



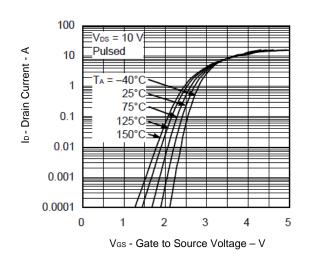
FORWARD BIAS SAFE OPERATING AREA



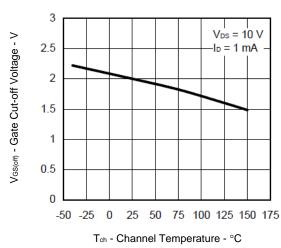
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



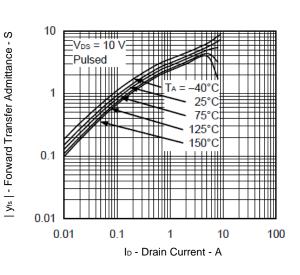
FORWARD TRANSFER CHARACTERISTICS



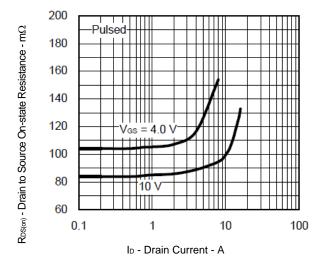
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



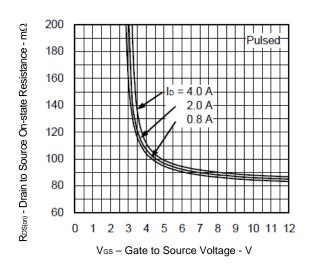
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



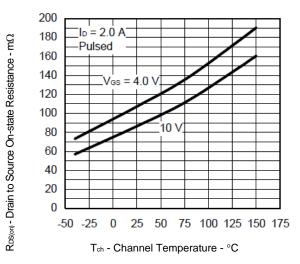
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



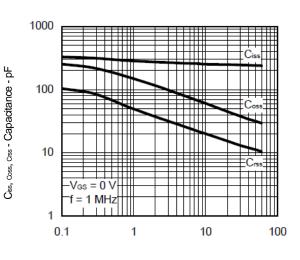
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

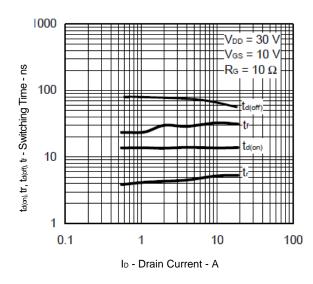


CAPACITANCE vs.
DRAIN TO SOURCE VOLTAGE

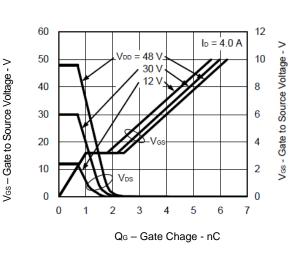


V_{DS} - Drain to Source Voltage - V

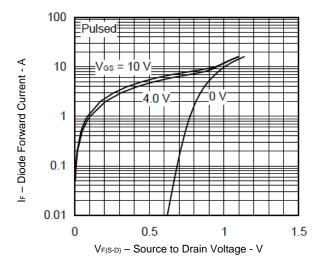
SWITCHING CHARACTERISTICS



DYNAMIC INPUT CHARACTERISTICS

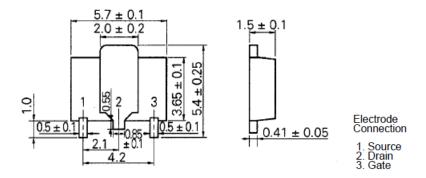


SOURCE TO DRAIN DIODE FORWARD VOLTAGE

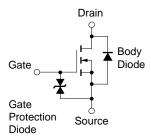


Package Drawings (Unit: mm)

SC-84 (MP-2)



Equivalent Circuit



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

2SK2054C

		Description		
Rev.	Date	Page	Summary	
1.00	Sep , 2013	_	First Edition Issued	
2.00	Jun , 2015	3	Added FORWARD BIAS SAFE OPERATING AREA	

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