

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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8P2SM, 8P4SM, 8P2SMA, 8P4SMA

8 A(av.) MOLD ISOLATED THYRISTOR

DESCRIPTION

The 8P^F JSM and 8P^F JSMA are P gate all diffused mold type Thyristor granted Amp On-state Average Current (T_C = 88 °C), with rated voltages up to 400 volts.

FEATURES

- Mold Isolated package.
- 100 A surge current.
- High Voltage : V_{DRM}, V_{RRM} = 200 V (8P2SM, 8P2SMA)
V_{DRM}, V_{RRM} = 400 V (8P4SM, 8P4SMA)

APPLICATIONS

- Motor speed control for household appliance.
- Temperature control for heater and constant temperature box.
- Constant voltage power source and battery charger.
- Automotive application such as regulator.
- Various solid state relay, etc.

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	8P2SM, 8P2SMA	8P4SM, 8P4SMA	UNIT	NOTE
Non-Repetitive Peak Reverse Voltage	V _{RSM}	300	500	V	
Non-Repetitive Peak Off-State Voltage	V _{DSM}	300	500	V	
Repetitive Peak Reverse Voltage	V _{RRM}	200	400	V	
Repetitive Peak Off-State Voltage	V _{DRM}	200	400	V	
Average On-State Current	I _{T(AV)}	8 (T _C = 88 °C, θ = 180 ° Single phase half wave)		A	See Fig. 11
Surge On-State Current	I _{TSM}	100		A	See Fig. 2
Fusing Current	∫ i _T ² dt	45 (1 ms ≤ t ≤ 10 ms)		A ² s	
Peak Gate Power Dissipation	P _{GM}	5 (f ≥ 50 Hz, Duty ≤ 10 %)		W	See Fig. 3
Average Gate Power Dissipation	P _{G(AV)}	0.5		W	
Peak Gate Forward Current	I _{FGM}	2 (f ≥ 50 Hz, Duty ≤ 10 %)		A	
Peak Gate Reverse Voltage	V _{RGM}	10		V	
Junction Temperature	T _j	-40 to +125		°C	
Storage Temperature	T _{stg}	-55 to +150		°C	
Isolation Voltage	-	1500 (AC 1 min)		V _{RMS}	Only 8P ^F JSM

Phase-out/Discontinued

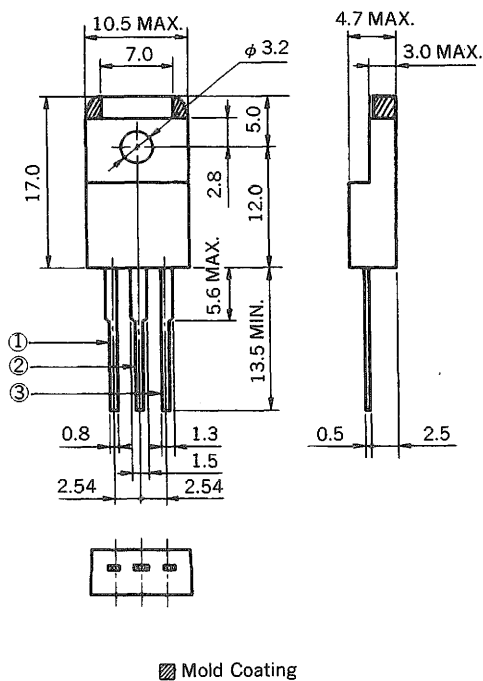
ELECTRICAL CHARACTERISTICS ($T_j = 25\text{ }^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Repetitive Peak Reverse Current	I_{RRM}	$V_{RM} = V_{RRM}, T_j = 125\text{ }^\circ\text{C}$	—	—	2	mA	
Repetitive Peak Off-State Current	I_{DRM}	$V_{DM} = V_{DRM}, T_j = 125\text{ }^\circ\text{C}$	—	—	2	mA	
On-State Voltage	V_{TM}	$I_{TM} = 25\text{ A}$	—	—	1.4	V	See Fig. 1
Gate-Trigger Current	I_{GT}	$V_{DM} = 6\text{ V}, R_L = 100\ \Omega$	—	—	10	mA	See Fig. 4
Gate-Trigger Voltage	V_{GT}	$V_{DM} = 6\text{ V}, R_L = 100\ \Omega$	—	—	1.5	V	
Gate Non-Trigger Voltage	V_{GD}	$V_{DM} = \frac{1}{2} V_{DRM}, T_j = 125\text{ }^\circ\text{C}$	0.2	—	—	V	
Critical Rate of Rise of Off-State Voltage	dv/dt	$V_{DM} = V_{DRM}, T_j = 125\text{ }^\circ\text{C}$	—	40	—	V/ μs	
Holding Current	I_H	$V_D = 24\text{ V}$	—	6	—	mA	
Circuit Commuted Turn-Off Time	t_q	$I_{TM} = 5\text{ A}, V_R \geq 25\text{ V}$ $V_{DM} = \frac{2}{3} V_{DRM}, diR/dt = 15\text{ A}/\mu\text{s}$ $dv/dt = 10\text{ V}/\mu\text{s}, T_j = 125\text{ }^\circ\text{C}$	—	100	—	μs	
Thermal Resistance	R_{th}	Junction to case	—	—	3.7	$^\circ\text{C}/\text{W}$	See Fig. 13

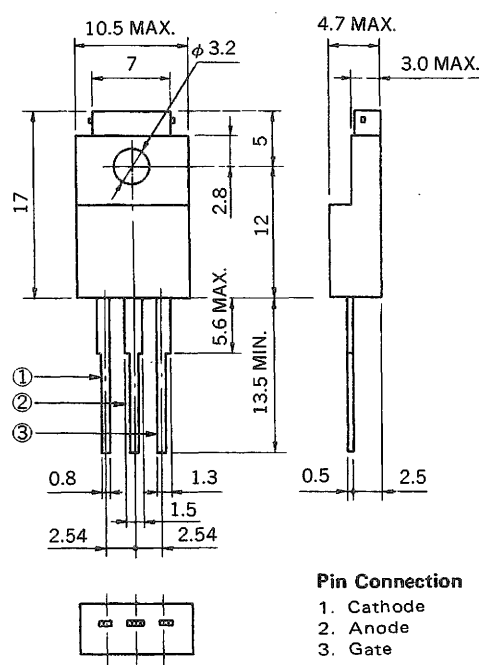
PACKAGE DIMENSIONS

(Unit : mm)

8P2SM, 8P4SM



8P2SMA, 8P4SMA



Pin Connection

- 1. Cathode
- 2. Anode
- 3. Gate

CHARACTERISTICS

Fig. 1 $i_T - V_T$ CHARACTERISTIC

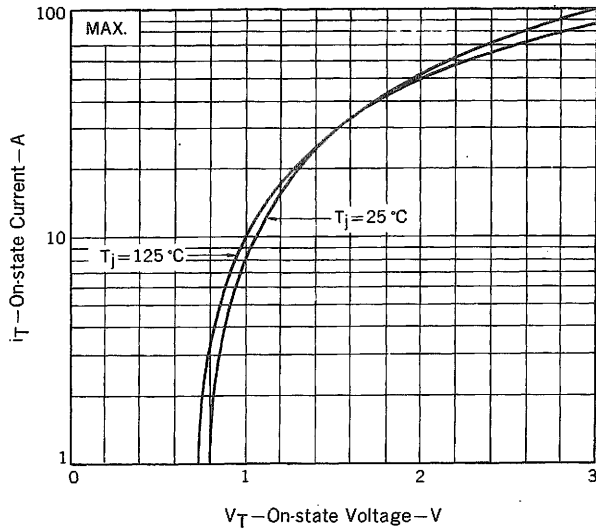


Fig. 2 I_{TSM} RATING

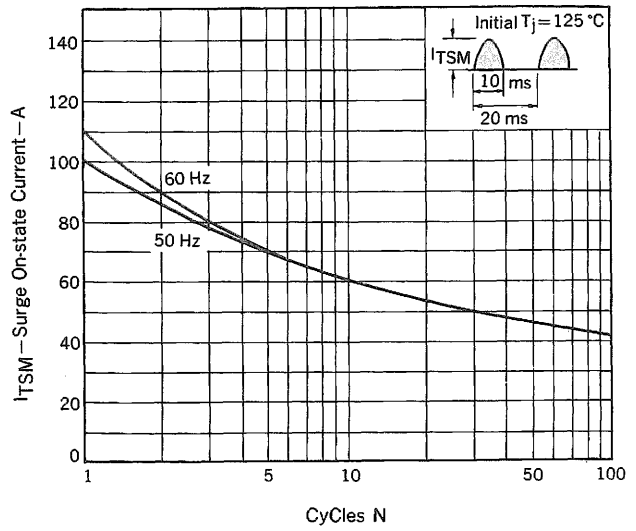


Fig. 3 GATE POWER RATING

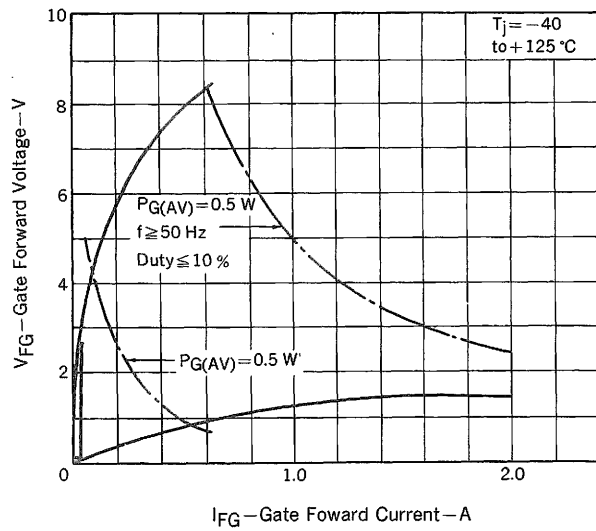


Fig. 4 $I_{GT} - V_{GT}$ DISTRIBUTION

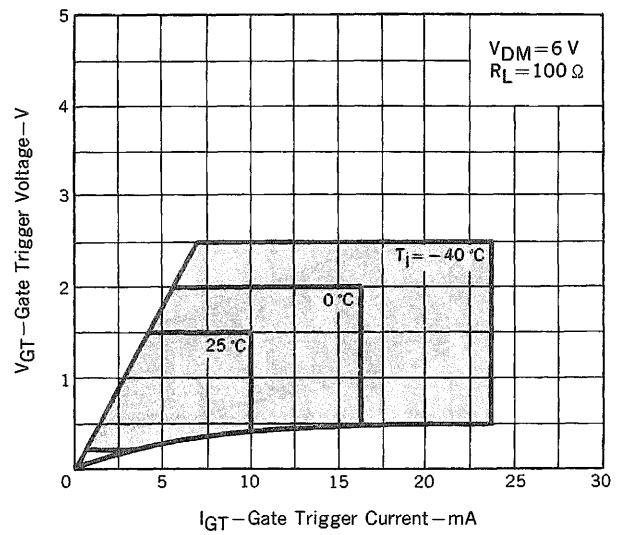


Fig. 5 $I_{GT} - T_a$ TYPICAL DISTRIBUTION

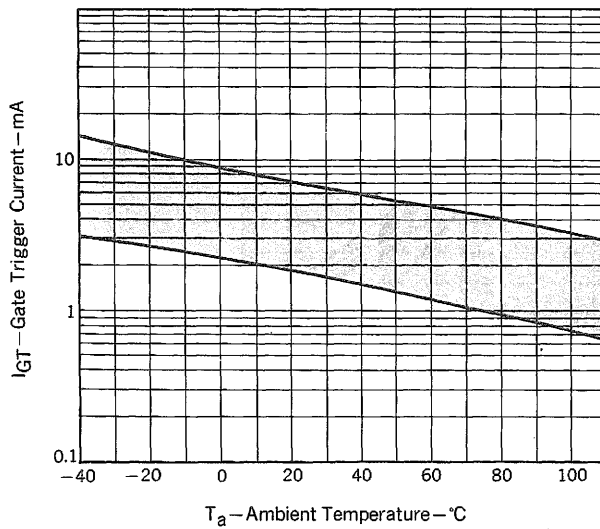


Fig. 6 $V_{GT} - T_a$ TYPICAL DISTRIBUTION

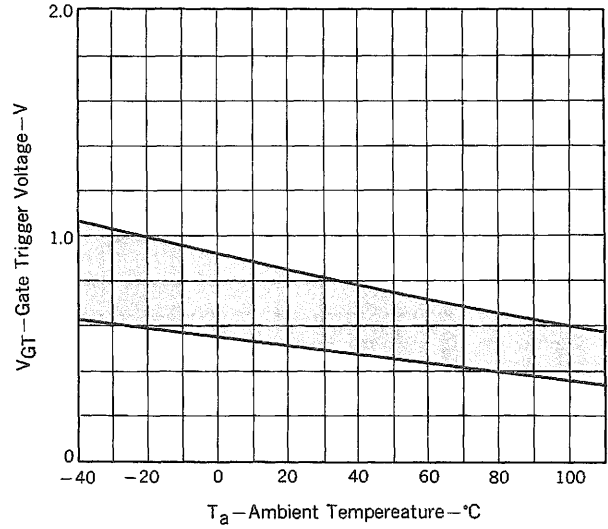


Fig. 7 $i_{GS} - \tau_G$ TYPICAL DISTRIBUTION

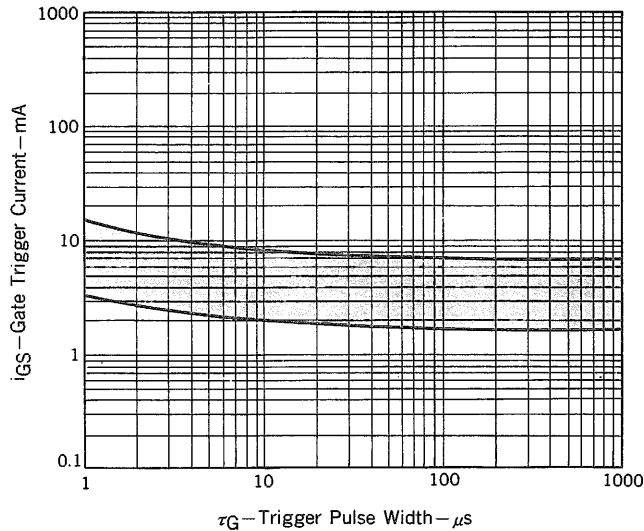


Fig. 8 $V_{GT} - \tau_G$ TYPICAL DISTRIBUTION

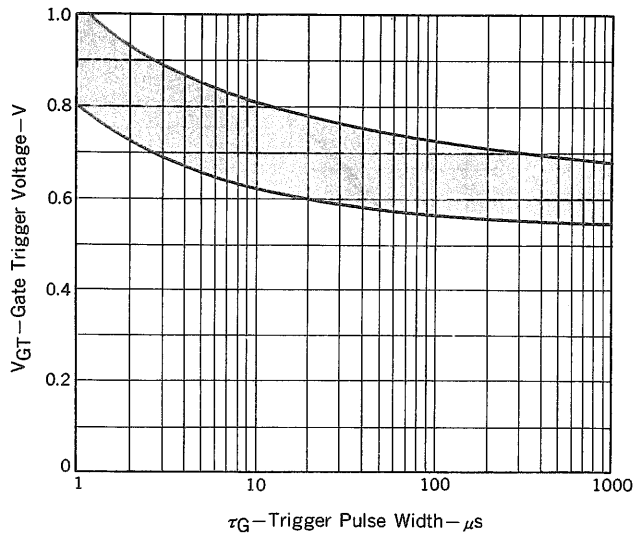


Fig. 9 $I_H - T_a$ TYPICAL DISTRIBUTION

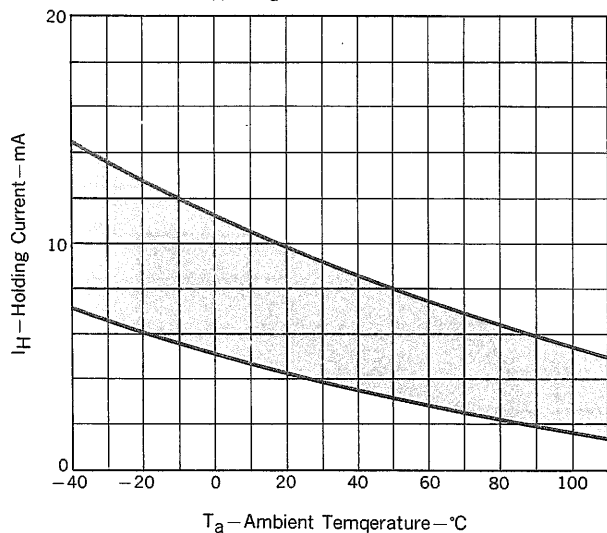


Fig. 10 $P_T(AV) - I_T(AV)$ CHARACTERISTICS

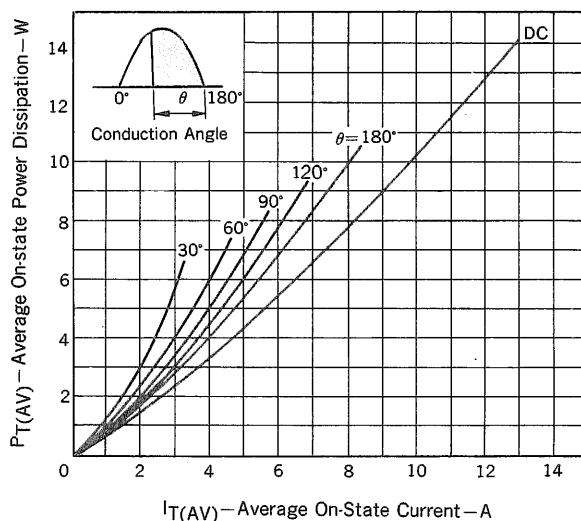


Fig. 11 $T_c - I_T(AV)$ RATING

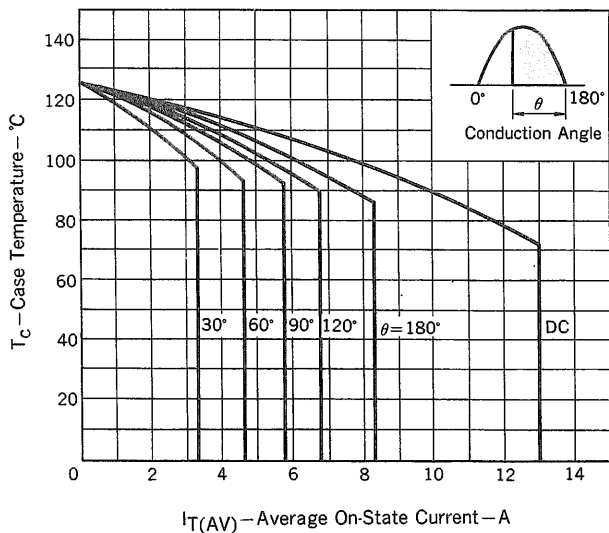
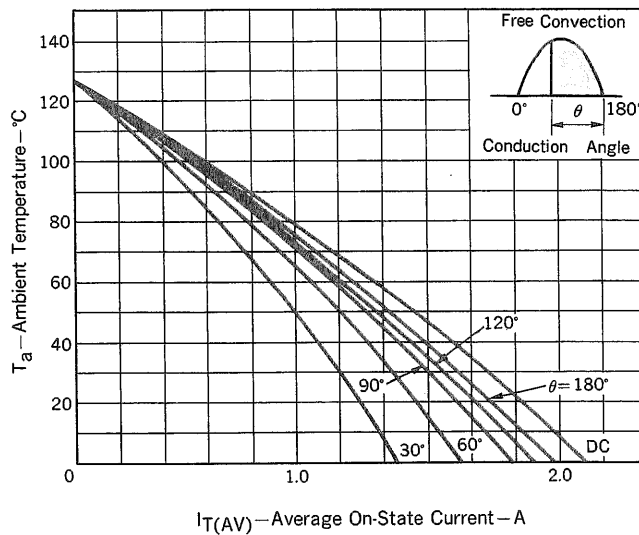
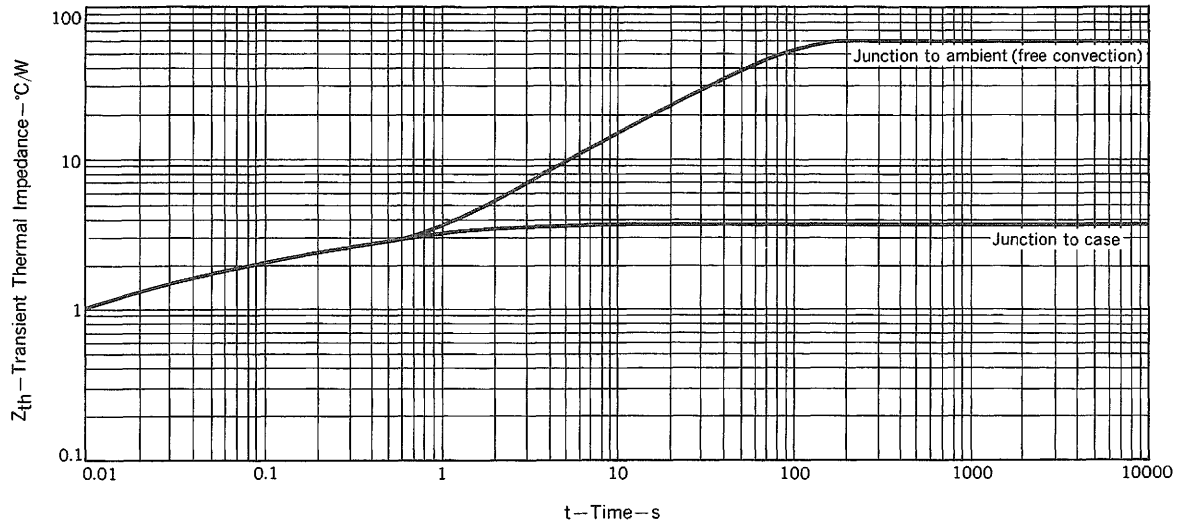


Fig. 12 $T_a - I_T(AV)$ RATING



Phase-out/Discontinued

Fig. 13 Z_{th} CHARACTERISTICS



Phase-out/Discontinued