

RBC25A125B1UFWA

1250V - 25A - Fast Recovery Diode

R07DS1503EJ0120 Rev.1.20 Oct.18th.2024

Features

• Forward voltage: $V_F = 2.8 \text{ V typ.}$ (at $I_F = 25 \text{ A}$)

• High speed switching

• Applications: UPS, Welding, photovoltaic inverters, Power converter system

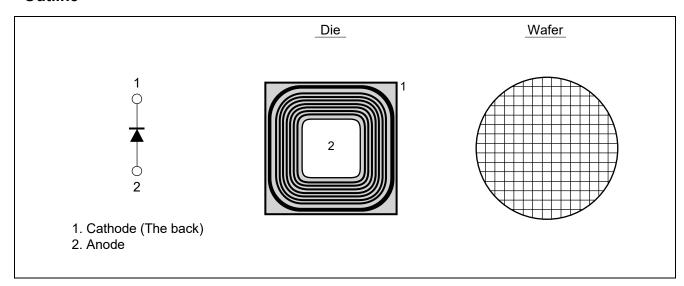
• Unsawn wafer Wafer size = 200 mm

• Quality grade: Standard

Key performance

Product name	V _R	lF	Die size	Package
RBC25A125B1UFWA	1250 V	25 A	9.0 mm ²	Unsawn wafer
			(3.0 mm x 3.0 mm)	

Outline



Mechanical parameter

Die size	3.0 x 3.0 m		
Area total	9.0 r		
Thickness	0.147 typ. r		
Wafer size	193.9		
Passivation front side	Polyimide		
Pad metal	AlSi – 5.2 μm		
Backside metal	Ni/Au		

Absolute Maximum Ratings

(Tj = 25 °C unless otherwise noted)

Item	Symbol	Ratings	Unit
Maximum reverse voltage	V _{RM}	1250	V
Forward current	lF	Notes1	Α
Junction temperature	T _j Notes2	175 Notes2	°C

Notes: 1. Depends on thermal properties of assembly. Tj = 175 °C.

- 2. Please use this device in the thermal conditions which the junction temperature does not exceed 175 $^{\circ}$ C.
- 3. Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Electrical Characteristics

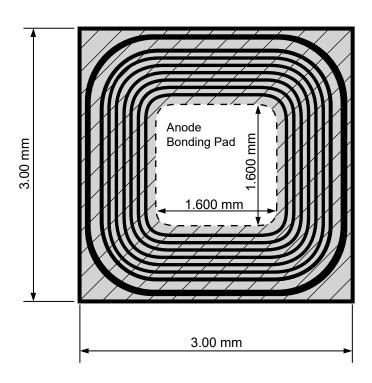
(Tj = 25 °C unless otherwise noted)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Forward voltage	VF	_	2.8	3.64	V	I _F = 25 A Notes5, 6, 7
Reverse current	I _R	_	_	10	μΑ	V _{CE} = 1250 V Notes4
Reverse voltage	V _R	1250	_	_	V	I _R = 100 μA ^{Notes4, 5}

Notes: 4. Tested on wafer

- 5. Pulse test
- 6. Designed target value on Renesas measurement condition. (Not tested)
- 7. Characteristic value on TO-247 package
- 8. Characteristic items prescribed in this document will guarantee the electrical characteristics in chip state but not the characteristic fluctuations or characteristic defects that occur in the processes after assembling.
- 9. Switching characteristics is depending strongly on module design and mounting technology and can therefore not be specified for a bare die.
- 10. Please refer to "R07DS1380 RBN40H125S1FPQ-A0 Data sheet" for packaged product datasheet.

Die Dimension

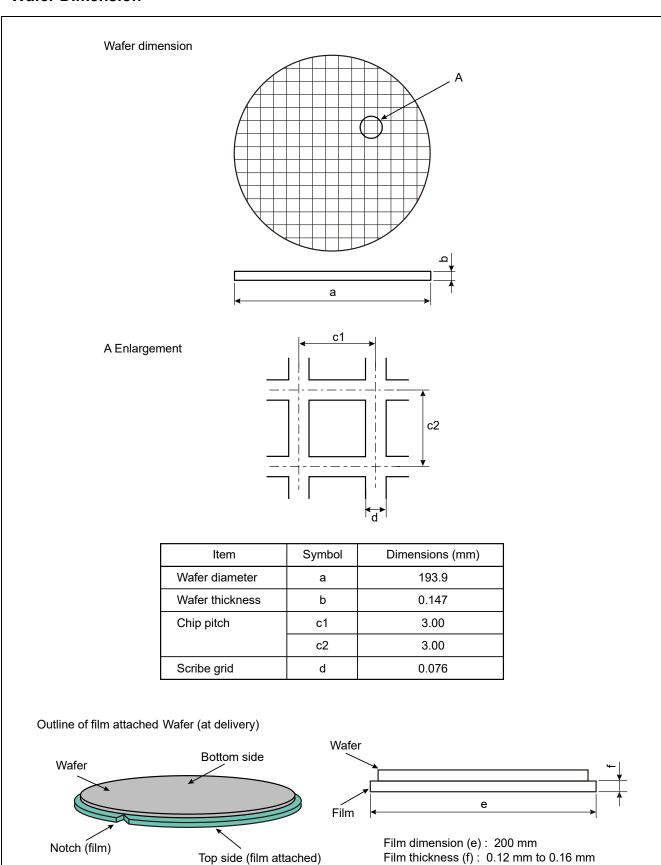


Notes 1:

Illustration	Definition	
Part of dotted line	Bonding area	
Part of gray	Final passivation	

Notes 2: Recognition, target and any other patterns which are not related to FRD operation, may be changed without notice.

Wafer Dimension



Ordering Information

Please contact your Renesas sales representative for sample requests.

Delivery Form	Ordering Part Number	Ordering Quantity Unit
Unsawn wafer	RBC25A125B1UFWA-030#FF0	4941 (3 wafers)
Unsawn wafer	RBC25A125B1UFWA-0F0#FF0	21411 (13 wafers)

Note. The order quantities indicate the maximum quantity of chips for each part number, and the actual quantity of chips shipped will be reduced due to yield. These is also a possibility that the number of wafers may decrease during the manufacturing process. The quantity shipped will be indicated on the label as the number of good chips.

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

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