

# RJP1C05DWA

1200V - 150A - IGBT

R07DS1548EJ0100 Rev.1.00 Oct.25th.2024

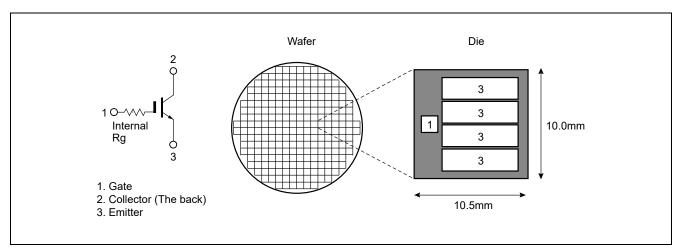
# Features

- 1200V AE4 Trench & Field Stop Technology
- Low collector to emitter saturation voltage V<sub>CE(sat)</sub> = 1.5 V typ. (at I<sub>C</sub> = 150 A, V<sub>GE</sub> = 15 V, Tj = 25 °C)
- Low Switching loss
- Easy paralleling by internal Rg
- Applications: Inverter
- Quality grade: Standard

#### Key performance

Product name	V <sub>CES</sub>	lc	Die size	Package
RJP1C05DWA	1200 V	150 A	105.0 mm <sup>2</sup>	Unsawn wafer
			(10.5 mm x 10.0 mm)	

#### Outline



# **Mechanical Parameters**

Parameter	value
Die size	105.0 mm <sup>2</sup> (10.5 mm x 10.0 mm)
Emitter pad size	See Die Dimension
Gate pad size	1.5 mm x 1.5 mm
Die thickness	130 μm
Wafer size	200 mm
Passivation front side	Polyimide
Pad metallization	AlSi – 5.5 μm
Backside metallization	Ni / Au - 0.6 μm / 0.1 μm
Die attach recommendation	Solder
Wire bond recommendation	Al wire $\leq$ 500 $\mu$ m
Recommended storage environment	Stored in original container, in dry air or nitrogen.
	< 6 months after packing, at an ambient temperature of
	20 to 30 °C, dew-point under -30 °C



### Absolute Maximum Ratings

		(1) = 25 C unless	otherwise noted)
ltem	Symbol	Ratings	Unit
Collector to emitter voltage	V <sub>CES</sub>	1200	V
Gate to emitter voltage	V <sub>GES</sub>	±30	V
Collector current	Ic Notes1	—	А
Pulse collector current	I <sub>C(pulse)</sub> Notes 2, 3	300	А
Junction temperature	Tj <sup>Notes4</sup>	175	С°

 $(Ti - 25 \circ C)$  unless otherwise noted)

Notes: 1. Depends on thermal properties of assembly, Tj = 175 °C.

2. Not subject to product test – verified by design/characterization.

3. PW = 10  $\mu$ s, Duty < 1 %

4. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C. IGBT Application Note is disclosed about reliability test and application condition up to Tj = 175 °C.

- It is not recommended to use this product in applications that the maximum power supply voltage exceeds 750 V. Switching operation under the condition that the power supply voltage exceeds 750 V might damage the product.
- 6. Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

#### **Electrical Characteristics 1**

		(Tested on wafer,	Tj= 25 °0	C unless	otherwise	e noted)
Item	Symbol	Test Conditions	Min	Тур	Max	Unit
Zero gate voltage collector current	Ices	V <sub>CE</sub> = 1200 V, V <sub>GE</sub> = 0 V	—	—	10	μA
Gate to emitter leak current	Iges	$V_{GE} = \pm 30 \text{ V}, \text{ V}_{CE} = 0 \text{ V}$	—	—	±1	μA
Gate to emitter cutoff voltage	$V_{\text{GE(th)}}$	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 150 mA	6.0	7.0	8.0	V
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	Ic = 150 A, V <sub>GE</sub> = 15 V	—	1.50	1.90	V
Internal Gate Resistor	Rg	—	—	4.0		Ω

Notes: 7. The characteristic items specified in this table guarantee the electrical characteristics in the wafer state but do not the characteristic fluctuations or characteristic defects that occur in the processes after assembling.

## **Electrical Characteristics 2**

(Not subject to production test, designed target value, Tj = 25 °C unless otherwise noted)

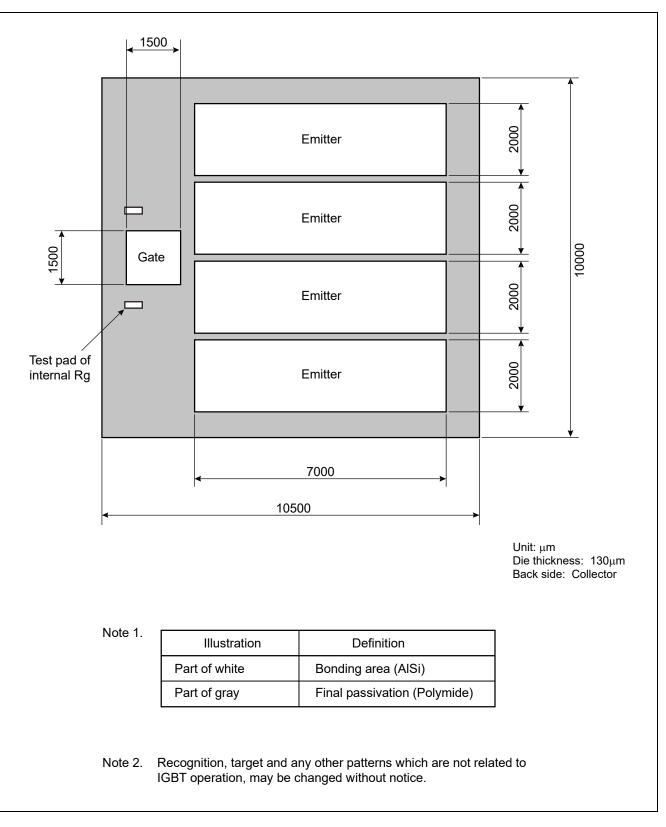
Item	Symbol	Test Conditions	Min	Тур	Max	Unit
Input capacitance	Cies Notes8,9	V <sub>CE</sub> = 25 V, V <sub>GE</sub> = 0		10000		рF
Output capacitance	Coes Notes8,9	f = 1 MHz	—	385	_	pF
Reveres transfer capacitance	Cres Notes8,9		—	115	_	pF
Short Circuit Withstand Time	tsc Notes8,9	$V_{GE}$ = 15 V, $V_{CC} \le 700$ V Tc = 150 °C	6	—	_	μS

Notes 8. Designed target value on Renesas measurement condition.

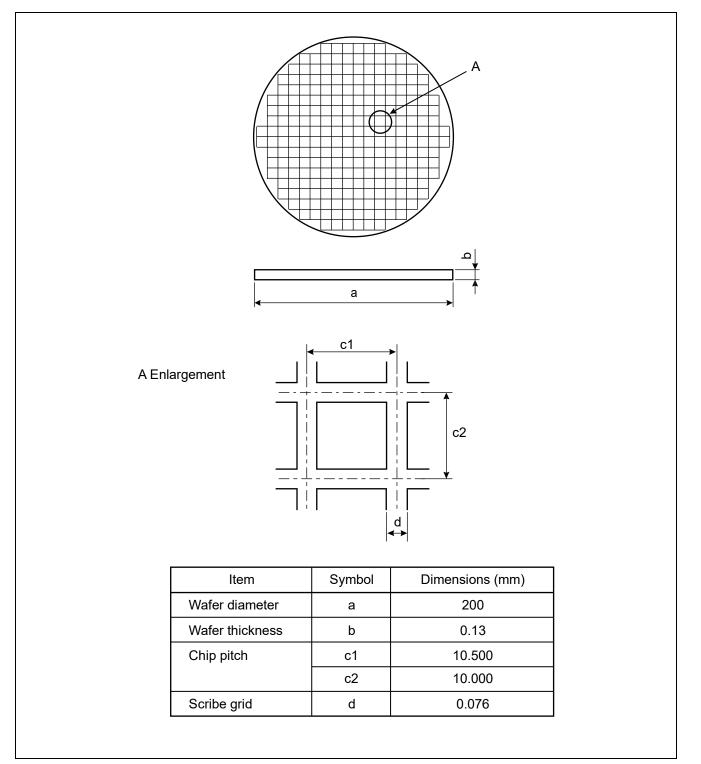
9. This value is influenced by parasitic inductance and assembly condition

10. Switching characteristics is depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

#### **Die Dimension**



#### Wafer Dimension



# **Ordering Information**

Please contact your Renesas sales representative for sample requests.

Delivery Form	Ordering Part Number	Ordering Quantity Unit
Unsawn wafer	RJP1C05DWA-00#W0	6100 (25 wafers)

Note. The order quantities indicate the maximum quantity of chips for each part number, and the actual quantity of chips shipped will be reduced due to yield. These is also a possibility that the number of wafers may decrease during the manufacturing process. The quantity shipped will be indicated on the label as the number of good chips.

# **Revision History**

		Description		
Rev.	Date	Page	Summary	
1.00	2024.10.25		First edition	

All documents should contain the following section break and paragraph as the last item. The footers of this document refer to the paragraph in order to reference the last page of the document.



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