

## Analog IP

# 12bit 2MSps A/D Converter

### Overview

This IP is Successive Approximation Register (SAR) Analog to Digital Converter.

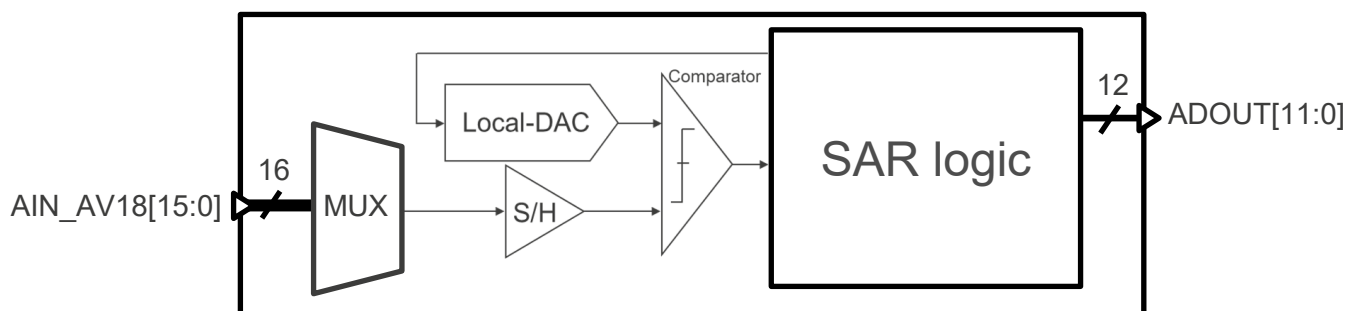
### Features

- Conversion method: Successive Approximation Register (SAR)
- Sampling rate: 2MSps(Max.)
- A/D clock frequency: 108 MHz
- Resolution: 12 bit
  - DNL= -1LSB(Min.) / +2LSB(Max.)
  - INL = -4LSB(Min.) / +4LSB(Max.)
- Analog supply voltage :  $V_{CCA} = 1.8 \pm 0.15 \text{ V}$
- Digital supply voltage:  $V_{DD} = 0.9 \pm 0.09 \text{ V}$
- Consuming current: 1mA(max)
- Junction Temperature:  $-40 - 125 \text{ }^\circ\text{C}$
- Input Channel: 16

### Technology

Process: T22ULP

### Block diagram



CTPD-24-170  
R06PF0060EJ0102