

Analog IP

Analog-PLL For Spread Spectrum Clock

Key Feature

- Including Loop-filter
- VCO operating range : 640MHz - 1056 MHz
- Output frequency range : 80MHz – 1056 MHz
- Input frequency range : 10MHz - 132MHz
- Multiplying (Output freq. / PFD freq.) : 8 - 88
- Divider
 - 7bit feedback divider and 3bit input divider
- Power-down Mode
- Multiple outputs with post-divider
- SSC function available
 - spread type : down
 - modulation frequency : 20kHz – 33kHz
 - modulation depth : 2.273% - 3.125%

Technology

Process : TSMC 40nm LP

Operating condition

Parameter		Min	Max	Unit
Operating Voltage (AVDD)		1.04	1.26	V
Operating Voltage (VDD)		1.045	1.26	V
Junction Temperature		-40	125	°C
Input Clock	Duty	30	70	%
	Rise/Fall time	-	0.2	ns

**This IP is contract design IP. Please contact for detail.*

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