

Renesas Starter Kit for RX62G CPU Board Schematics

REV	REF	DATE	DRAWN BY
1.00	Release	02.10.2012	YOI

PAGE	DESCRIPTION
1	INDEX
2	RX62G Microcontroller
3	PSU, RESET, Switches, LEDs
4	E1 & E20 Emulator, MCU Mode Setting, Serial Port
5	Application Headers, Header Function Select, MCU Pin Function Select
6	Debug LCD, CAN, LIN
7	Microcontroller Pin Headers

Note:

R : Fixed Resistor
 RV : Potentiometer
 U : Integrated Circuit
 X : Crystal
 RES : Reset Switch
 SW : Switch
 LED : Light Emitting Diode
 PWR : Power Jack
 J : Connector, Jumper

Board Code:

R0K50562GC000BE : MP Product Board

* "DNF" marking means that component does not fit by default.

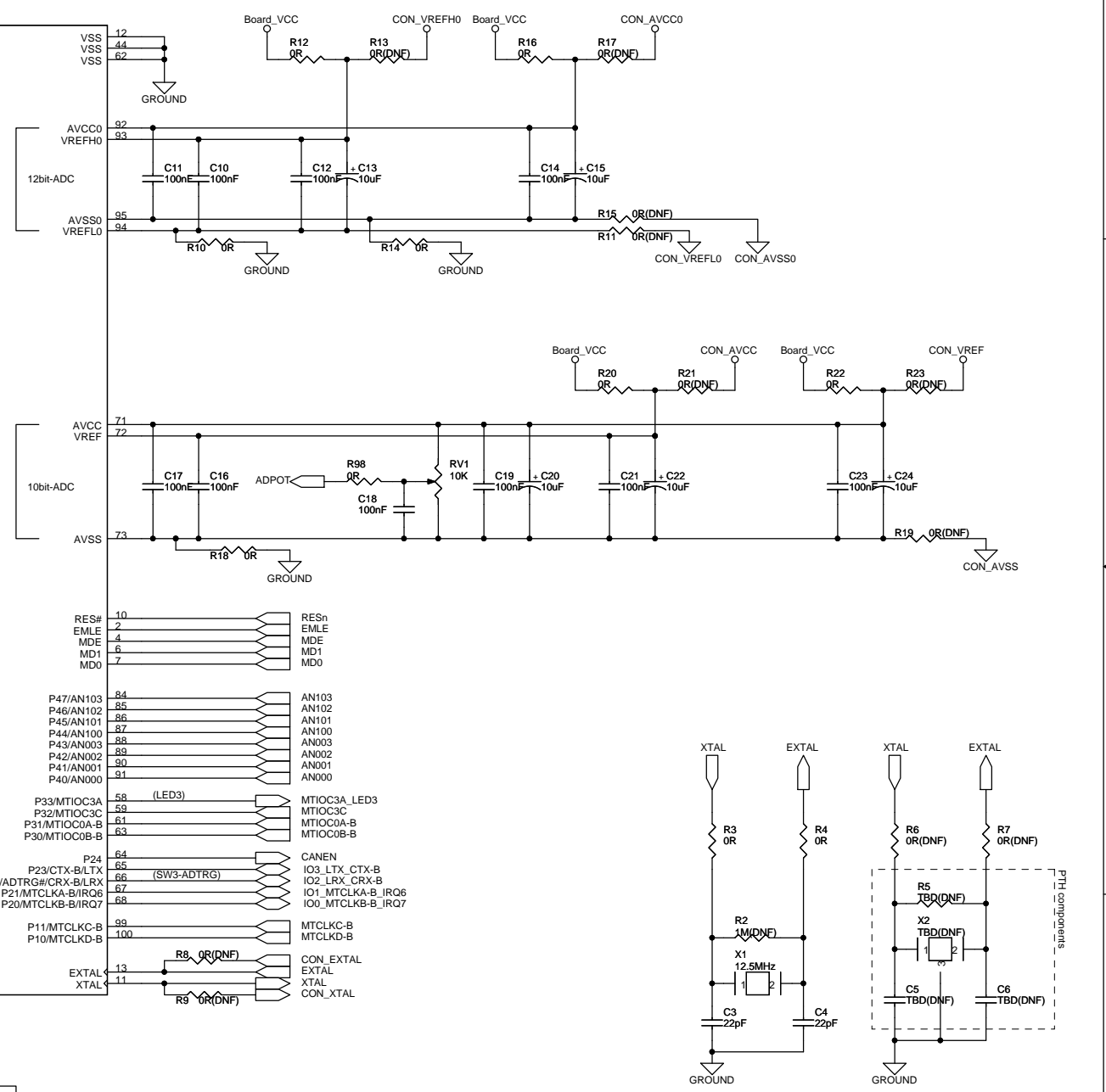
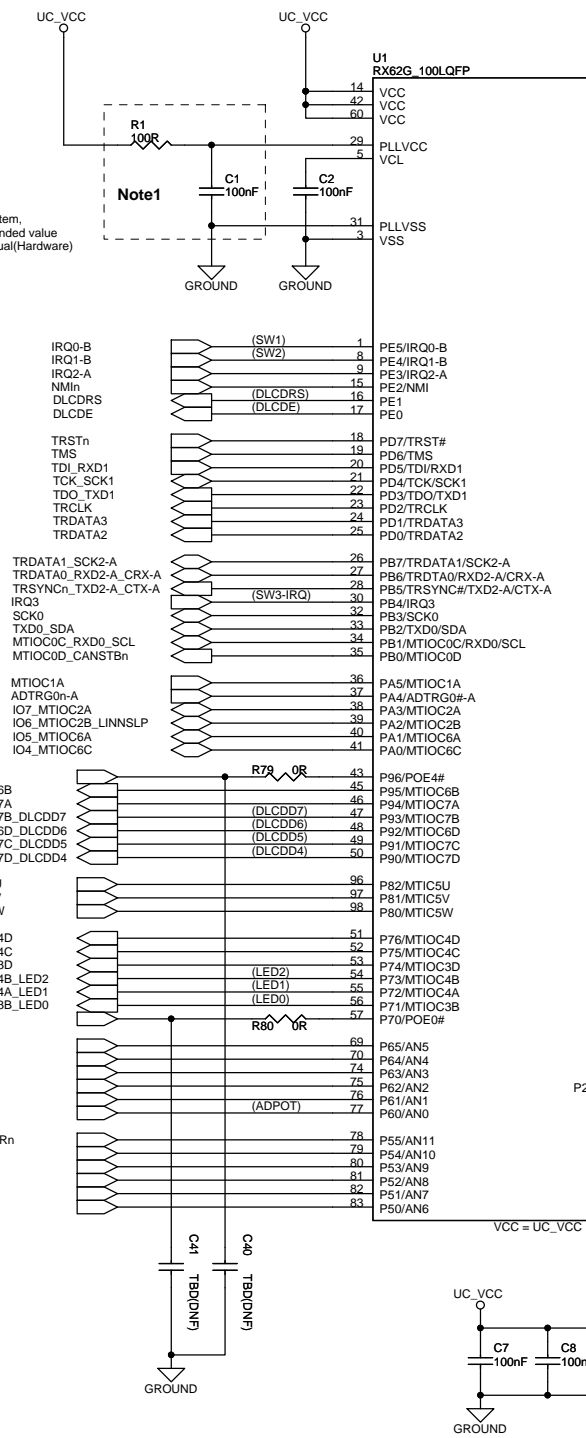
REE Drawing No. D011442_04

Renesas Solutions Corp.			
Title			
RSKR62G [INDEX]			
Size	Document Number		Rev
	R20UT222EG0100		1.00
Date:	Tuesday, October 02, 2012	Sheet	1 of 7

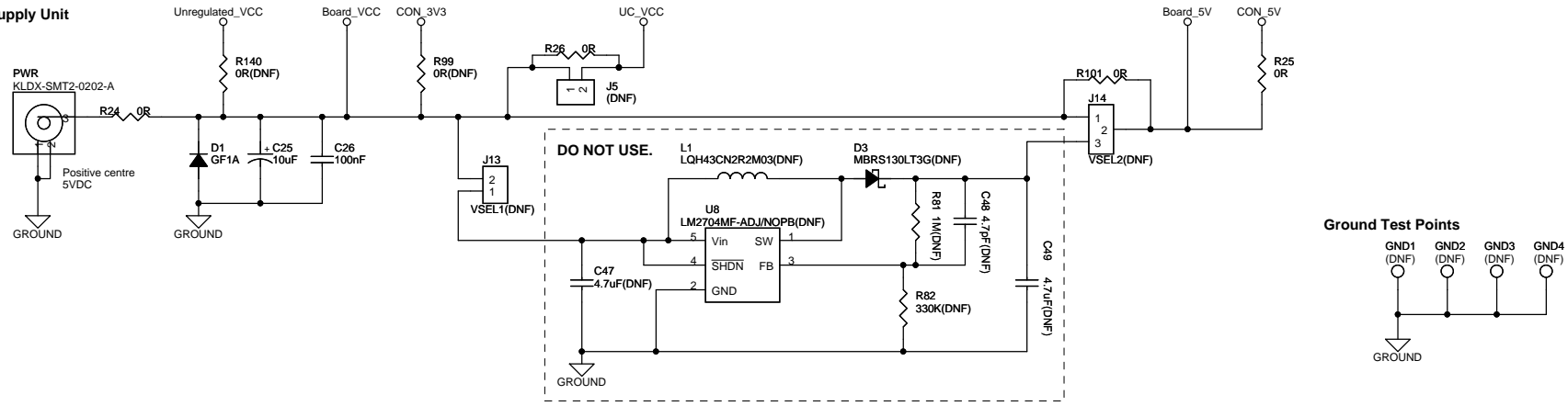
RX62G 100LQFP Microcontroller

Note:
Microcontroller's pins are not described by the full pin function. For full pin details, please refer to latest RX62G User's Manual (Hardware) on Renesas web site.

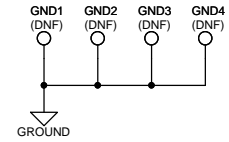
Note1:
If you design your target system, please refer to the recommended value of latest RX62G User's Manual (Hardware) on Renesas web site.



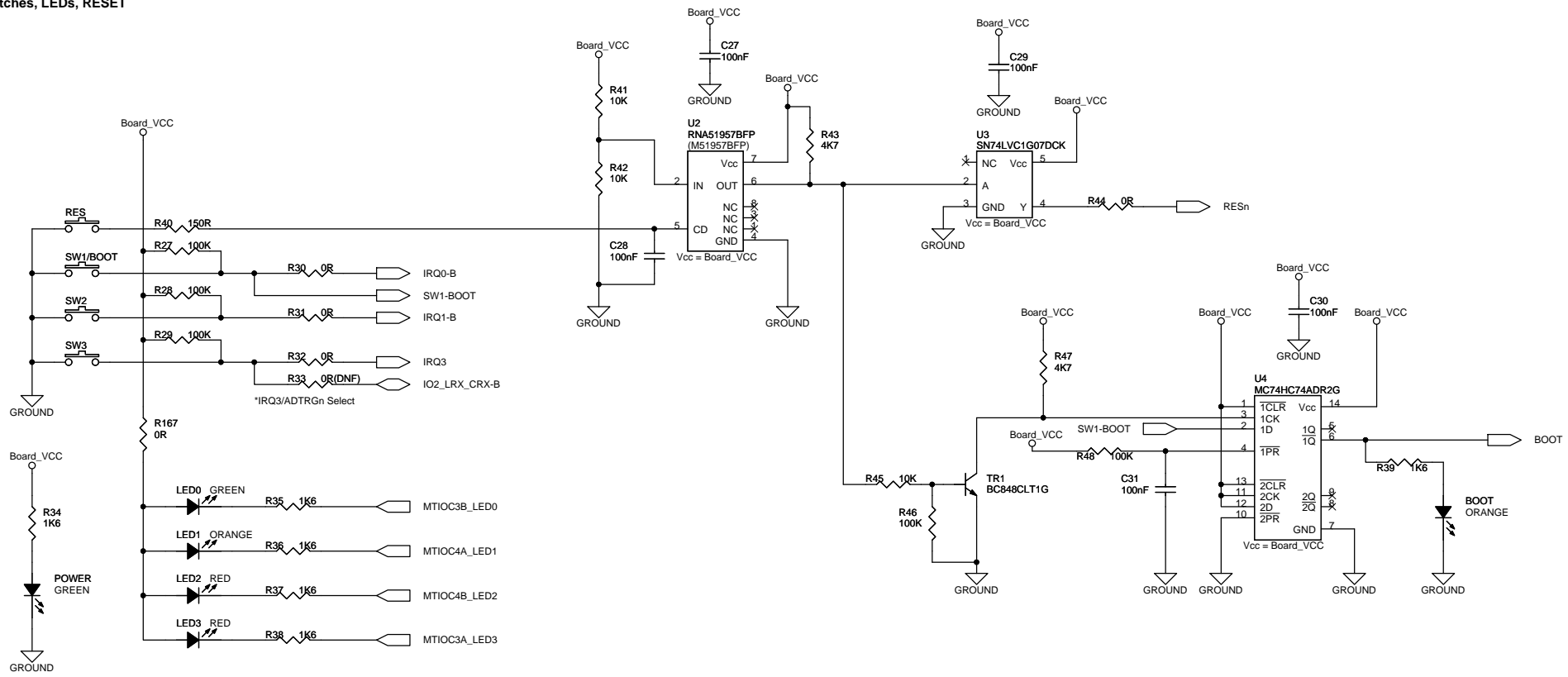
Power Supply Unit



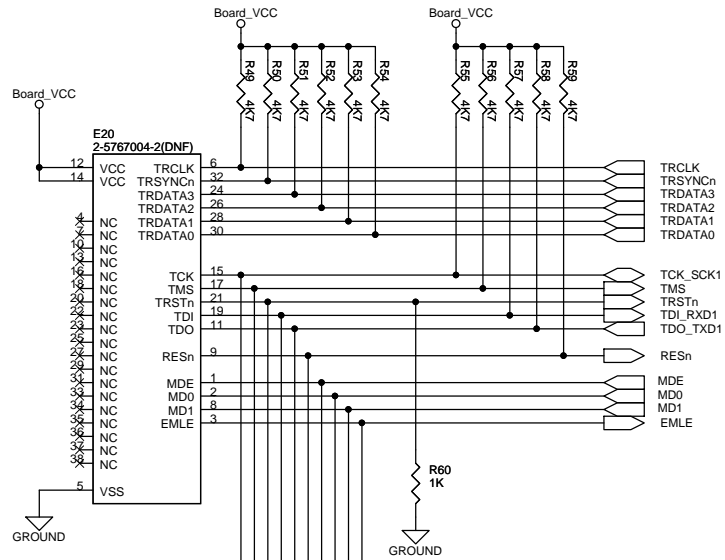
Ground Test Points



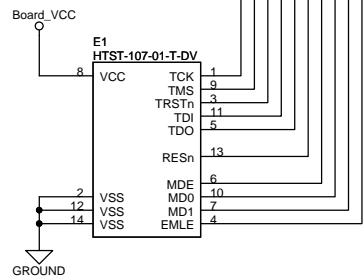
Switches, LEDs, RESET



E20 Emulator Interface



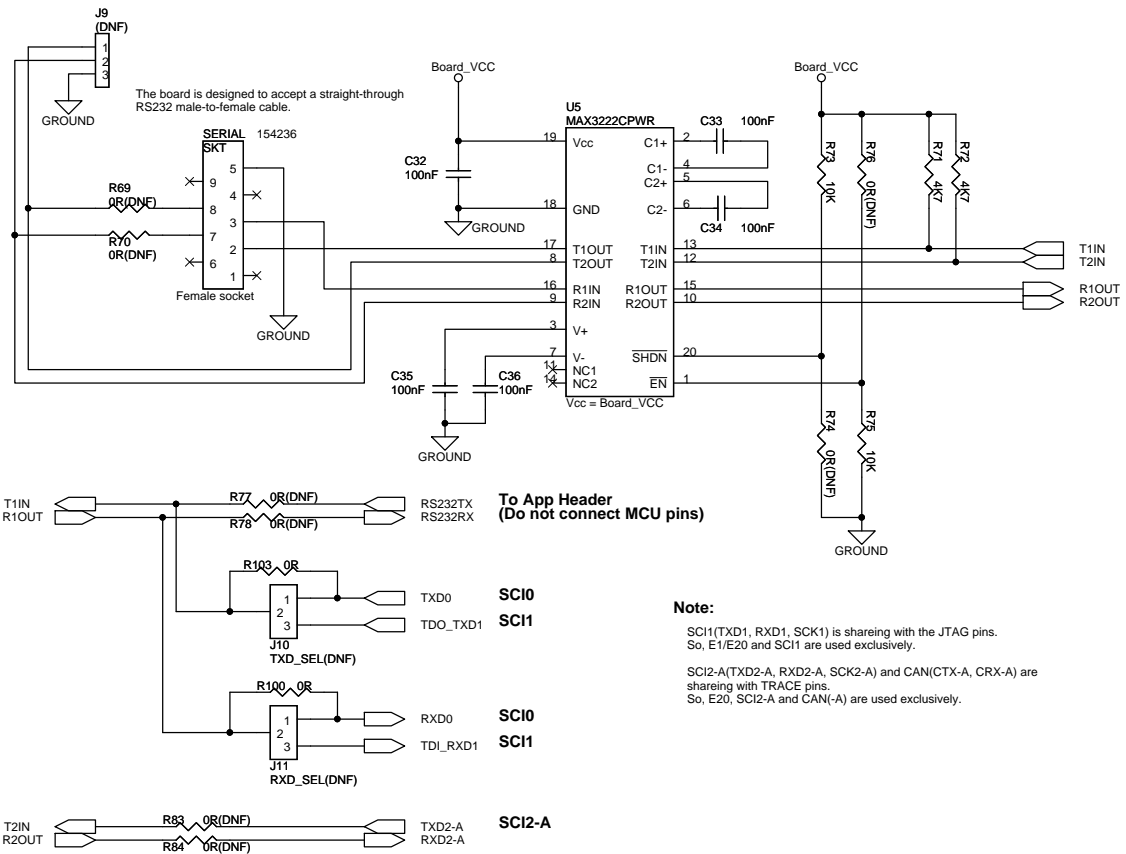
E1 Emulator Interface



Note:

SCI2-A(TXD2-A, RXD2-A, SCK2-A) and CAN(CTX-A, CRX-A) are sharing with TRACE pins. So, E20, SCI2-A and CAN(-A) are used exclusively. Moreover, E20 cannot use by default.

Serial Port

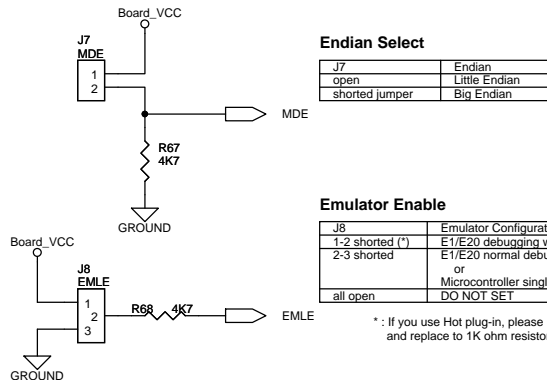
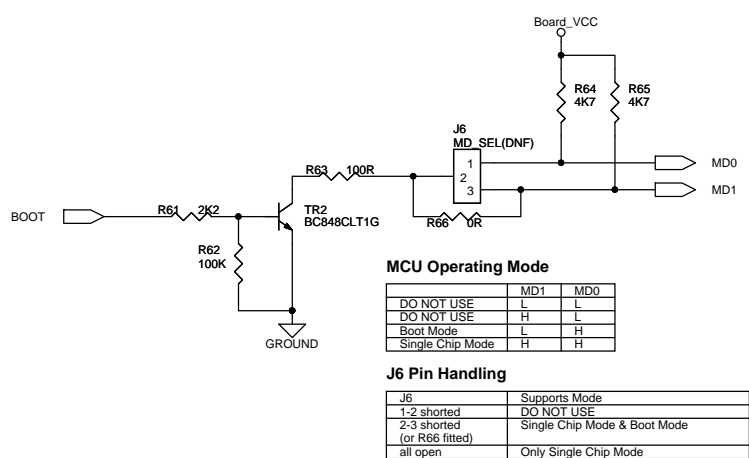


To App Header (Do not connect MCU pins)

Note:

SCI1(TXD1, RXD1, SCK1) is sharing with the JTAG pins. So, E1/E20 and SCI1 are used exclusively. SCI2-A(TXD2-A, RXD2-A, SCK2-A) and CAN(CTX-A, CRX-A) are sharing with TRACE pins. So, E20, SCI2-A and CAN(-A) are used exclusively.

MCU Mode Setting



Endian Select

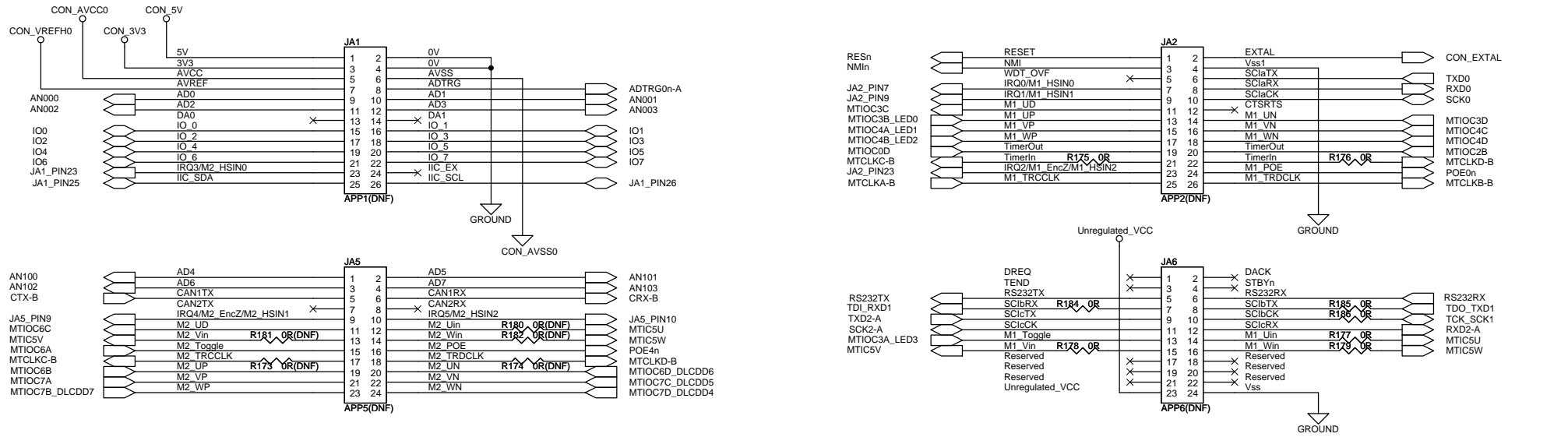
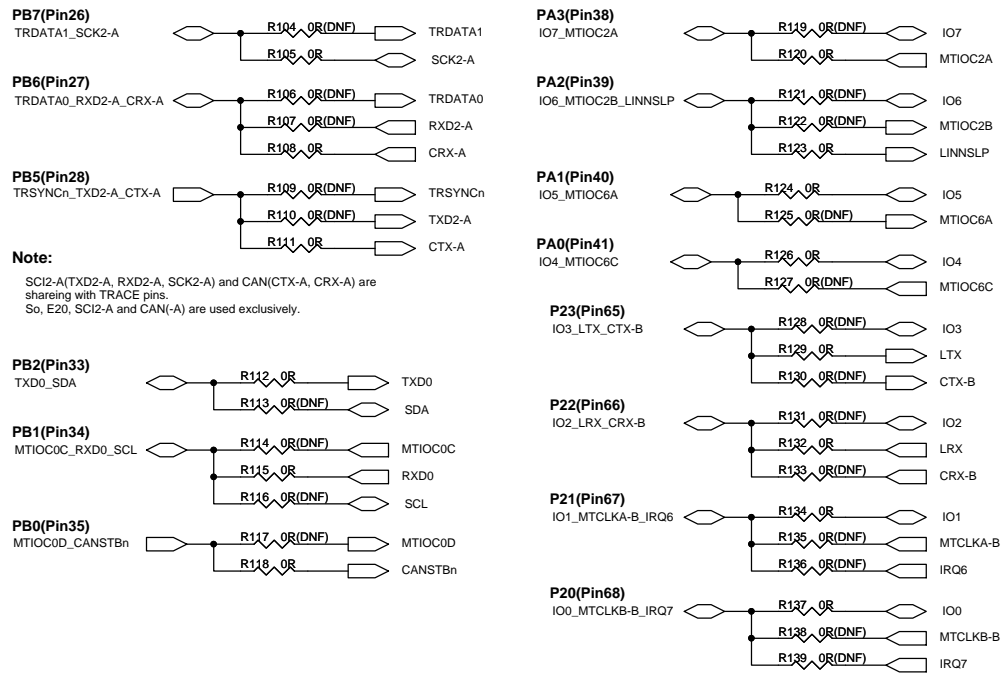
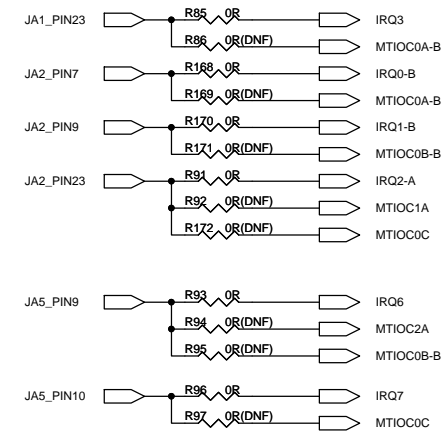
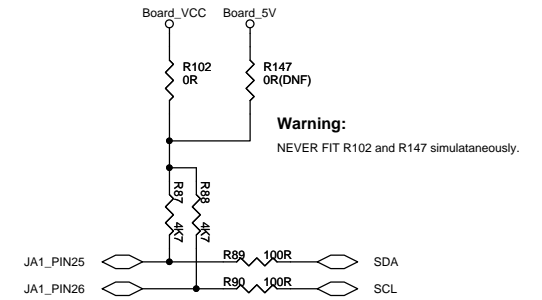
J7	Endian
open	Little Endian
shorted jumper	Big Endian

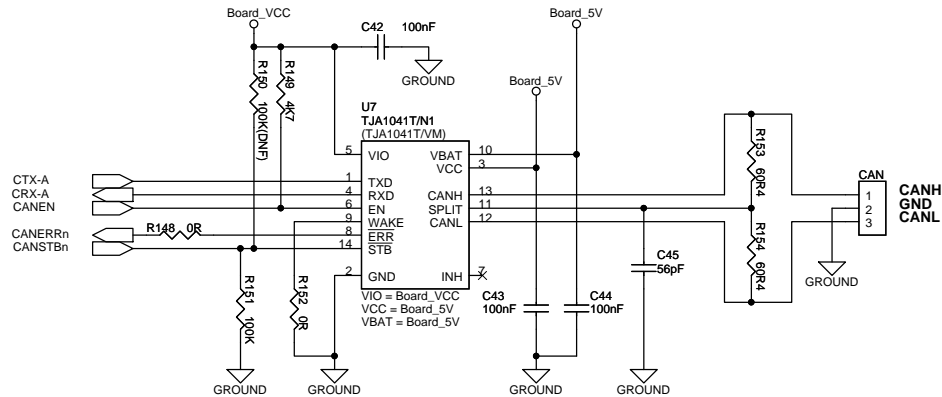
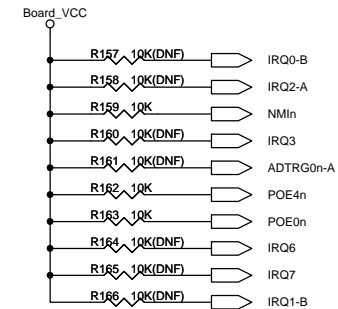
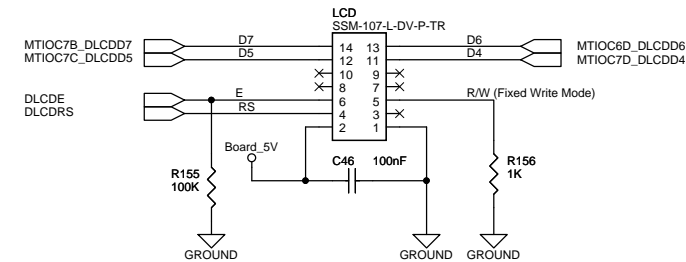
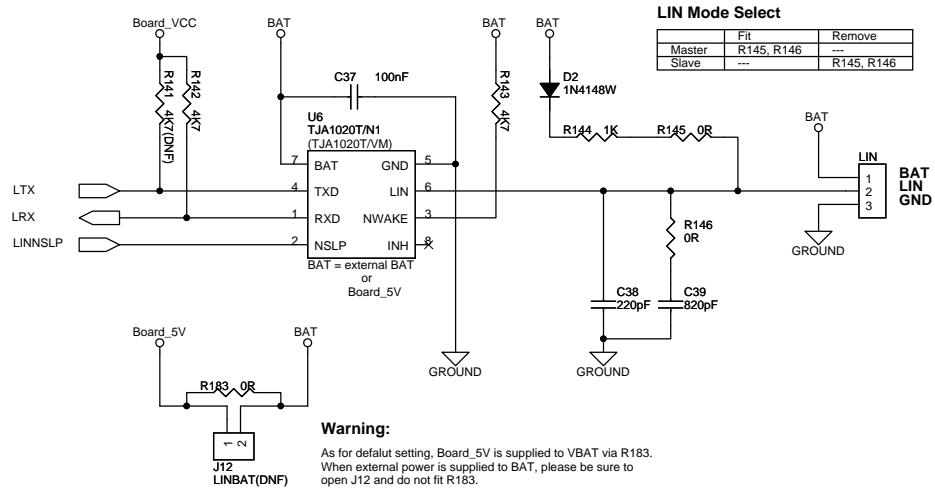
Emulator Enable

J8	Emulator Configuration
1-2 shorted (*)	E1/E20 debugging with Hot plug-in
2-3 shorted	E1/E20 normal debugging
or	Microcontroller single operation
all open	DO NOT SET

*: If you use Hot plug-in, please remove R68 and replace to 1K ohm resistor.

[Initial setting for J7 and J8]
 J7: open
 J8: 2-3 shorted

Application Headers**MCU Pin Function Select****Header Function Select****IIC resistor handling**



Microcontroller Pin Headers

