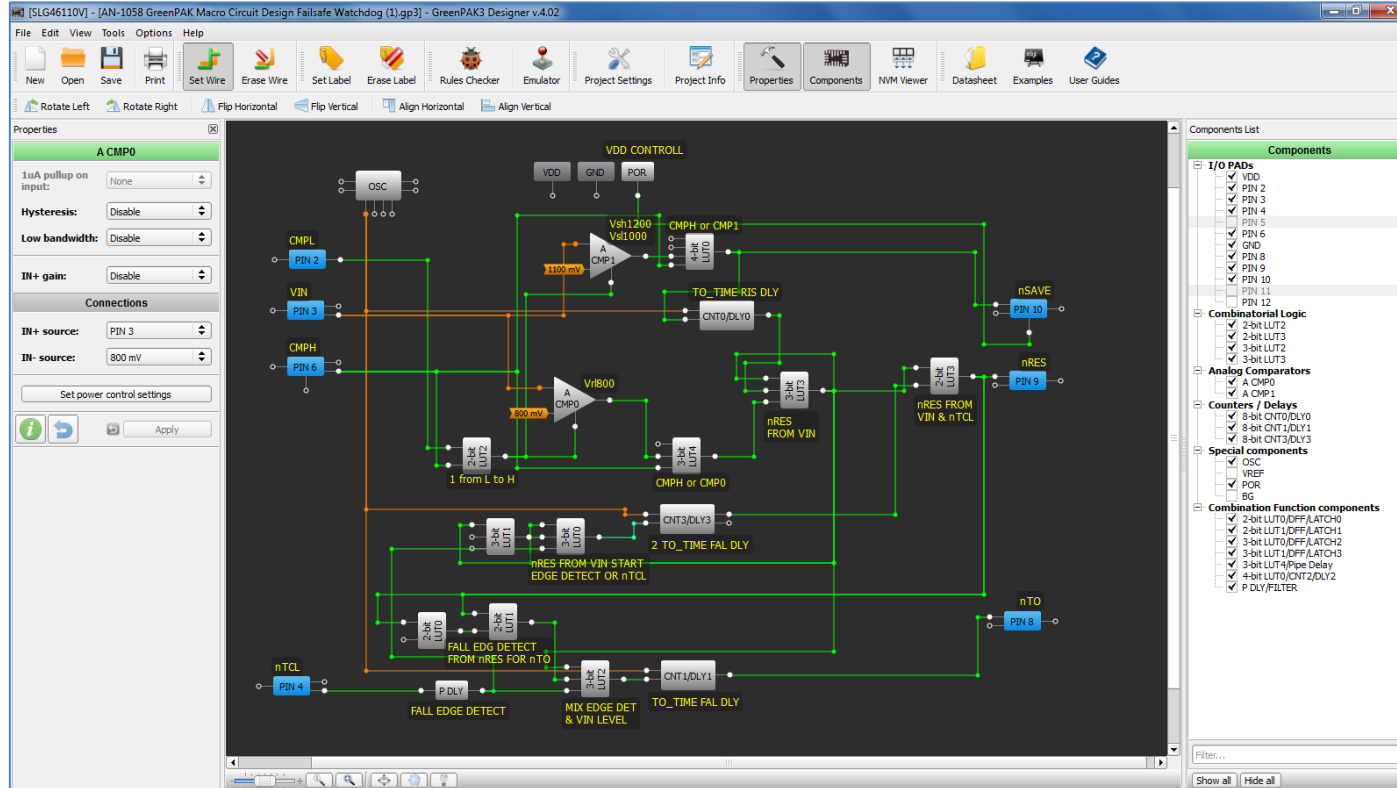


Free Development Software

Silego Technology's GreenPAK Designer development software enables a completely graphical design process, requiring no programming language or compiler allowing a designer to configure, program, and test custom GreenPAK samples in minutes.

- Schematic capture-like design and routing
- Entire component library showing available resources for each device
- Easy component configuration
- Example projects and support documentation



UNIVERSAL DEVELOPMENT KIT

Working in tandem with the GreenPAK Designer Emulator, Silego Technology's Universal Development Kit allows designers to

- Program custom samples in minutes
- Test GreenPAK projects in-circuit
- Develop using any GreenPAK 3 or 4 device



**GreenPAK Universal
Development Kit**

For more information please visit www.silego.com

Or email your questions to info@silego.com

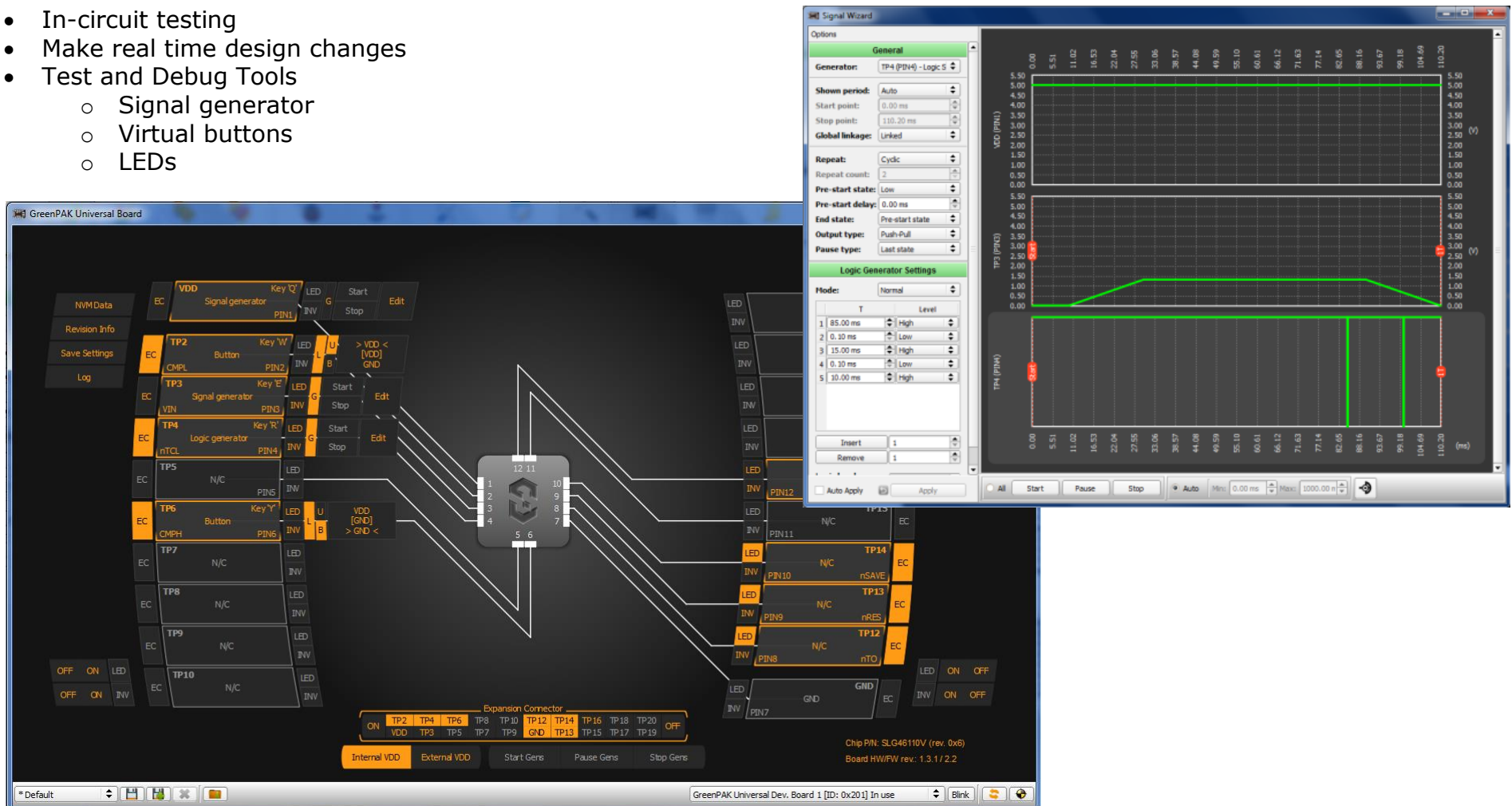
Q2 2015

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1515 Wyatt Drive
Santa Clara, CA 95054
Phone: 408.327-8800

Design Emulator

Using the included GreenPAK design emulator, designers can test their project in the development environment, no soldering required.

- In-circuit testing
- Make real time design changes
- Test and Debug Tools
 - Signal generator
 - Virtual buttons
 - LEDs



The screenshot displays the GreenPAK Designer Development Environment. The main window shows a circuit board layout with various components like buttons (Key Q, Key W, Key E, Key Y), signal generators (TP2, TP3, TP4), and LEDs. A central chip is connected to these components. An 'Expansion Connector' is visible at the bottom with pins TP2 through TP20 and VDD, GND, and LED indicators.

Overlaid on the right is the 'Signal Wizard' window, which is used for configuring and viewing signal waveforms. It includes a 'General' tab with settings for the generator (TP4 (PIN4) - Logic 5), shown period (Auto), start and stop points, and repeat settings. The 'Logic Generator Settings' tab shows a table of logic states:

T	Level
1 85.00 ms	High
2 0.30 ms	Low
3 15.00 ms	High
4 0.30 ms	Low
5 10.00 ms	High

The window also displays three timing graphs: VDD (PIN4), TP3 (PIN3), and TP4 (PIN4), showing voltage levels over time. The VDD graph shows a steady high level. The TP3 graph shows a pulse. The TP4 graph shows a square wave. The x-axis for all graphs is time in milliseconds (ms), ranging from 0.00 to 110.20.