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Migrating from the TI PCI2050B to the Tsi350™ PCI-to-PCI Bridge

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1. Migrating from the TI PCI2050B to the Tsi350 PCI-to-PCI Bridge

This document is structured to provide information that helps system designers migrate from the Texas Instruments (TI) PCI2050B to the Tsi350.

This document discusses the following:

- "Feature Compatibility" on page 3
- "Software Compatibility" on page 3
- "Package Compatibility" on page 4

1.1 Overview

The Tsi350 can be used as a drop in replacement for the TI PCI2050B without any modifications to an existing board design.

1.2 Feature Compatibility

The Tsi350 is feature and pinout compatible (208 pin PQFP package) with the TI PCI2050B.

1.3 Software Compatibility

The Tsi350 and TI PCI2050B are fully software compatible within the standard PCI configuration space, with the exception of the values returned by the vendor ID, device ID, and revision ID registers.

Refer to the *Tsi350 User Manual* for more information on using device specific features and registers which are outside the standard PCI configuration space.

1.3.1 Vendor ID Register, Offset 01h – 00h

The TI PCI2050B returns 0x104C when this register is read.

Table 1: Tsi350 Vendor ID

Bits	Type	Value on Reset	Description
15:0	R	0x1011	This 16-bit read only field contains the Vendor ID.

1.3.2 Device ID Register, Offset 03h – 02h

The TI PCI2050B returns 16'hAC28 when this register is read.

Table 2: Tsi350 Device ID

Bits	Type	Value on Reset	Description
15:0	R	0x0023	This 16-bit read only field contains the Device ID.

1.3.3 Revision ID Register, Offset 08h

The TI PCI2050 returns 0x00 when this register is read. The TI PCI2050B returns 0x02 when this register is read.

Table 3: Tsi350 Revision ID

Bits	Type	Value on Reset	Description
7:4	R	0x60	This 8-bit, read only field specifies the Revision ID for the Tsi350 bridge device.

1.4 Package Compatibility

The Tsi350 208 PQFP package is fully pin compatible with the TI PCI2050B.