
PRODUCT CHANGE NOTICE

**Product Improvement –Design
Change for the Listed Intersil
Products**

**Refer to:
PCN14062**

Date: December 19, 2014

December 19, 2014

To: Our Valued Intersil Customer

Subject: Product Improvement – Design Change for the Listed Intersil Products

This notice is to inform you that Intersil is planning a design change to address a low PPM start-up issue associated with the products listed below. The change is a single level, single cut to re-route a signal from TSEN to GND (see Appendix I). The change compliments additional test screen and is necessary to eliminate the failure mode for applications that use a slow Vin ramp (when EN = Vin) during start up.

Products affected:

ISL78213ARZ	ISL78214ARZ
ISL78213ARZ-T	ISL78214ARZ-T
ISL78213ARZ-TR5303	ISL78214ARZ-TR5303
ISL78213ARZ-TS2568	ISL78214ARZ-TS2568

The qualification plan is designed using AEC Q100, JEDEC and other applicable industry standards to confirm form, fit, function, and interchangeability of product. A summary of the qualification plan is included for reference. The remainder of the manufacturing operations (wafer fabrication, assembly, package level electrical testing, etc.) will continue to be processed to previously established conditions and systems.

There will be no change in the external marking of the packaged parts or to the product data sheet electrical specification. Product affected by this change is identifiable via Intersil's internal traceability system.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to receive product manufactured with the changed materials beginning *ninety* days from completion of product validation or earlier with approval.

If you have concerns with this change notice, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards,



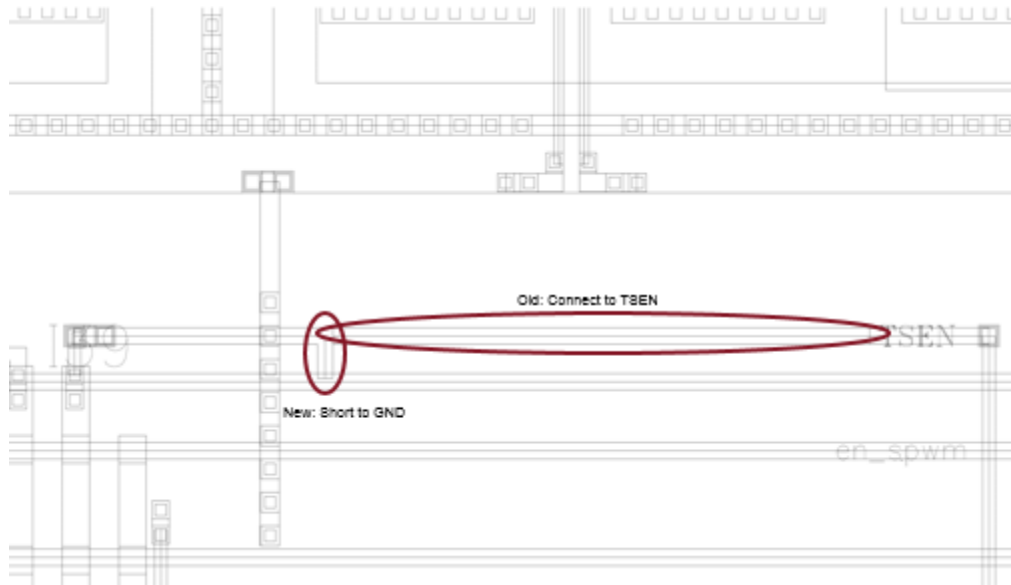
Jeffrey R. Touvell
Intersil Corporation

PCN14062

cc: D. Wackley N. Lyne L. Altschaeffl D. Zheng S. Schulte D. DeCrosta

PCN14062 – Appendix I

Schematic change, one cut / metal routing where a 3 input NOR Gate was changed from TSEN to GND, this re-routing requires one simple one layer metal edit. Design validation and simulation runs complete.



PCN14062 – Reliability Qualification Plan

Qualification Plan					
Stress	Test Method	Sample Size	# of Lots	Result	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	77	1		168 hour @ 125C
ESD - Human Body Model (HBM)	AEC Q100-002	3	1		
ESD - Charged Device Model (CDM)	AEC Q100-011	3	1		
Latch-up (LU)	AEC Q100-004	6	1		
Electrical Distribution (ED)	AEC Q100-009	30	1		