
PRODUCT CHANGE NOTICE

**Product Improvement –Design
Change for the Listed Intersil
ISL94203* Products**

**Refer to:
PCN15011**

Date: February 13, 2015

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To: Our Valued Intersil Customer

Subject: Product Improvement – Design Change for the Listed Intersil ISL94203* Products

This notice is to inform you that Intersil has revised the design of the ISL94203IRTZ product family. The modification is to address a timing issue where the CFET switch can stay on under certain conditions as a result of logic being in an undetermined state. The change is a three level metal edit to Metal 1, Metal 2 and Via3. The change will fix the latch state of VL and prevent the latch from becoming undetermined.

Products affected: ISL94203IRTZ ISL94203IRTZ-T ISL94203IRTZ-T7

The qualification plan is designed using JEDEC and other applicable industry standards to confirm form, fit, function, and interchangeability of product. A summary of the evaluation results are included for reference. The remainder of the manufacturing operations (wafer fabrication, assembly, package level electrical testing, etc.) will continue to be processed to previously established conditions and systems.

There will be no change in the external marking of the packaged parts or to the product data sheet electrical specification. Product affected by this change is identifiable via Intersil's internal traceability system.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to receive product manufactured with the changed materials beginning *thirty* days from the date of this notice earlier with approval.

If you have concerns with this change notice, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards,



Jeffrey R. Touvell
Intersil Corporation

PCN15011

cc: P. Lee W. Choroco

PCN15011 – Evaluation Results

Stress	Sample Size	# of Lots	Result	Comments
High Temperature Operating Life (JESD22-A108)	80	1	Pass	168hr @ 125°C
Temperature Characterization	150	1	Pass	Evaluated at 25, -40, 60 and 85°C – datasheet parameters meet minimum Cpk of 1.67
Latch-up (JESD 78)	3	1	Pass	85°C +/- 100ma
Human Body Model (JESD22-A114)	3	1	Pass	2000 v
Charged Device Model (JESD22-C101)	3	1	Pass	1000 v
Machine Model (JESD22-A115)	3	1	Pass	100 v