

March 22, 2012

Main Specifications of the RX62G MCU

	RX62G Group
CPU Core	<ul style="list-style-type: none"> • RX CPU • General registers: 32-bit × 16 • Multiplier: 32-bit multiplier • Divider: Yes • Multiply-accumulator: Yes (two types: memory-to-memory operations and register-to-register operations)
Maximum operating frequency	100MHz
Power supply voltage	4.0 to 5.5V
Floating-point processing unit	Single-precision floating-point processing unit (Supports add / subtract / compare / multiply / divide and other instructions)
Flash ROM (for program storage)	Max. 256 KB
Flash ROM (for data storage)	Max. 32 KB
RAM	Max. 16 KB
On-chip peripheral functions	<ul style="list-style-type: none"> • Data transfer controller (DTC) • Multifunction timer pulse unit (MTU3) × 8 channels • Compare match timer (CMT) × 4 channels • Port output enable (POE3) • General PWM timer (GPT) × 4 channels • Watchdog Timer (WDT) • Independent Watchdog Timer (IWDT) • Serial communication interface (SCIb) × 3 channels • CAN Interface × 1 or 0 channel • LIN interface × 1 channel • I2C bus interface × 1 channel

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	<ul style="list-style-type: none"> • Serial Peripheral Interface (RSPI) × 1 channel • A/D converter(12-bit) × 8 channels • A/D converter(10-bit) × 12 channels • CRC calculator • Power-On Reset (POR) / Voltage Detection Circuit (LVD)
On-chip debugging function	Yes (with trace function)
Low power consumption modes	4 modes <ul style="list-style-type: none"> • Sleep mode • All-module clock stop mode • Software standby mode • Deep software standby mode
Packages	LQFP112, LQFP100