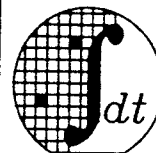


LIMITS SYMBOLS	INCHES	
	MIN	MAX
A	.140	.165
A1	.015	.035
b	.015	.020
b1	.050	.070
C	.008	.012
D	.885	.910
E	.300	.325
E1	.247	.260
e	.090	.110
eA	.310	.370
L	.120	.150
α	0°	15°
N	18	
Q1	.050	.070
S	.040	.060

JEDEC	EXCEPTIONS
NOT REGISTERED	

TOLERANCES UNLESS OTHERWISE SPECIFIED
FRAC DEC ANGLES
± - ± - ± -



Integrated Device Technology, Inc.

3236 Scott Blvd., Santa Clara, CA 95051

(408) 727-6116 TWX: 910-338-2070

APPROVALS DATE
DRAWN *Ad* 7/88
CHECKED
MF 8/88

18 LD P-DIP MKT DWG
(300 MIL)

SCALE	SIZE	DRAWING NO.	REV
N/A	A	PSC-4015	02

DO NOT SCALE DRAWING

SHEET 2 OF 2