

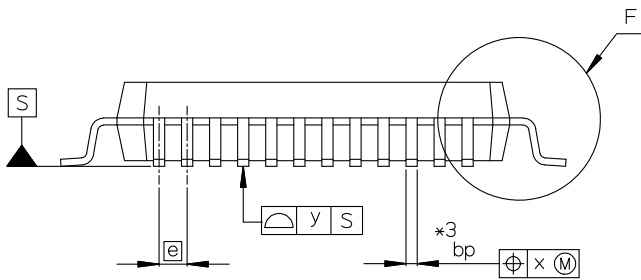
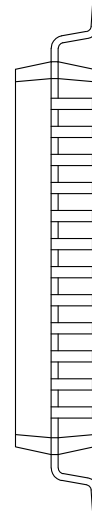
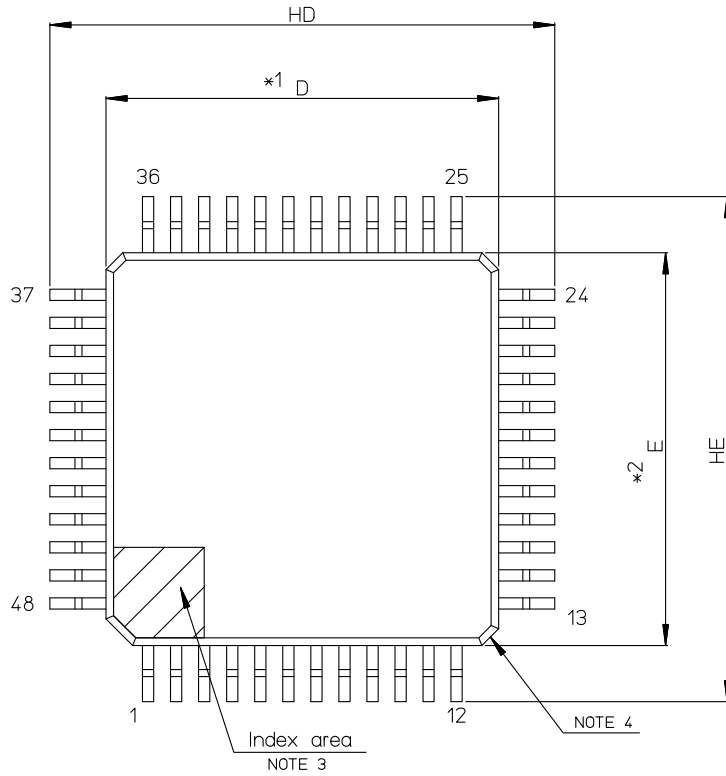
# 外形図 Outline Drawing (PLQP0048KB-B)

RDK-G-001025

1/1

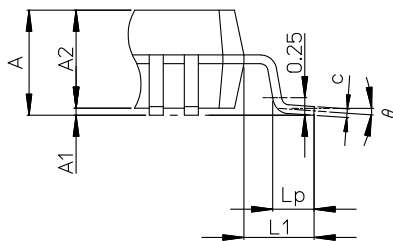
ルネサス エレクトロニクス株式会社  
Renesas Electronics Corporation

|                    |              |               |            |
|--------------------|--------------|---------------|------------|
| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
| P-LFQFP48-7x7-0.50 | PLQP0048KB-B | —             | 0.2g       |



NOTE)

1. DIMENSIONS \*1\* AND \*2\* DO NOT INCLUDE MOLD FLASH.
2. DIMENSION \*3\* DOES NOT INCLUDE TRIM OFFSET.
3. PIN 1 VISUAL INDEX FEATURE MAY VARY, BUT MUST BE LOCATED WITHIN THE HATCHED AREA.
4. CHAMFERS AT CORNERS ARE OPTIONAL; SIZE MAY VARY.



Detail F

| Reference Symbol | Dimension in Millimeters |      |      |
|------------------|--------------------------|------|------|
|                  | Min                      | Nom  | Max  |
| D                | 6.9                      | 7.0  | 7.1  |
| E                | 6.9                      | 7.0  | 7.1  |
| A2               | —                        | 1.4  | —    |
| HD               | 8.8                      | 9.0  | 9.2  |
| HE               | 8.8                      | 9.0  | 9.2  |
| A                | —                        | —    | 1.7  |
| A1               | 0.05                     | —    | 0.15 |
| bp               | 0.17                     | 0.20 | 0.27 |
| c                | 0.09                     | —    | 0.20 |
| $\theta$         | 0°                       | 3.5° | 8°   |
| e                | —                        | 0.5  | —    |
| x                | —                        | —    | 0.08 |
| y                | —                        | —    | 0.08 |
| Lp               | 0.45                     | 0.6  | 0.75 |
| L1               | —                        | 1.0  | —    |