


REVISIONS				
DCN	REV	DESCRIPTION	DATE	CHANGE BY
	1.0	INITIAL RELEASE	2009-12-05	T. TRAN

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		TITLE EB-LOGAN-23		
		SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000
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Mon Apr 19 14:29:53 2010			SHEET 1 OF 50	

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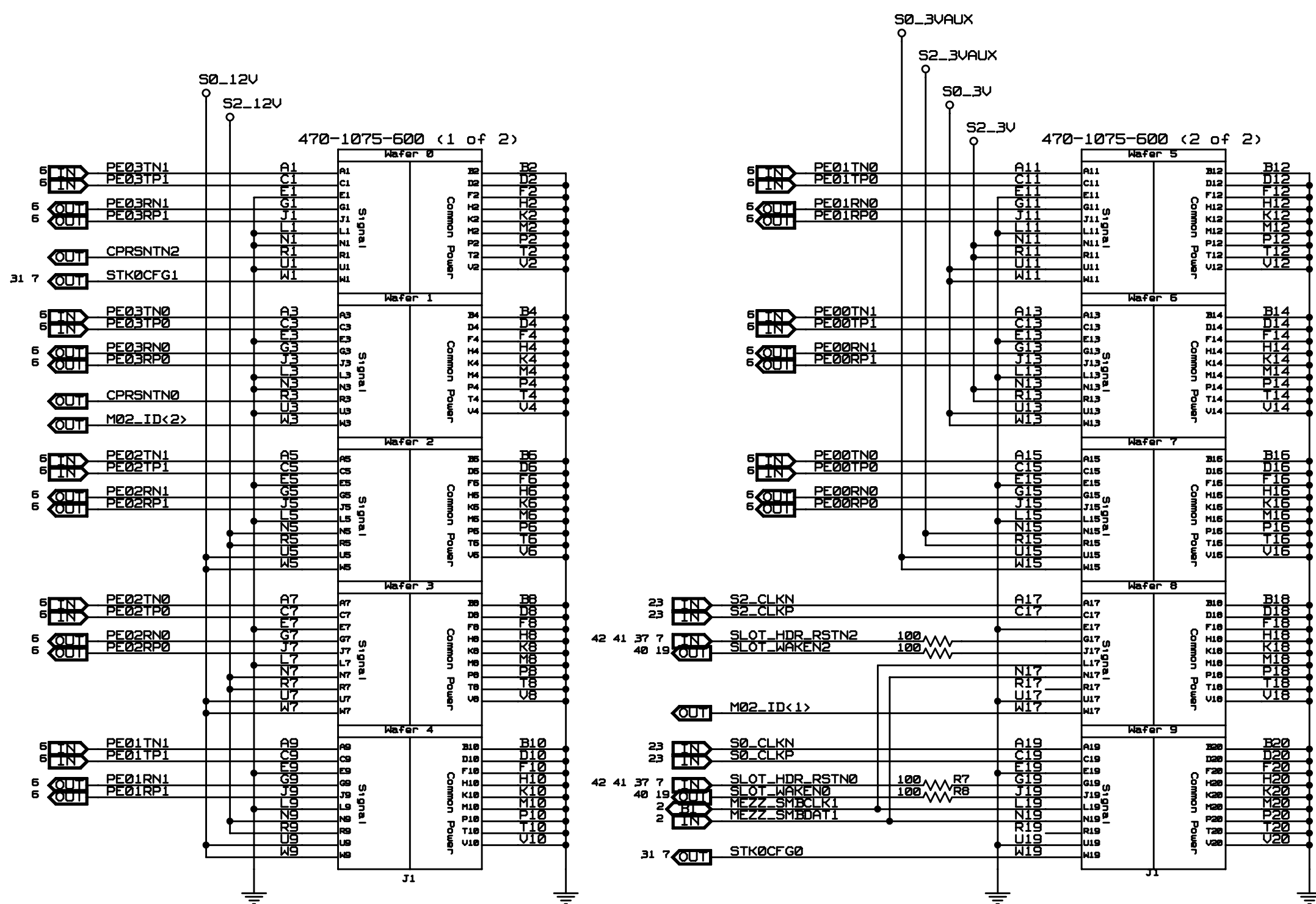
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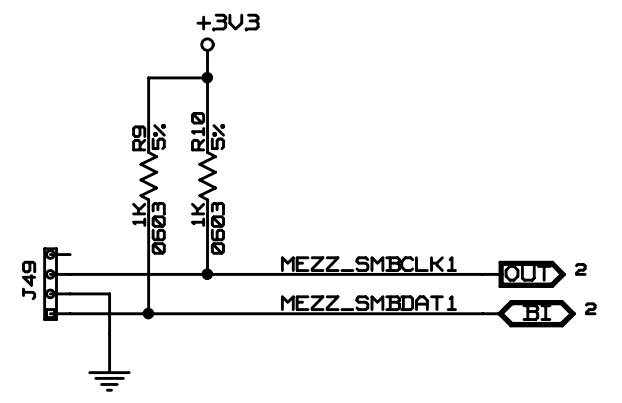
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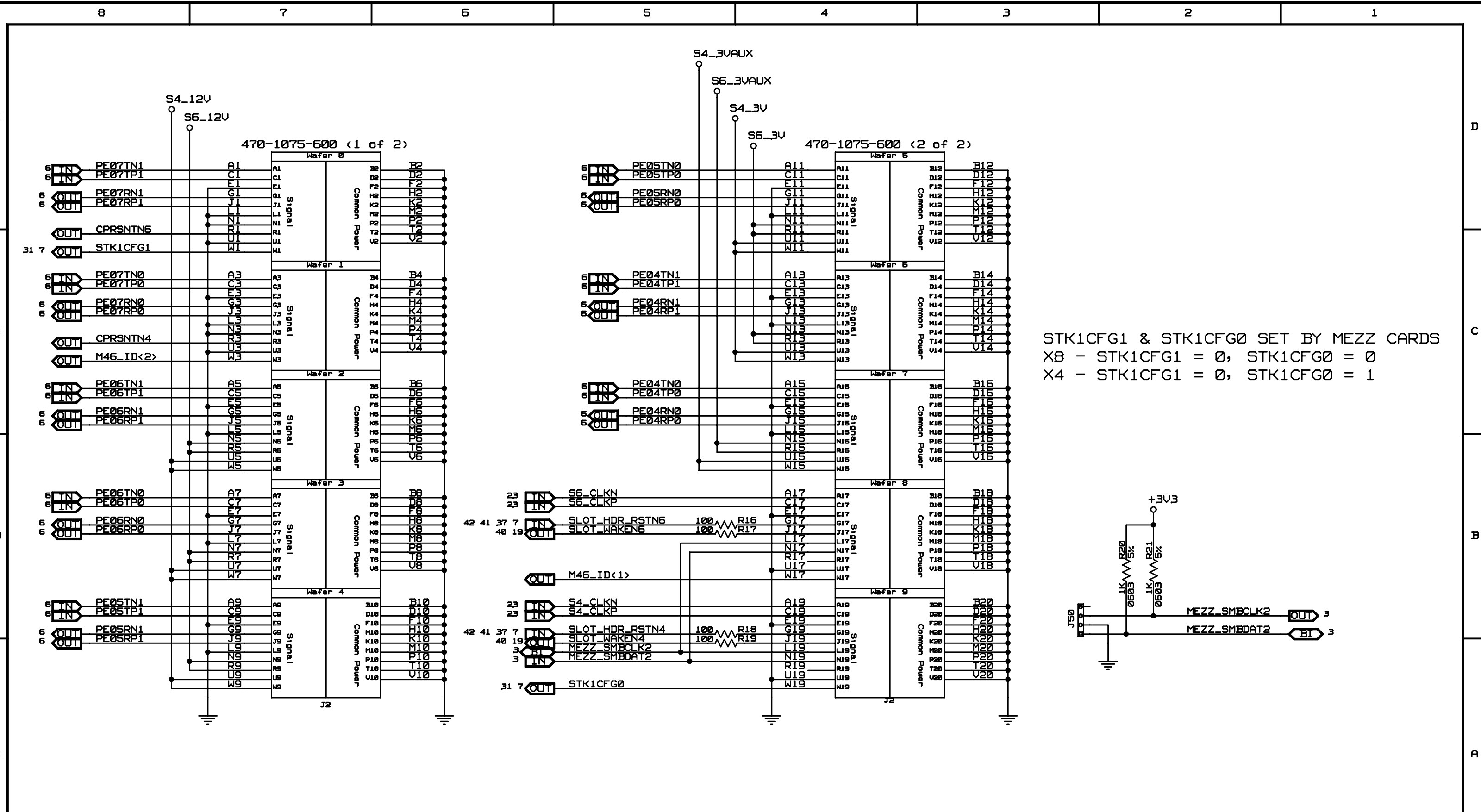


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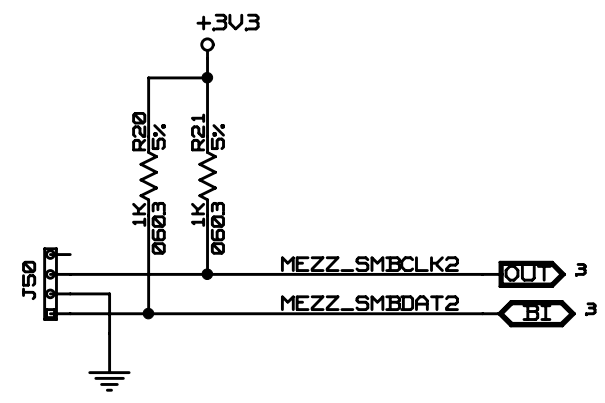


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MEZZANINE CONNECTOR PORTS 0/2			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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Mon Apr 19 14:30:52 2010			SHEET 2 OF 50



STK1CFG1 & STK1CFG0 SET BY MEZZ CARDS  
 X8 - STK1CFG1 = 0, STK1CFG0 = 0  
 X4 - STK1CFG1 = 0, STK1CFG0 = 1



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MEZZANINE CONNECTOR PORTS 4/6			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:54 2010			SHEET 3 OF 50

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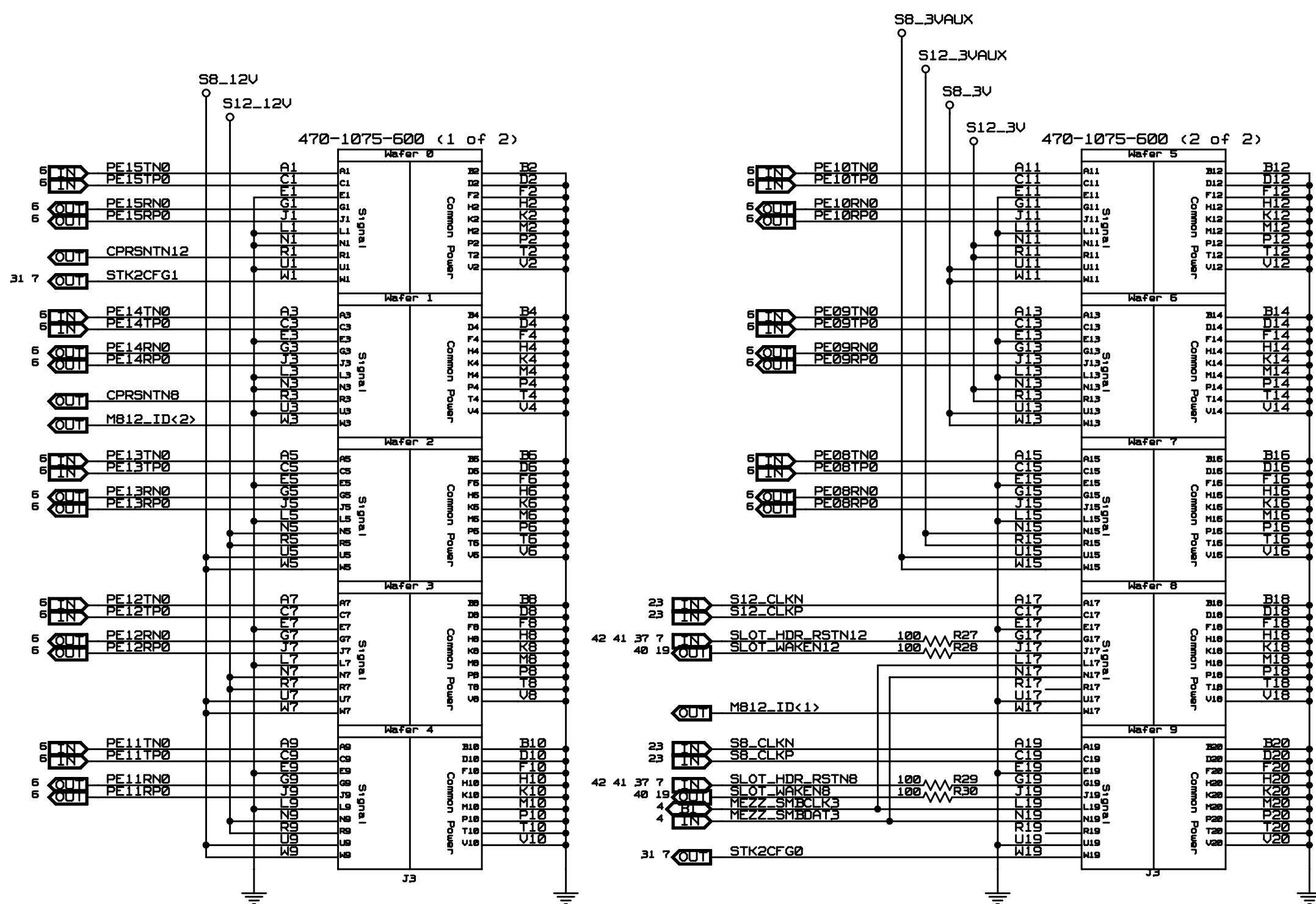
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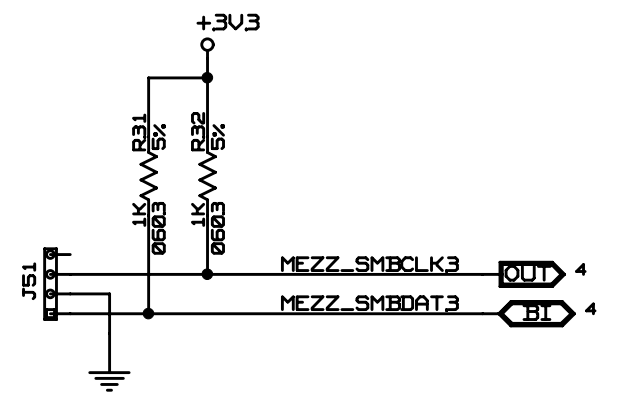
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MEZZANINE CONNECTOR PORTS 8/12			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
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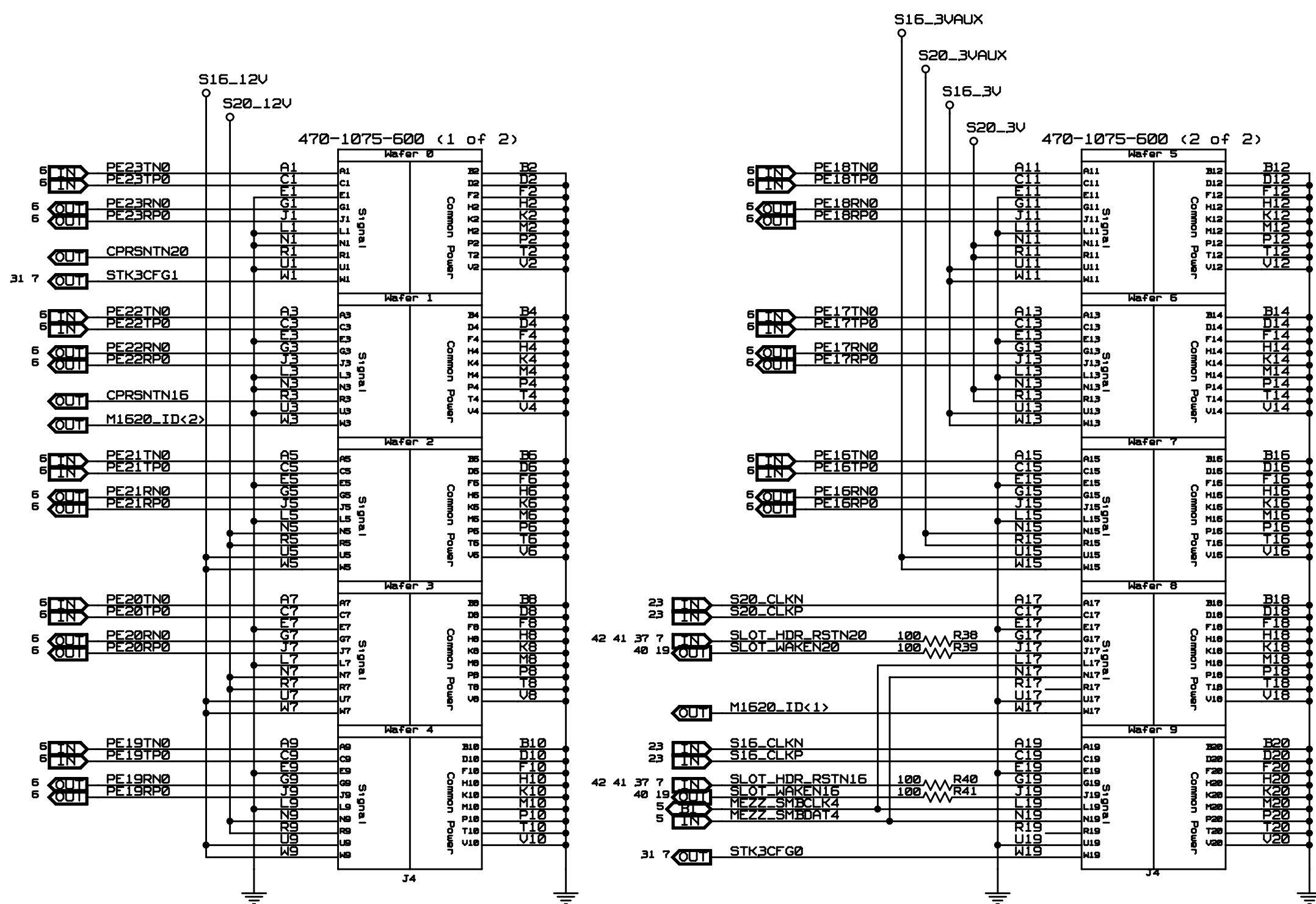
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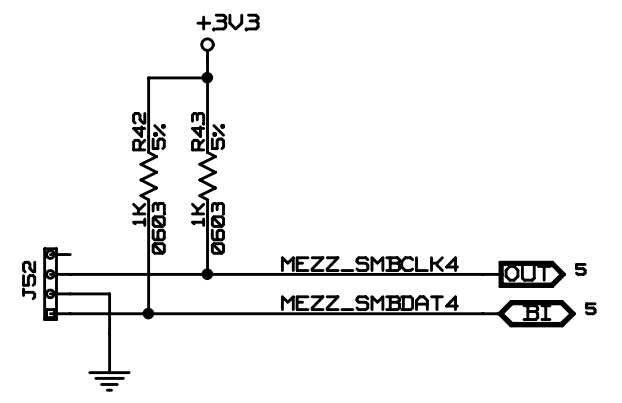
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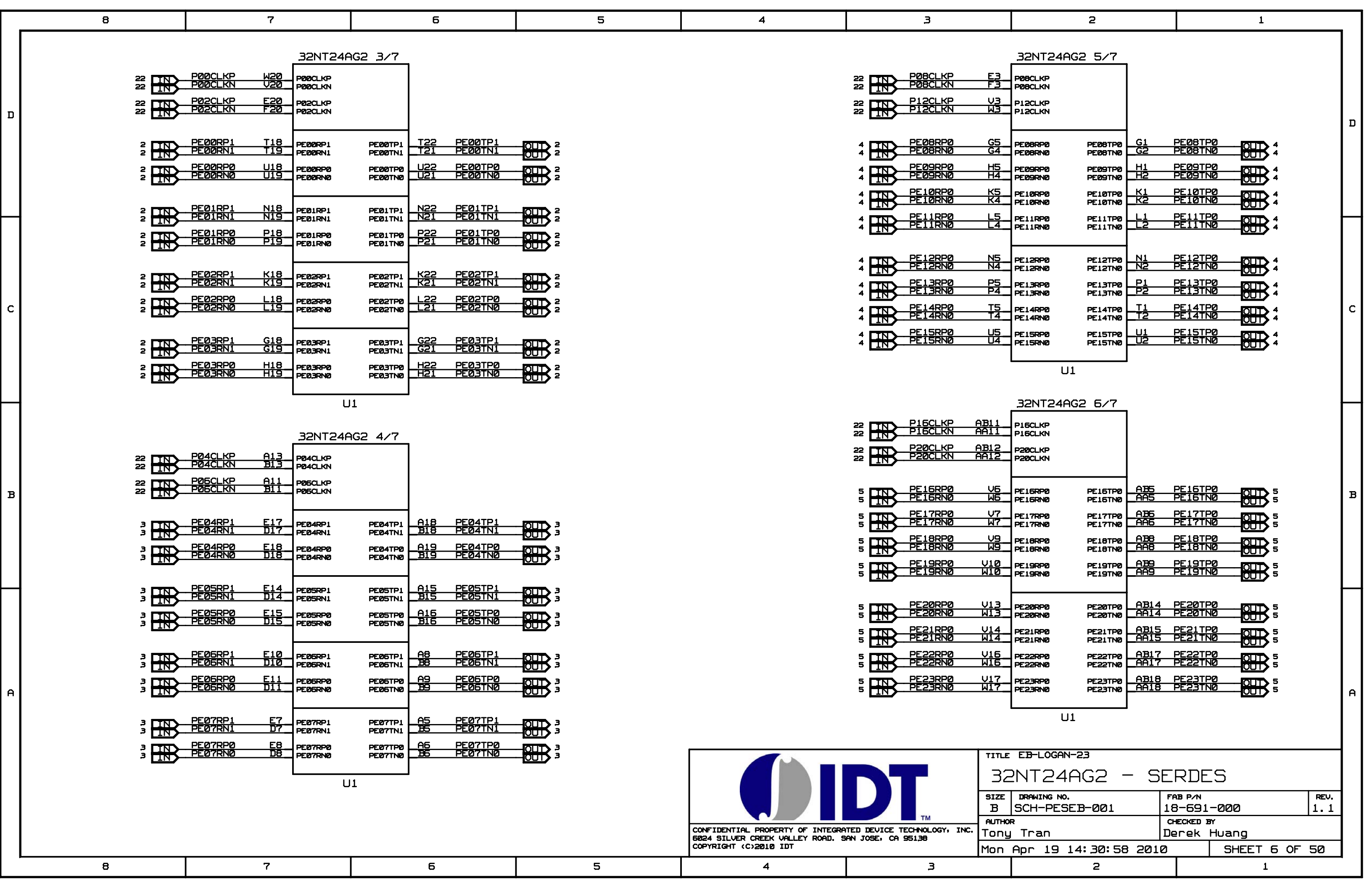
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MEZZANINE CONNECTOR PORTS 16/20			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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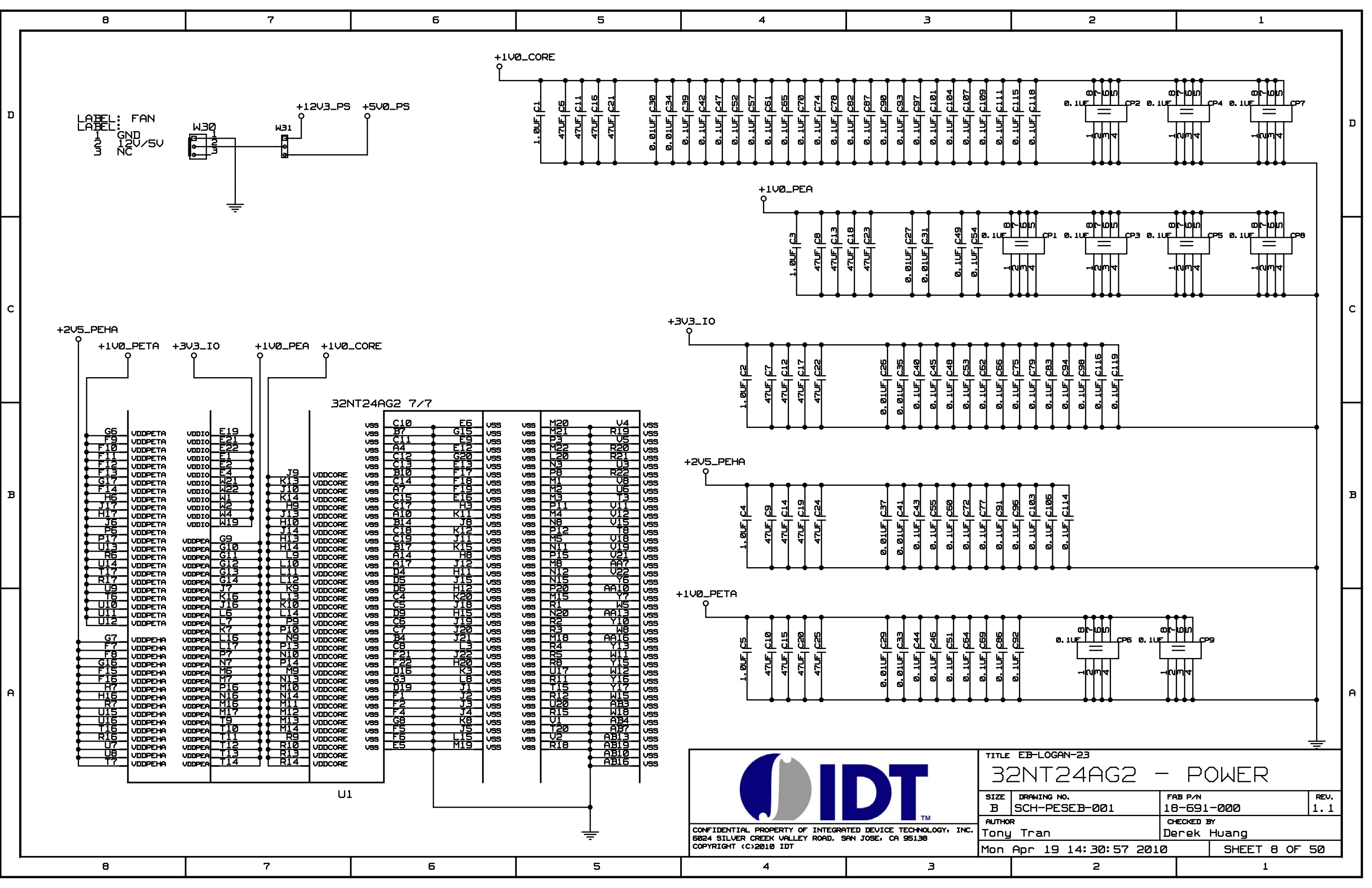




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32NT24AG2 - SERDES			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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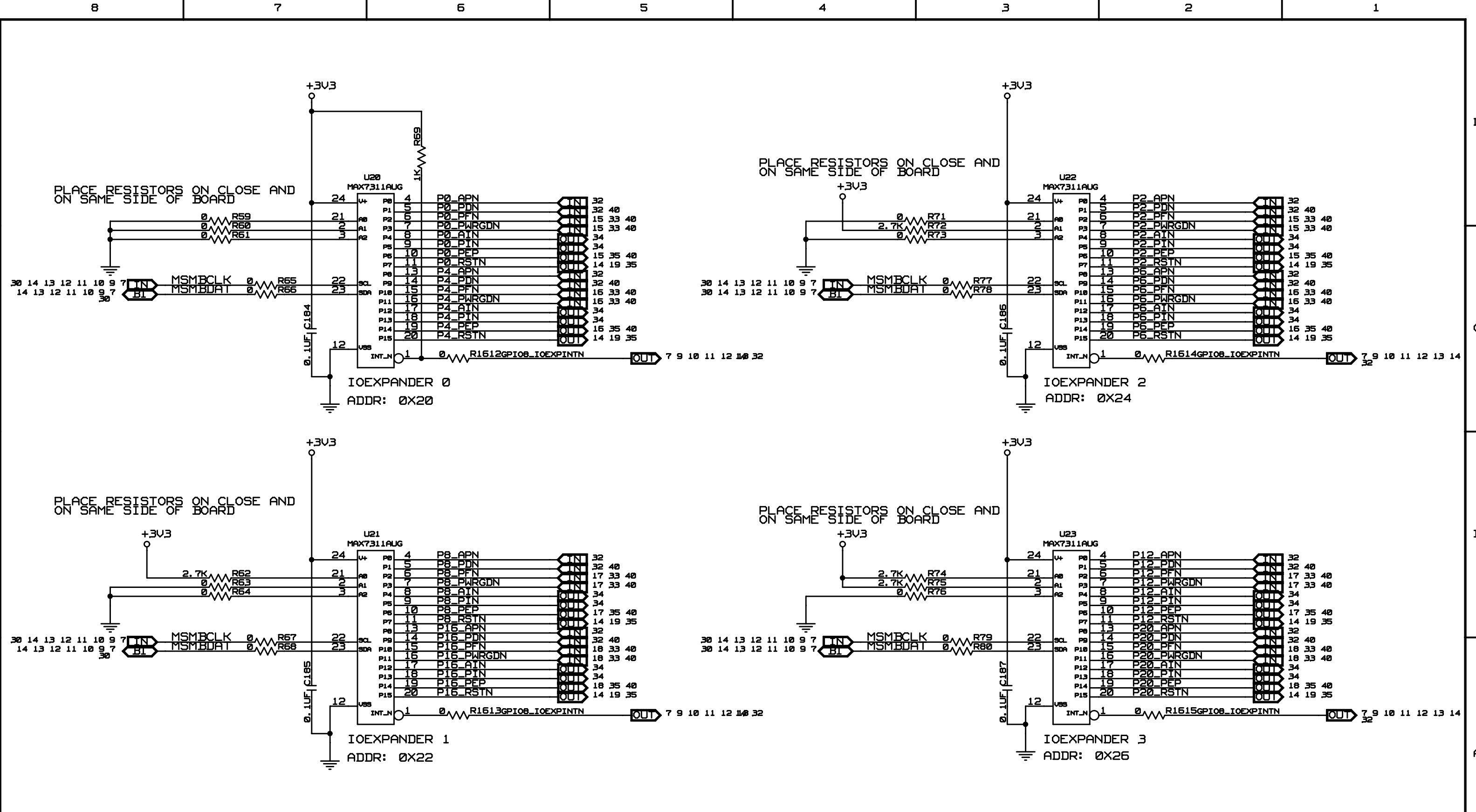





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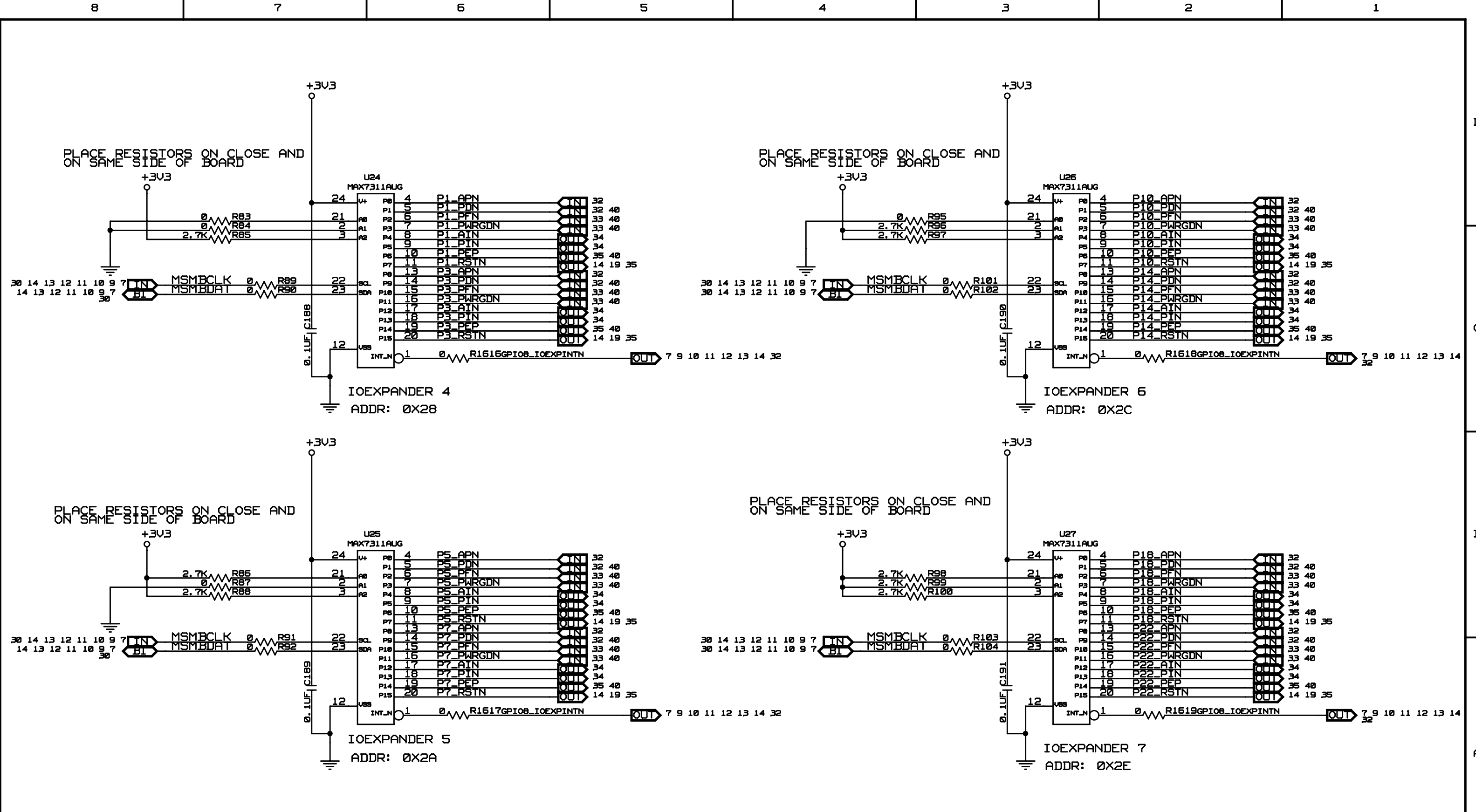
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SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:57 2010			SHEET 8 OF 50






		TITLE EB-LOGAN-23	
		IOEXPANDER 0-3	
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
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TITLE EB-LOGAN-23  
**IOEXPANDER 4-7**

SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:45 2010			SHEET 10 OF 50

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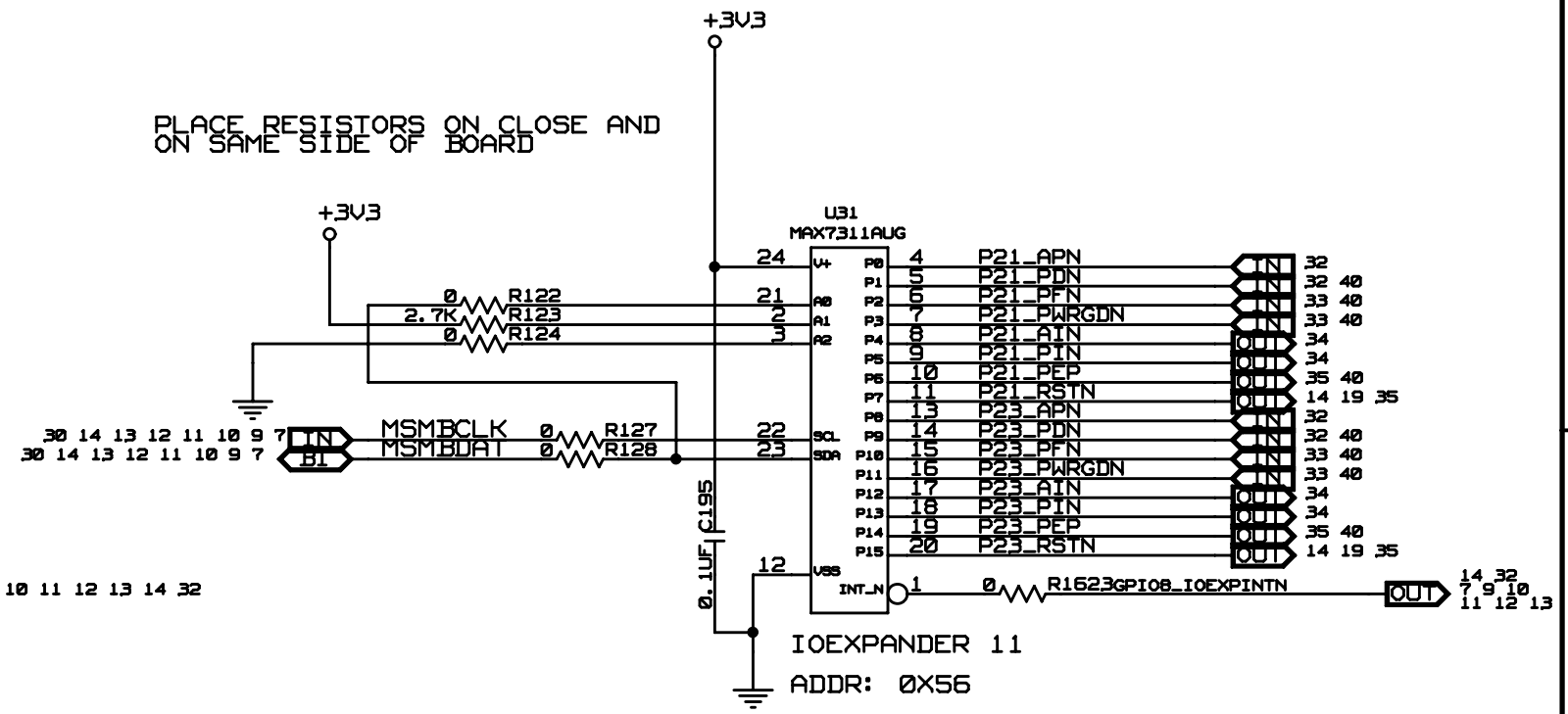
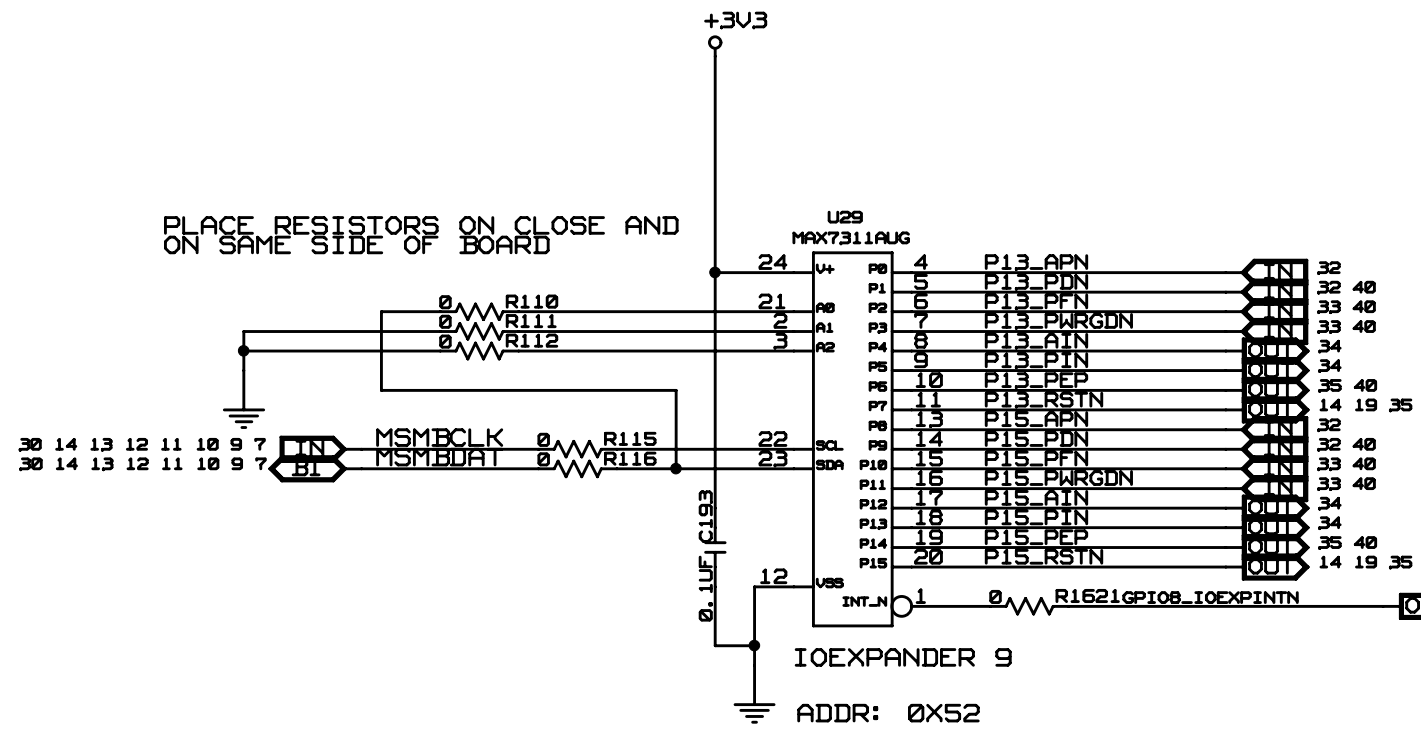
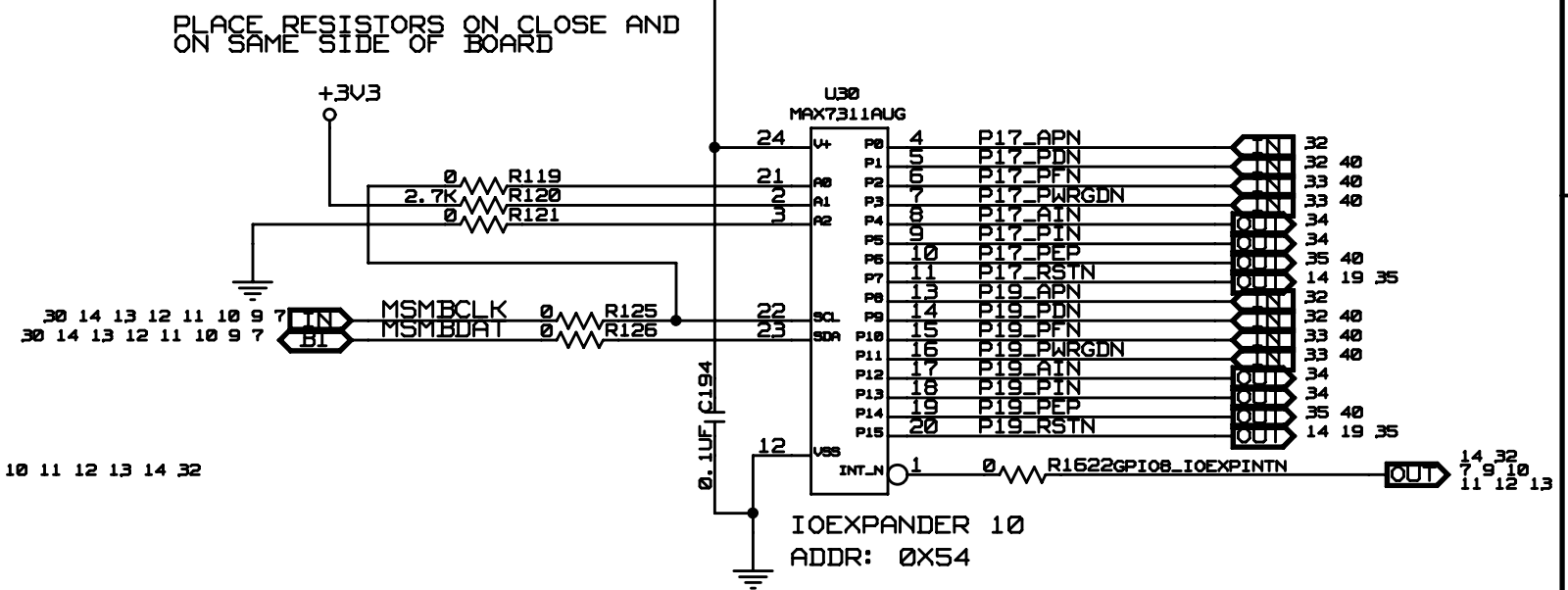
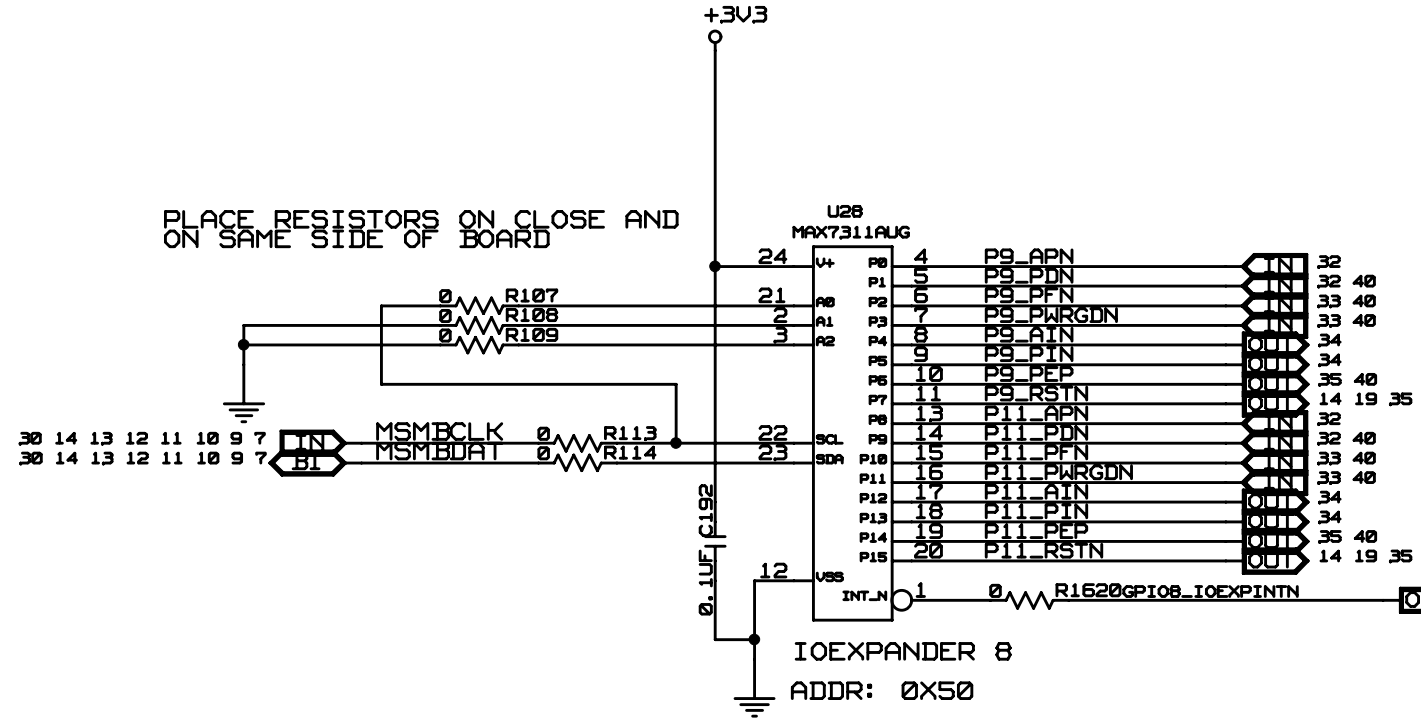
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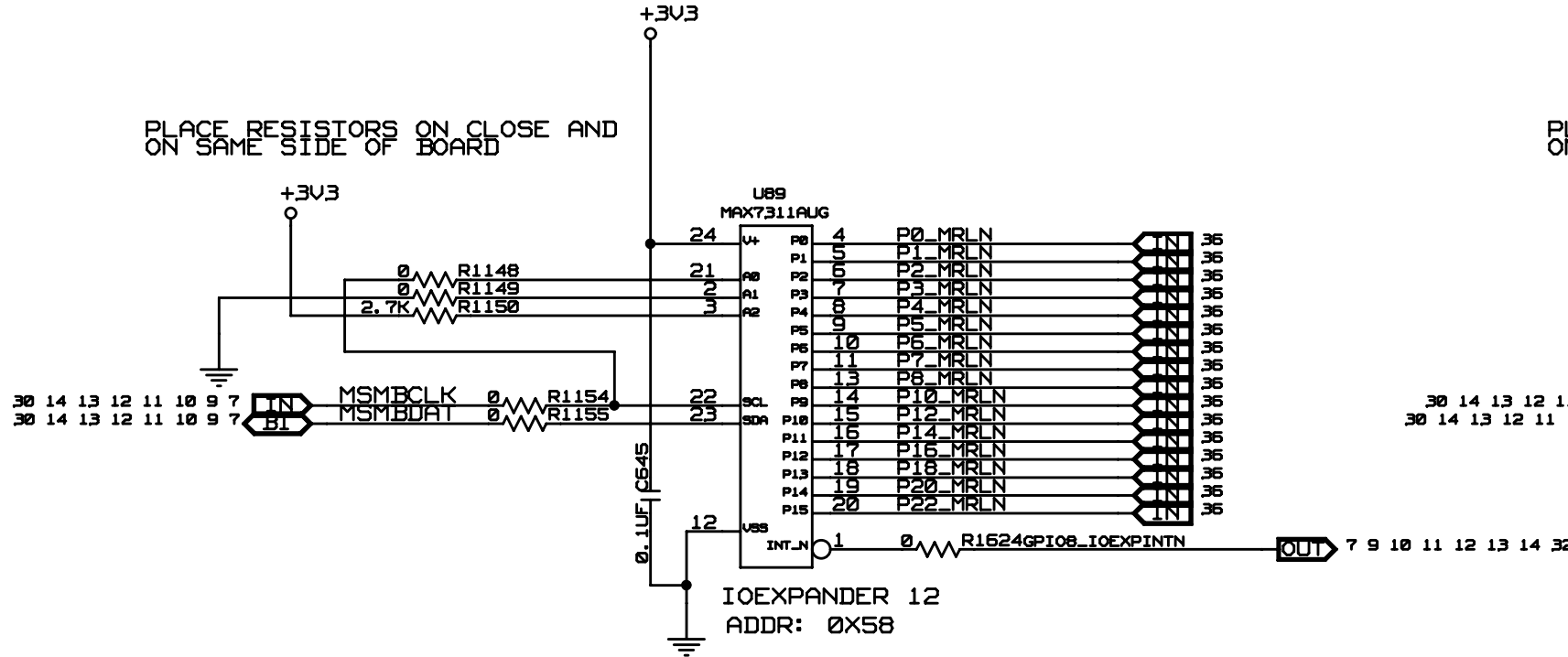


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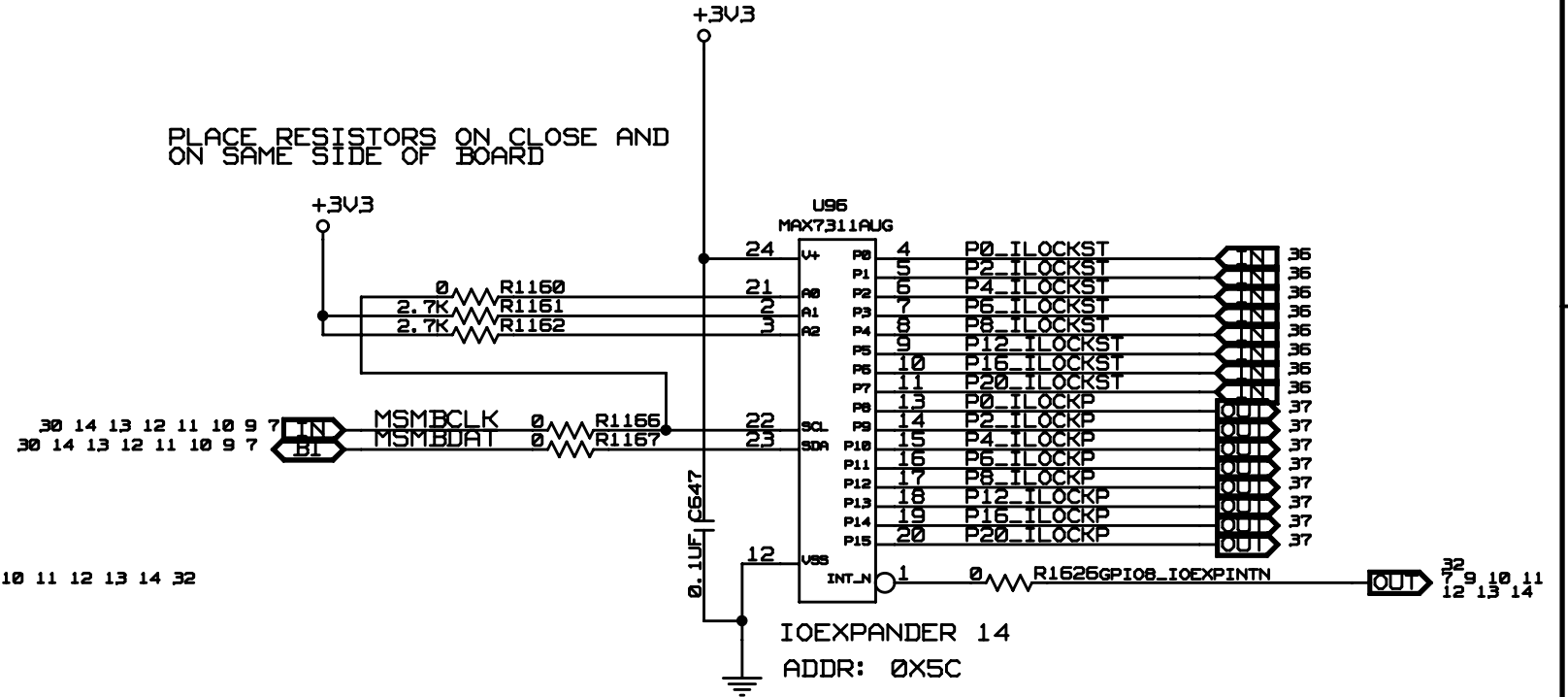
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AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
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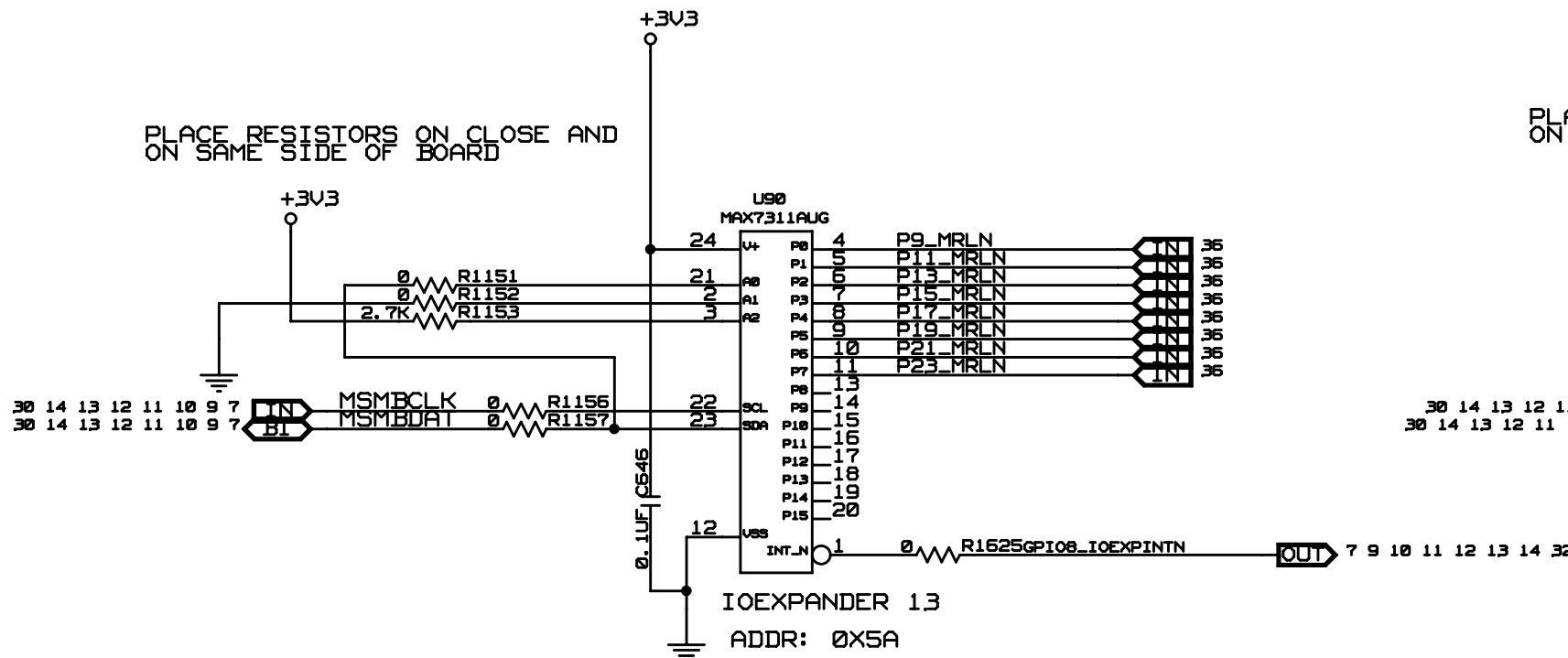
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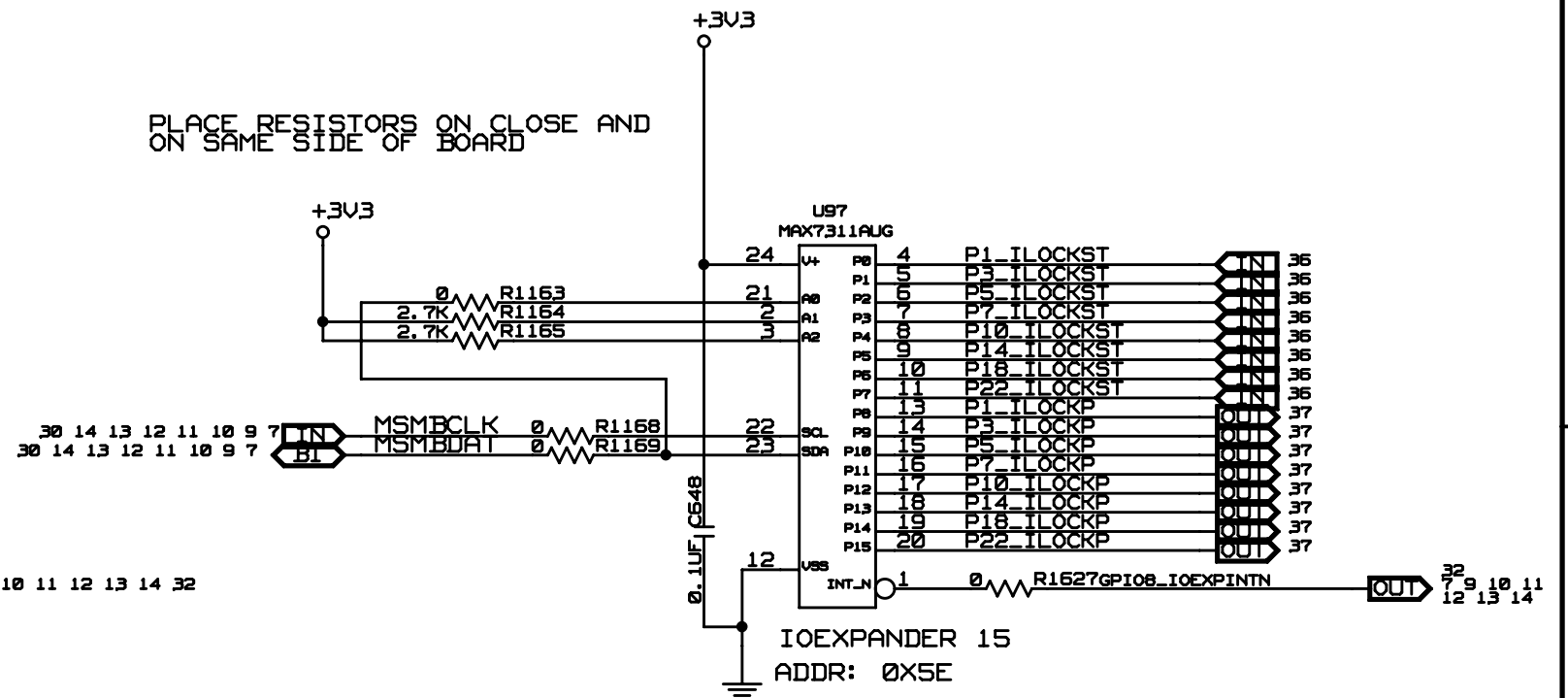
PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD



PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD



PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD



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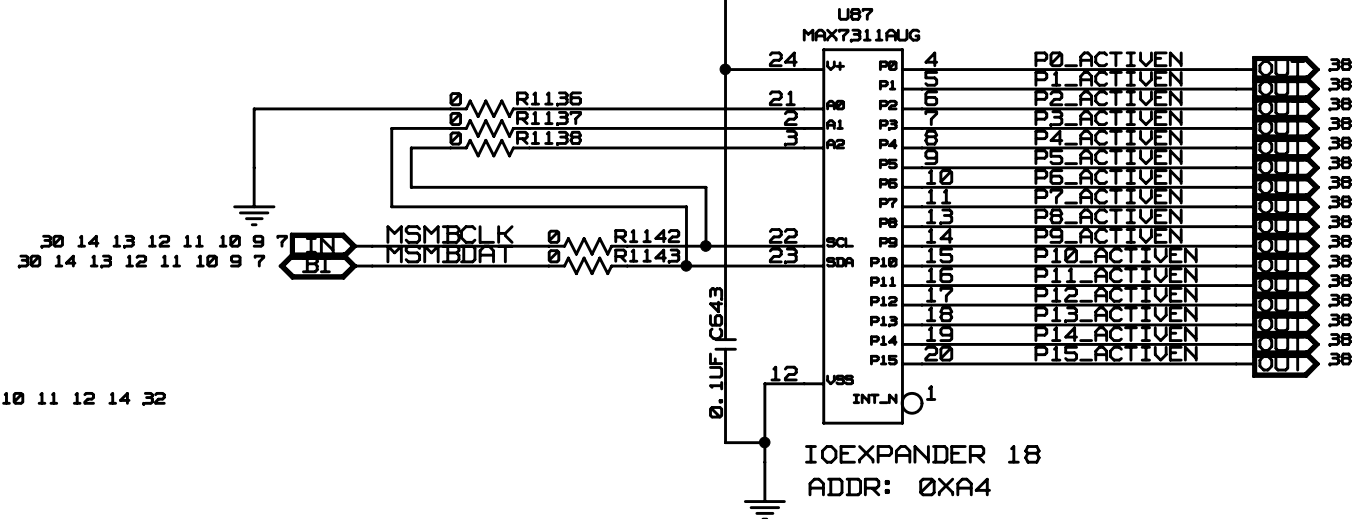
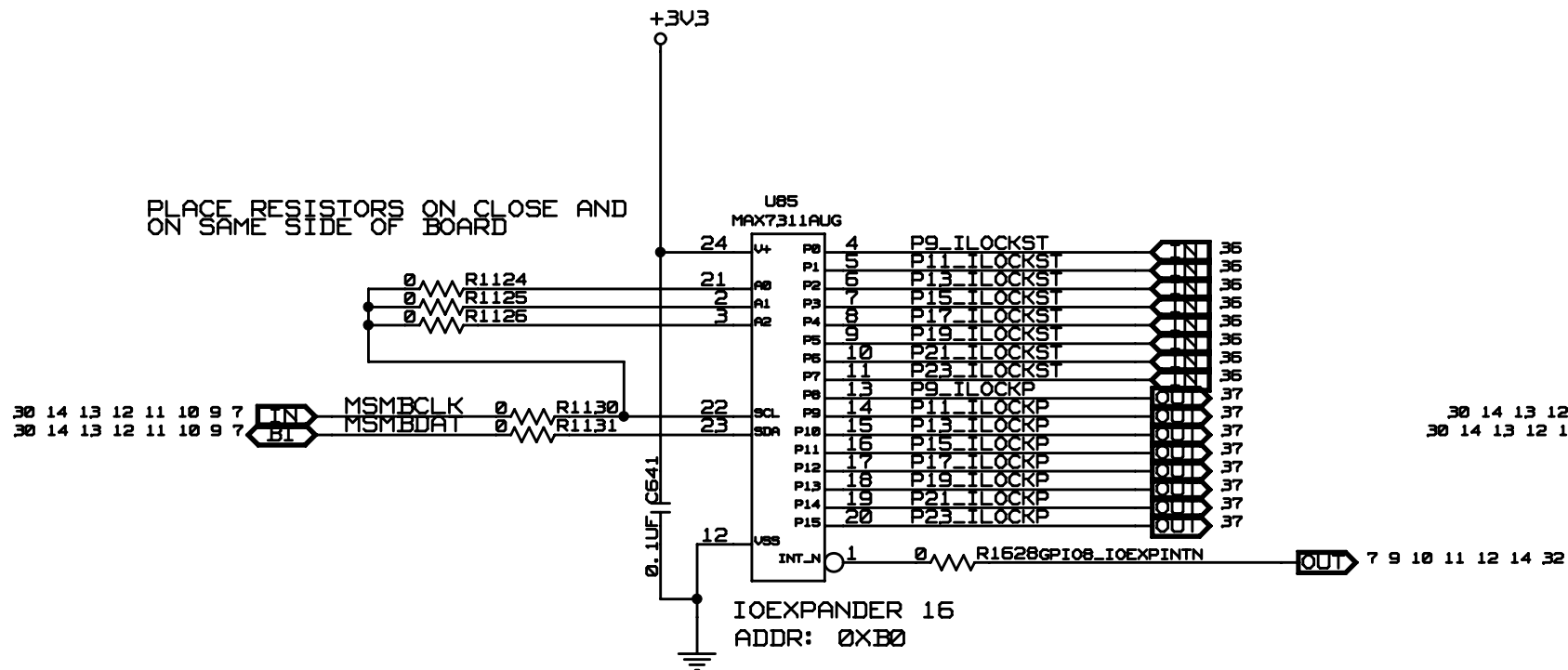
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SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:45 2010			SHEET 12 OF 50

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PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD

PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD

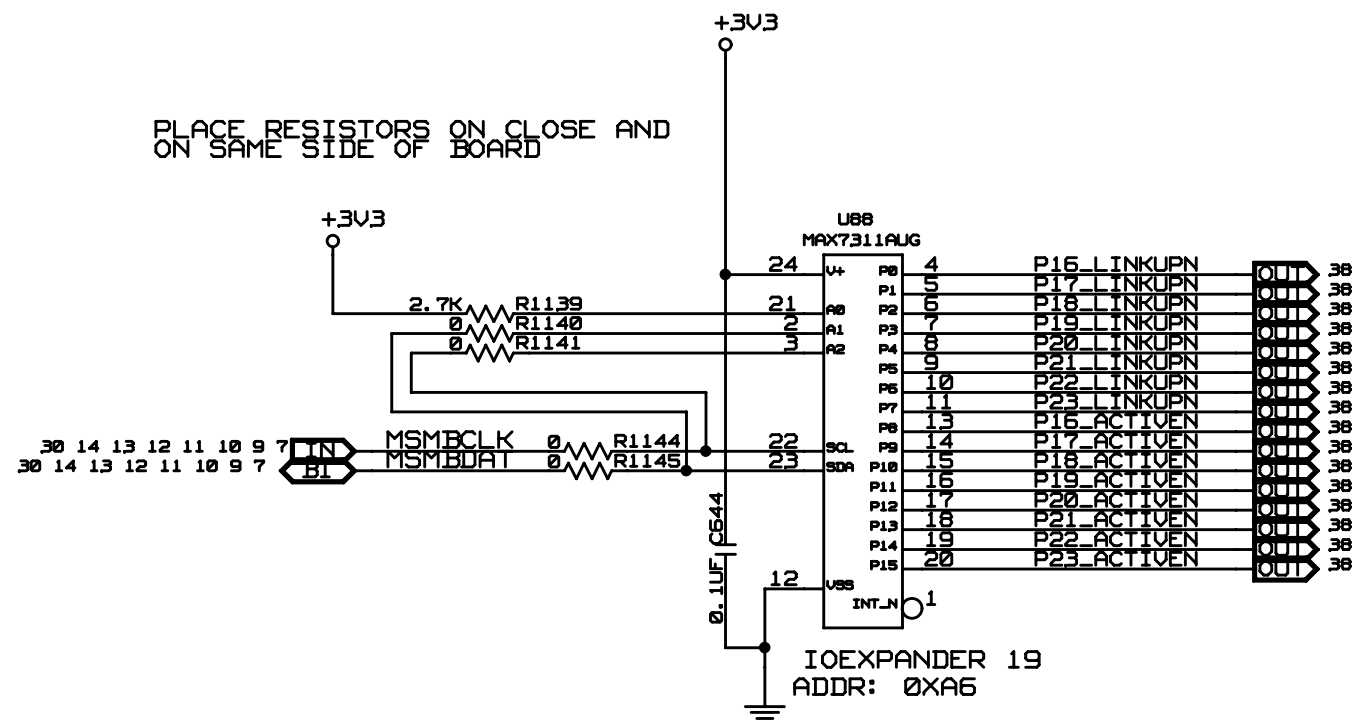
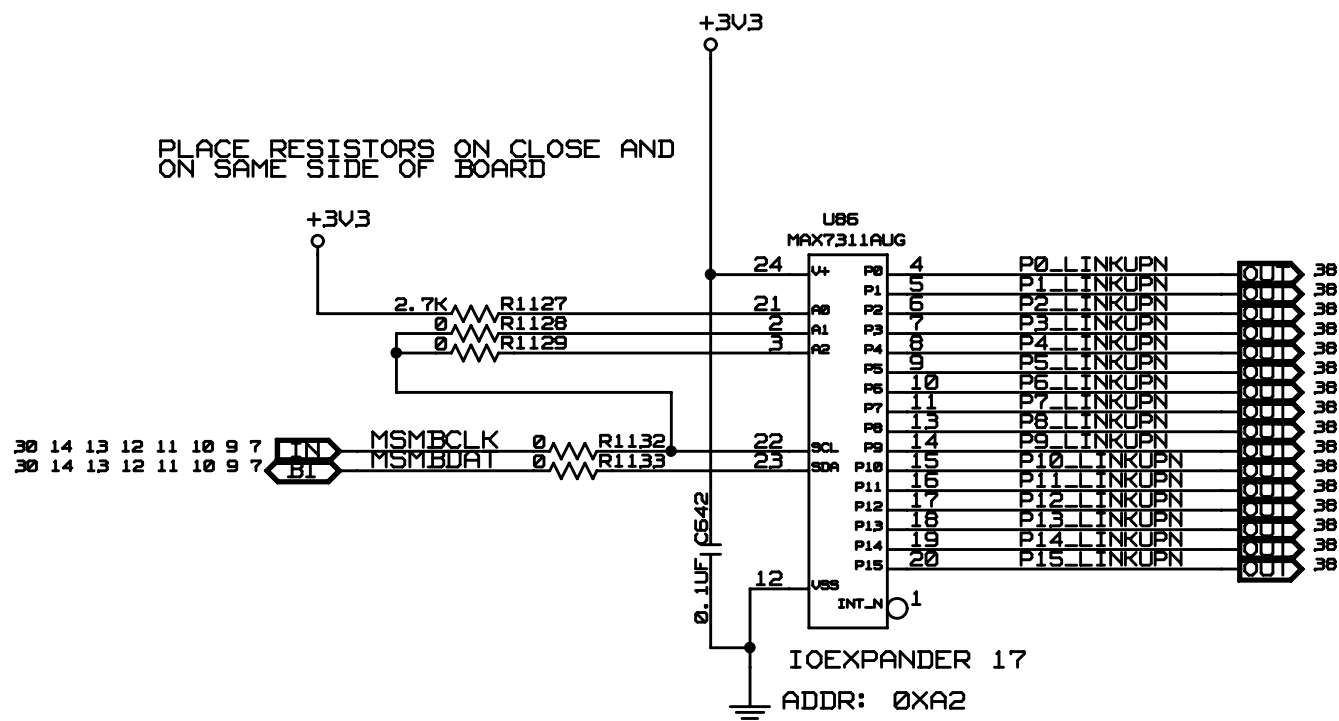


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PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD

PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD



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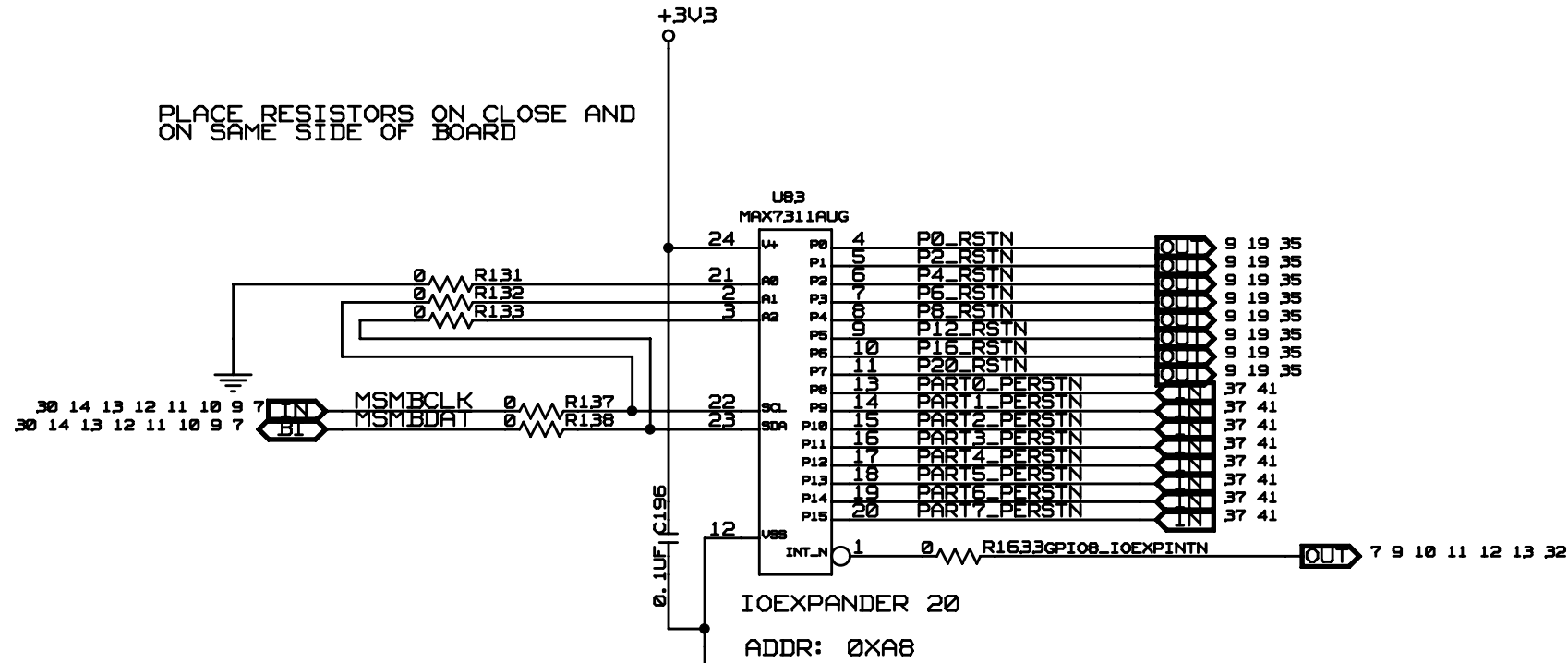


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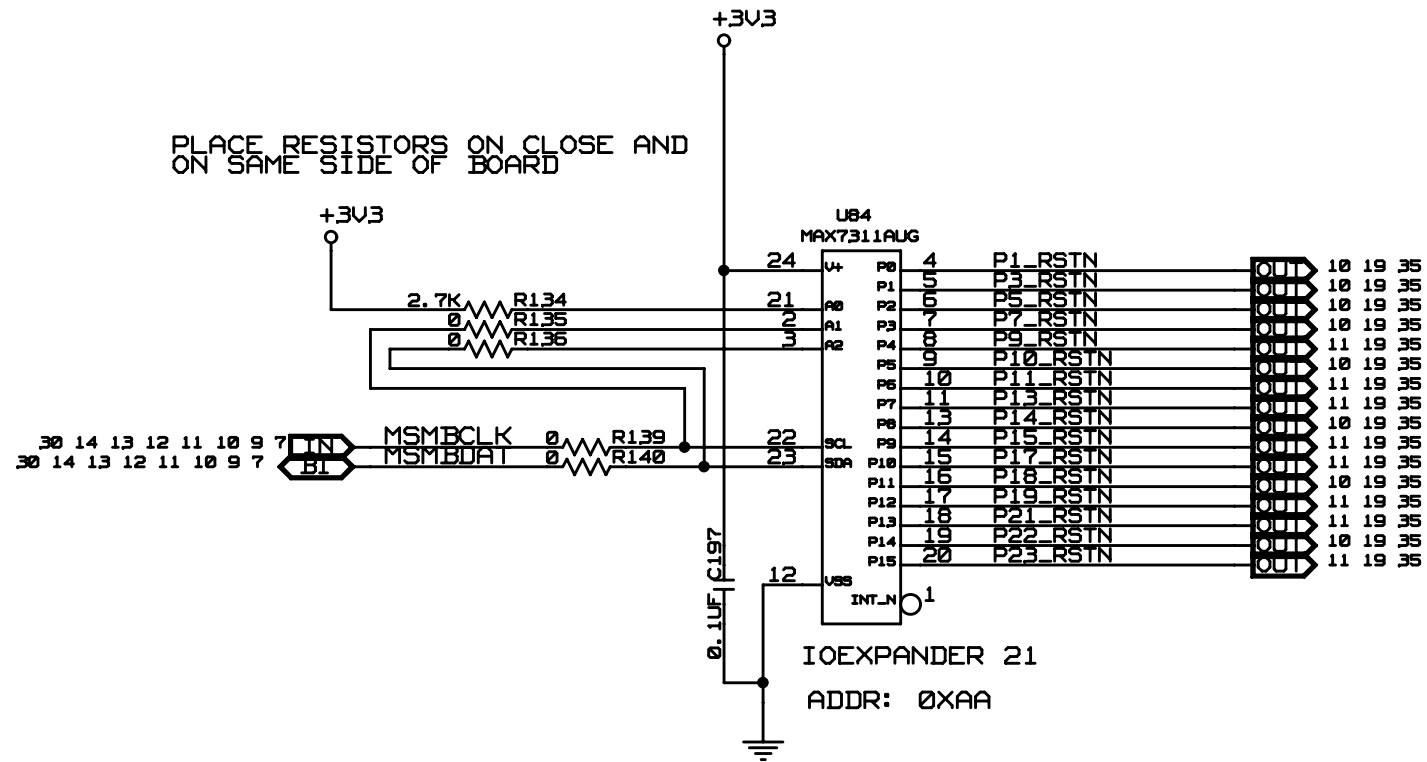
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IOEXPANDER 16-19			
SIZE	DRAWING NO.	FAB P/N	REV.
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AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
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PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD

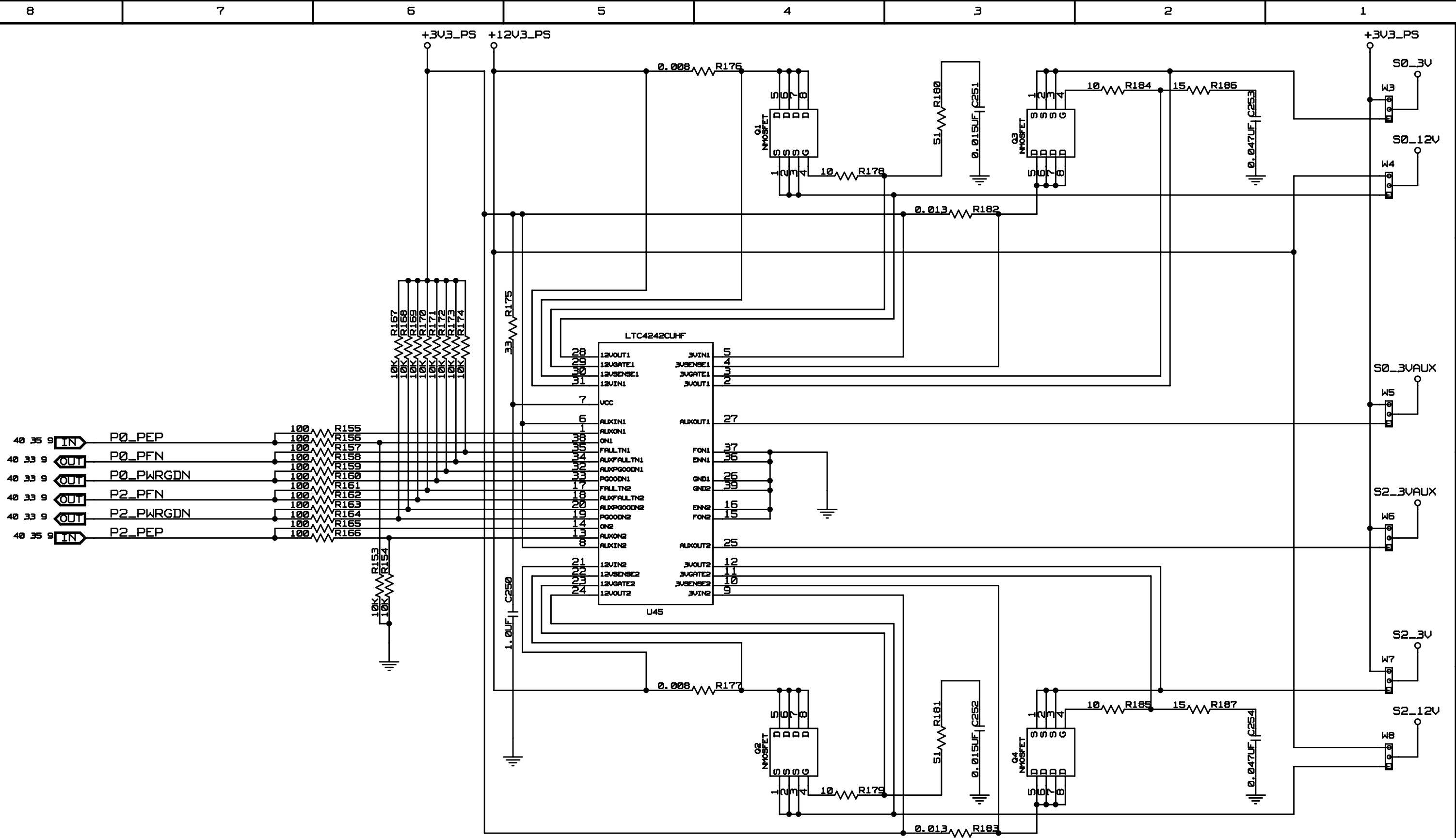


PLACE RESISTORS ON CLOSE AND ON SAME SIDE OF BOARD



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IOEXPANDER 20-21			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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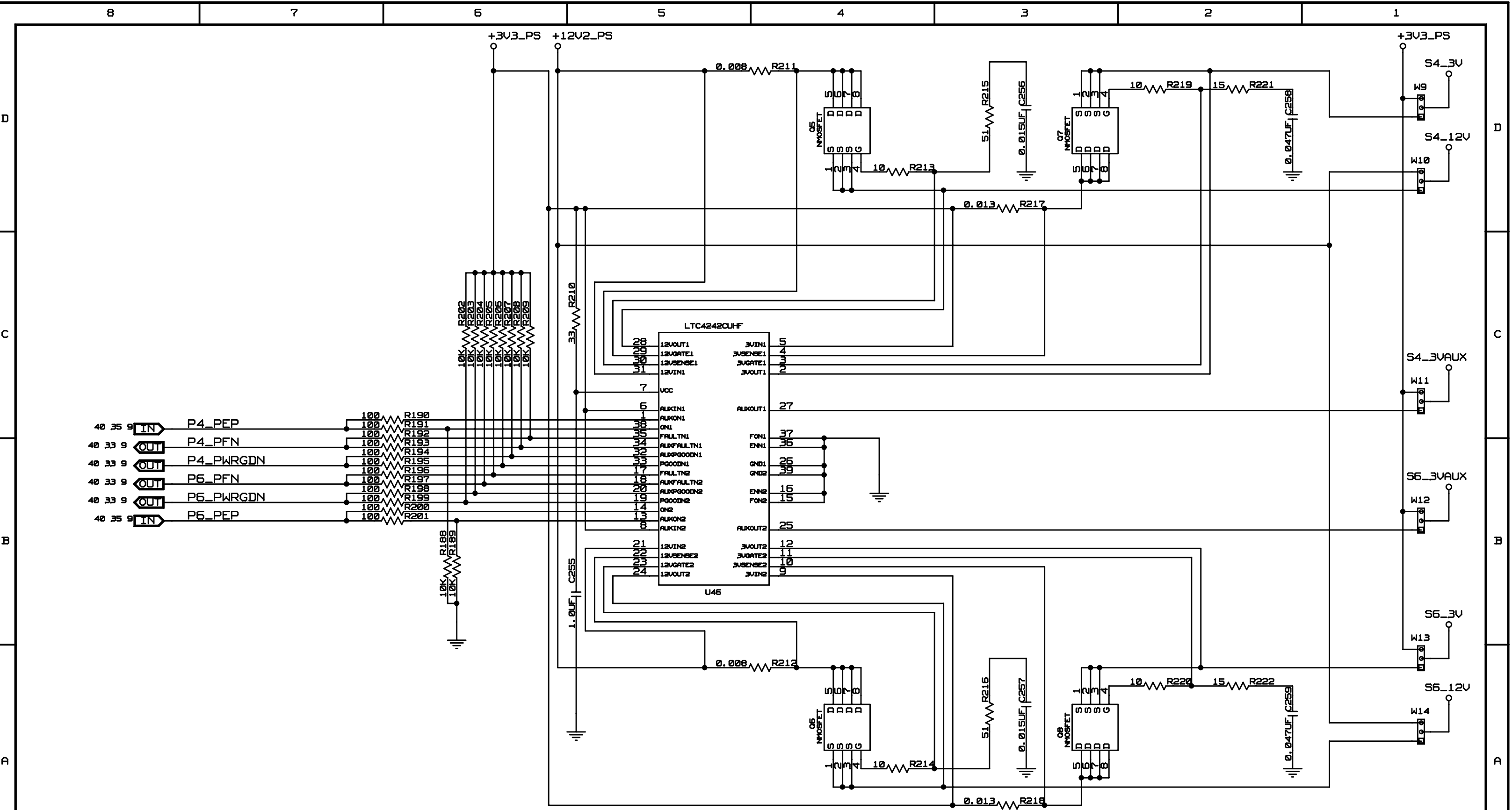
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HOT PLUG CONTROL PORTS 0-2

SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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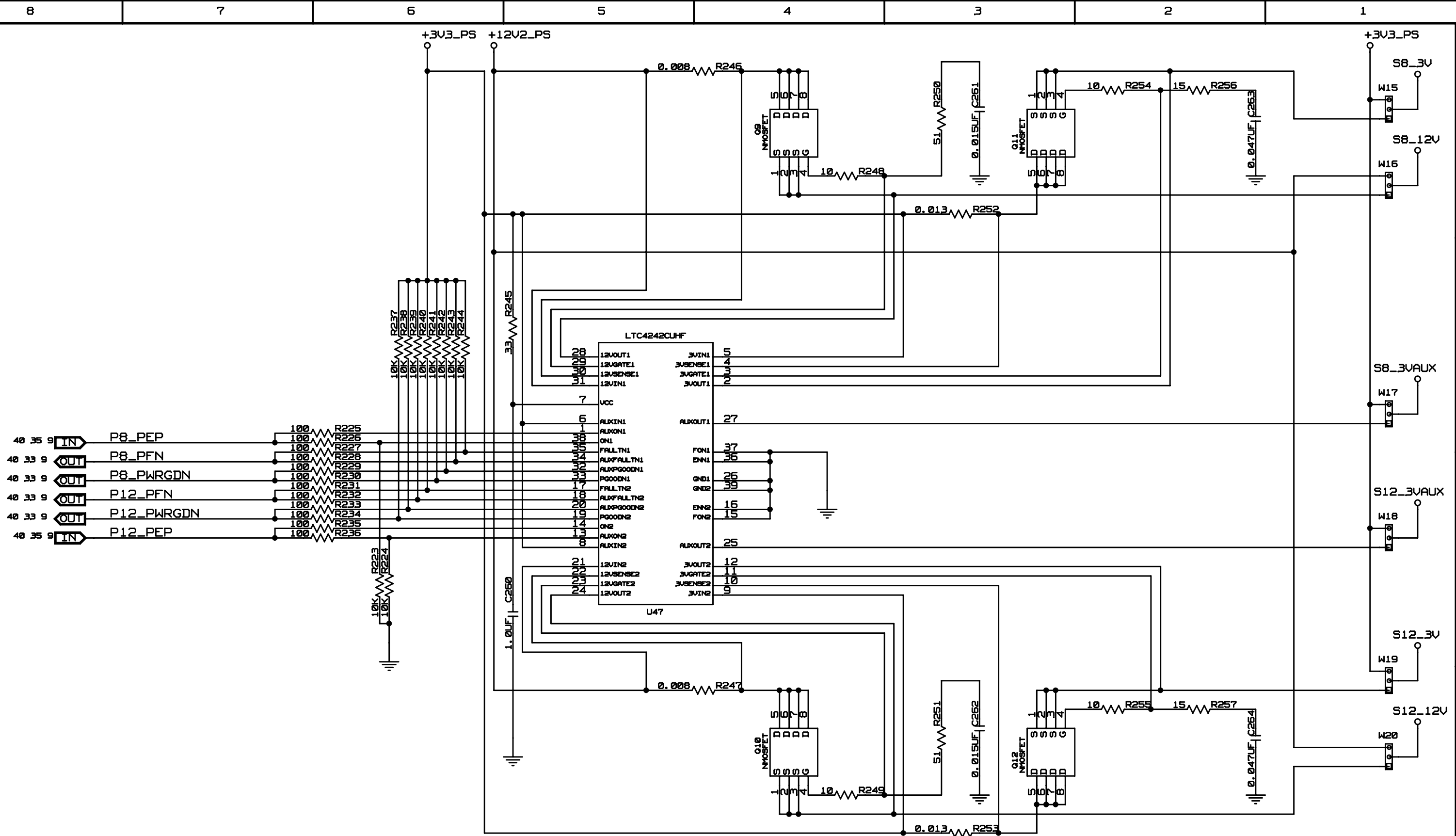
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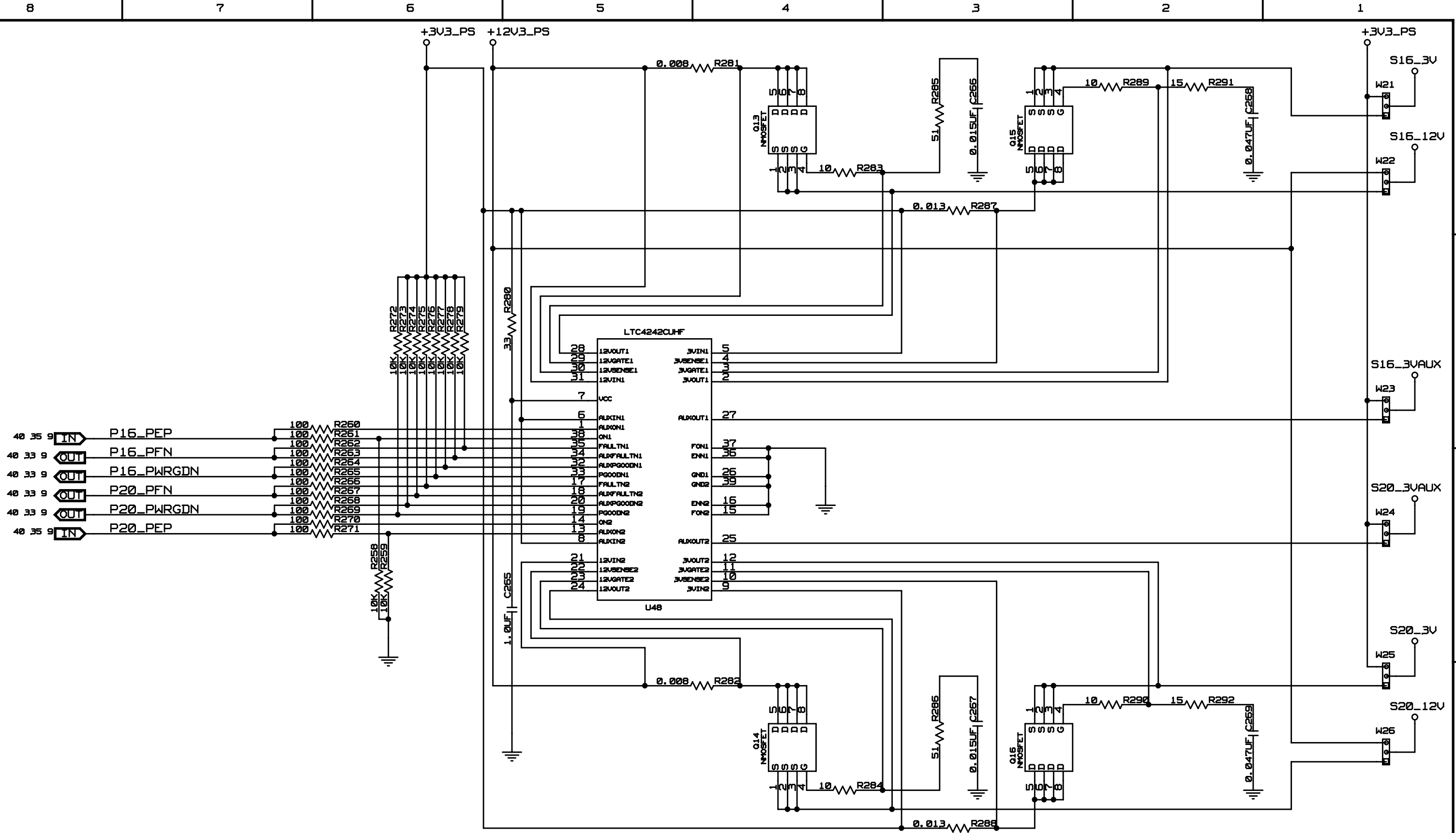
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HOT PLUG CONTROL PORTS 4-6			
SIZE	DRAWING NO.	FAB P/N	REV.
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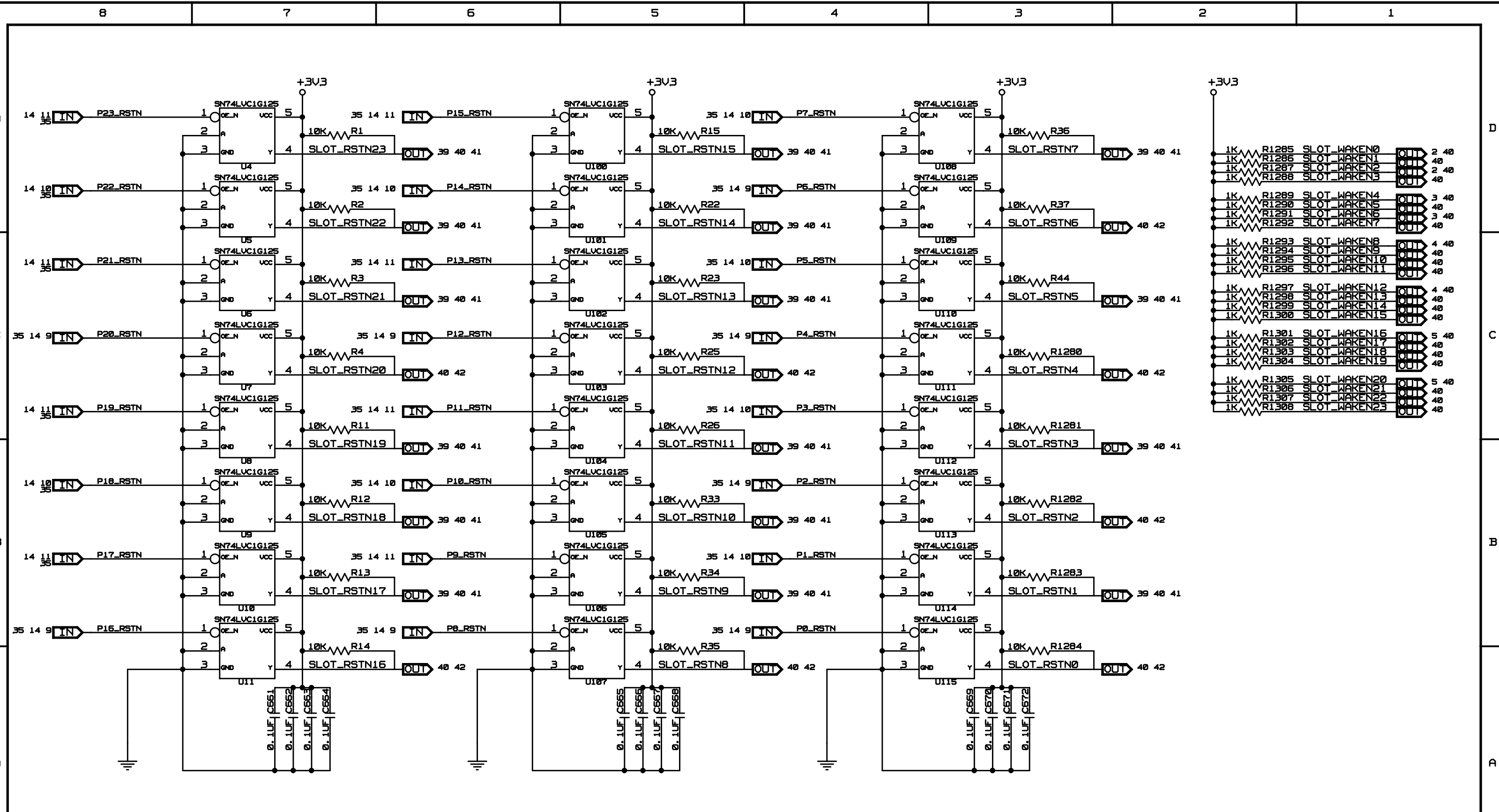

TITLE EB-LOGAN-23			
HOT PLUG CONTROL PORTS 8-12			
SIZE	DRAWING NO.	FAB P/N	REV.
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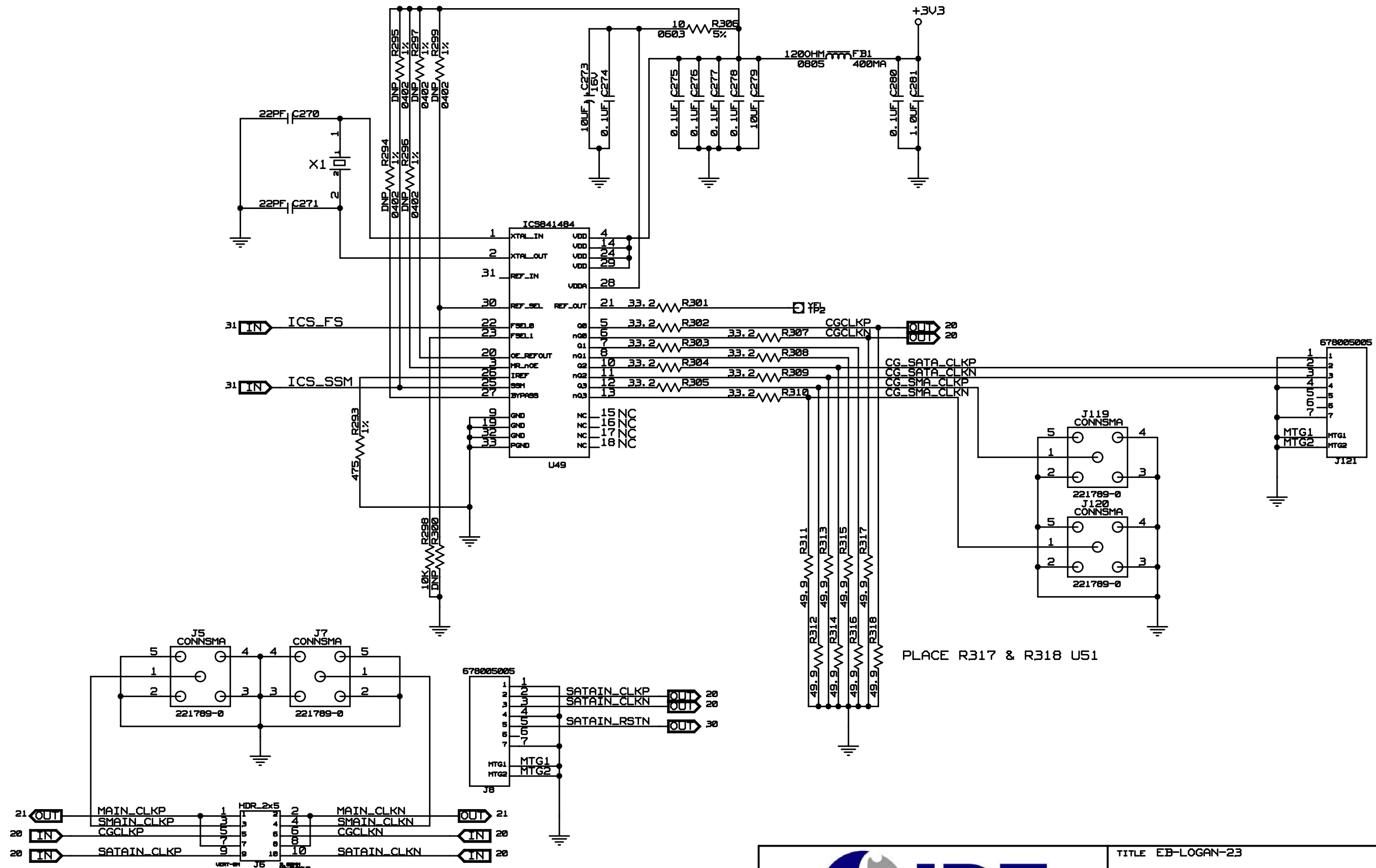
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HOT PLUG CONTROL PORTS 16-20			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
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TITLE EB-LOGAN-23			
SLOT RESETS AND WAKE PULL-UPS			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
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PLACE R317 & R318 U51

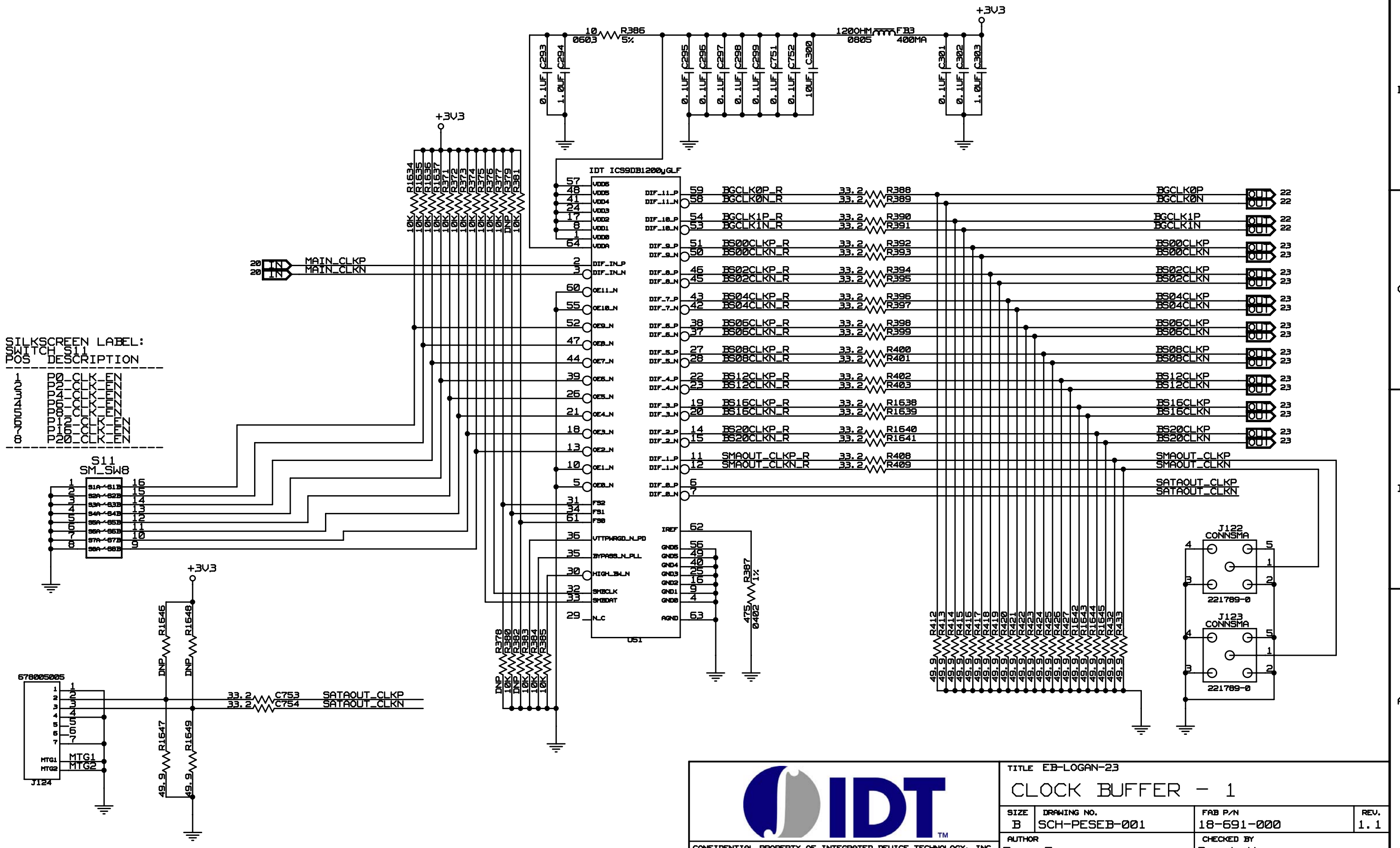
PLACE J6 CLOSE TO U51

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TITLE EB-LOGAN-23			
CLOCK			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:49 2010			SHEET 20 OF 50

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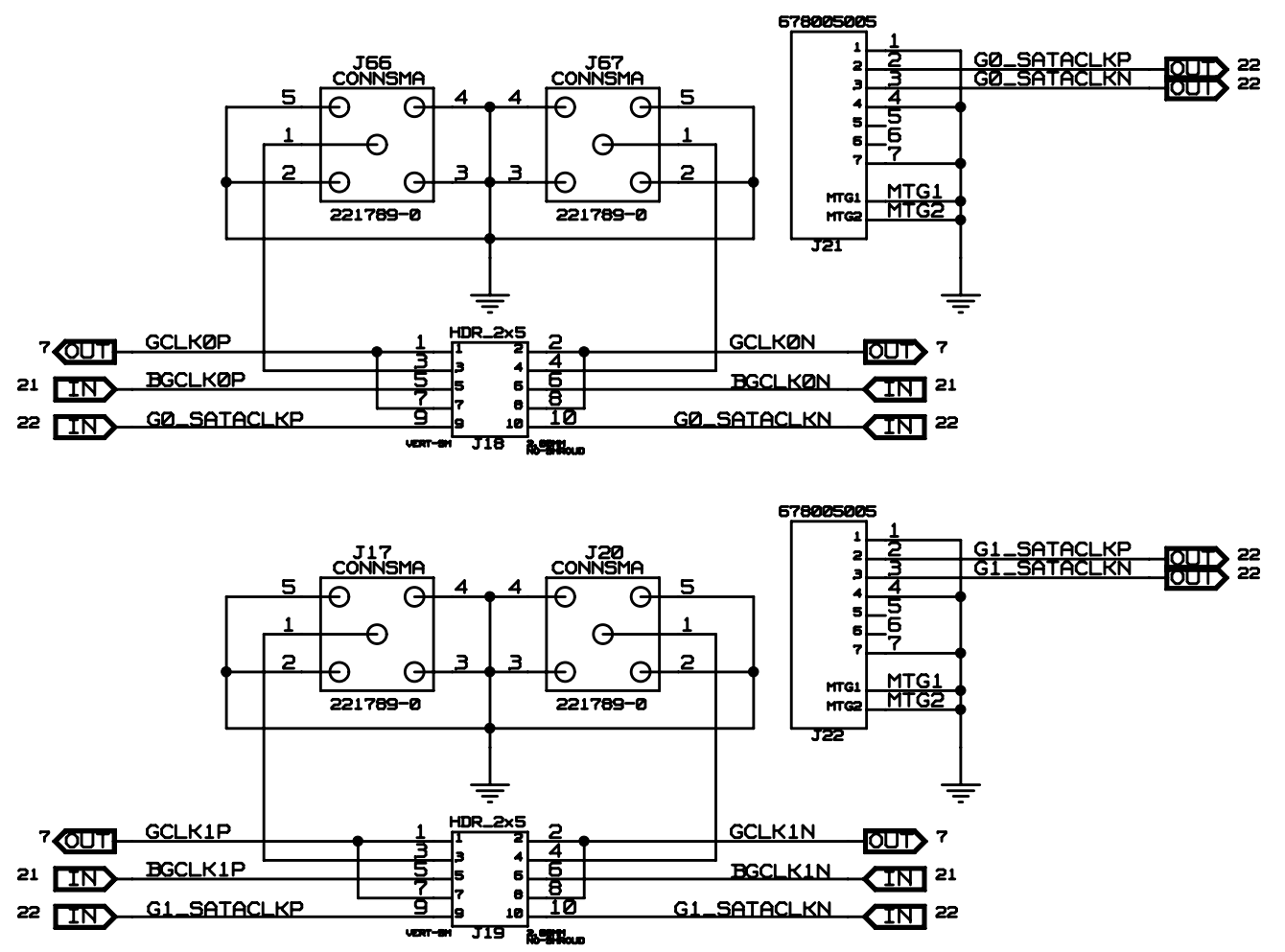
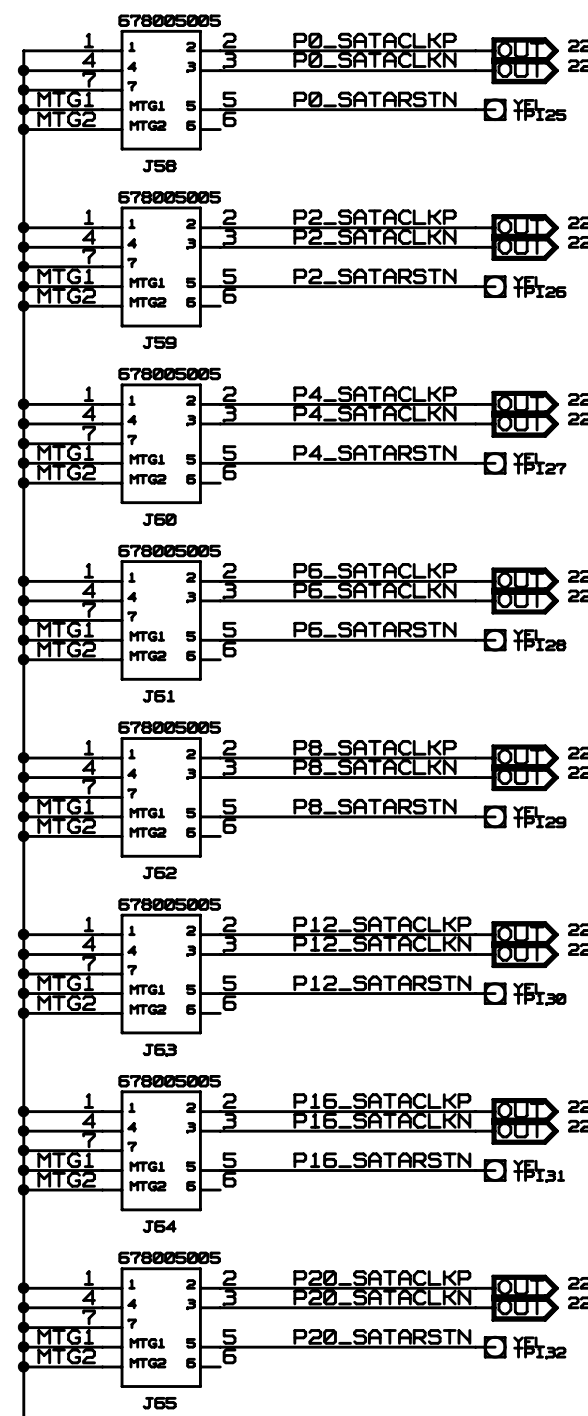
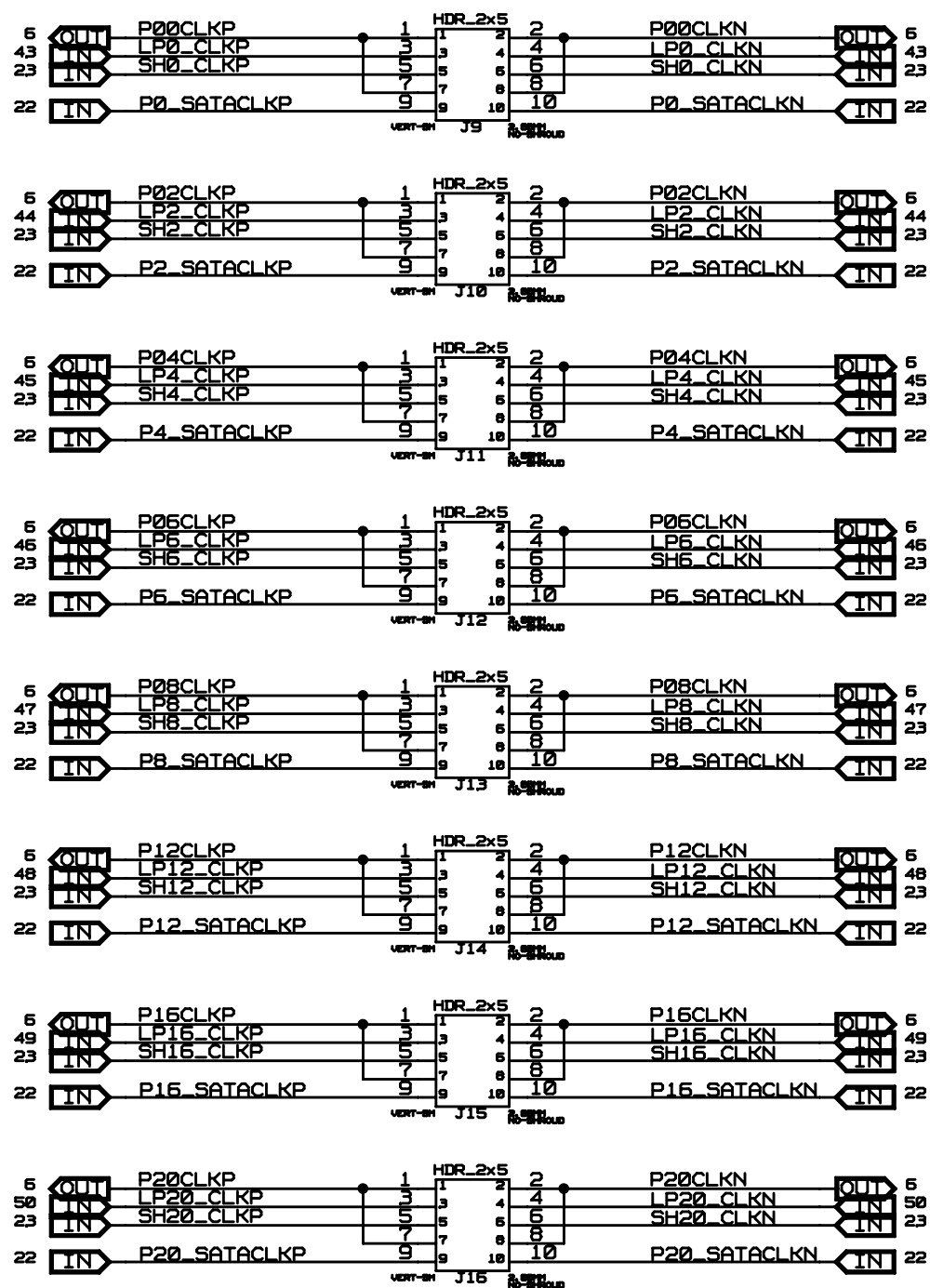
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TITLE EB-LOGAN-23			
CLOCK BUFFER - 1			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:49 2010			SHEET 21 OF 50

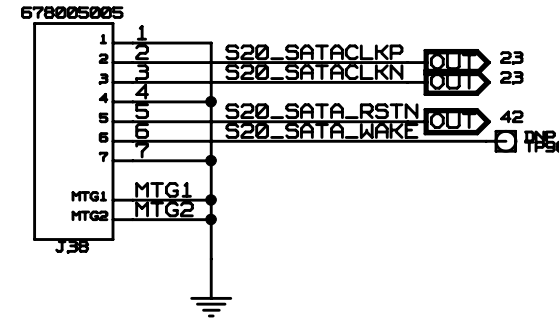
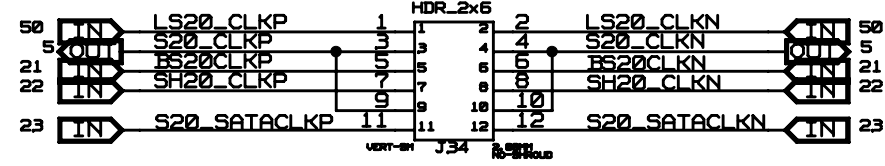
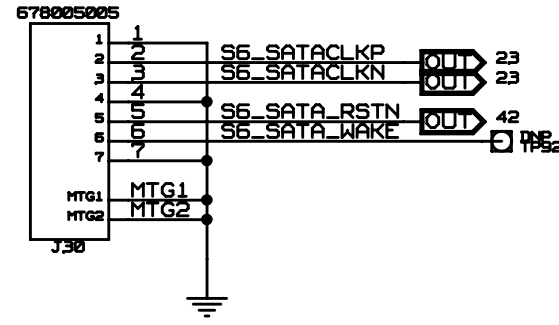
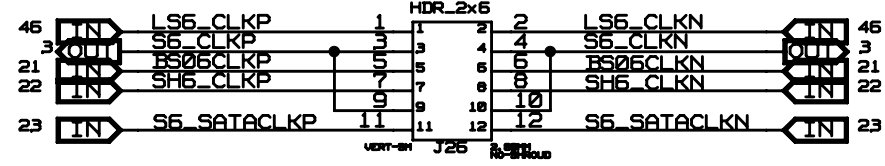
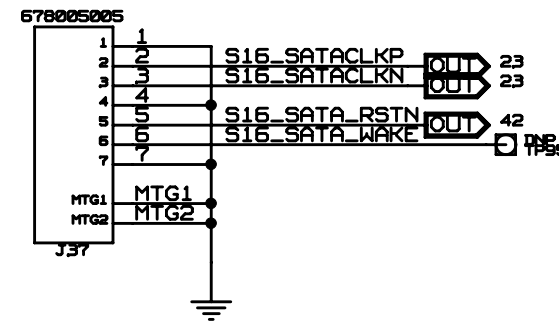
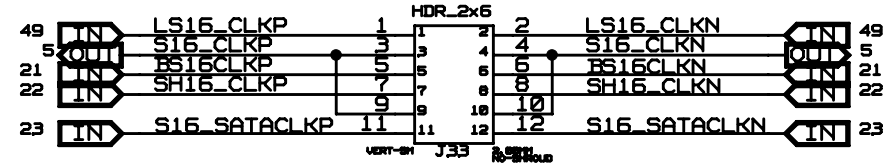
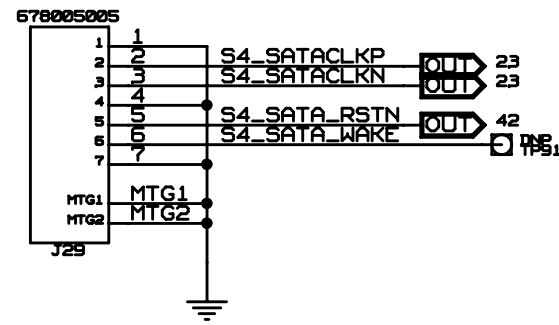
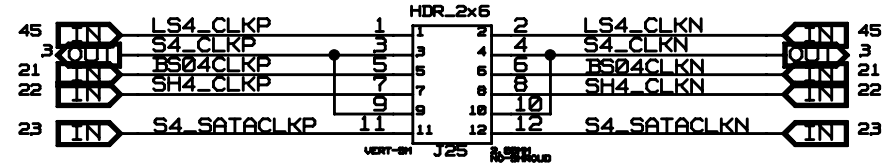
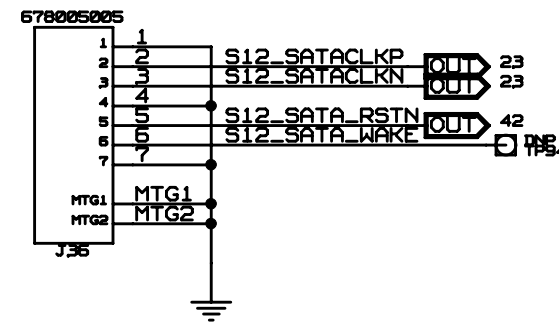
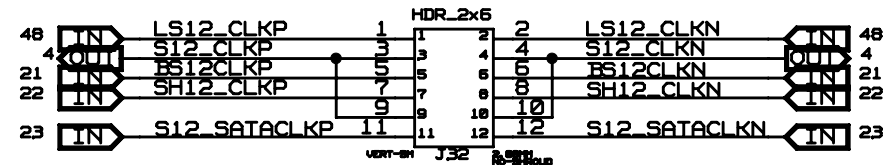
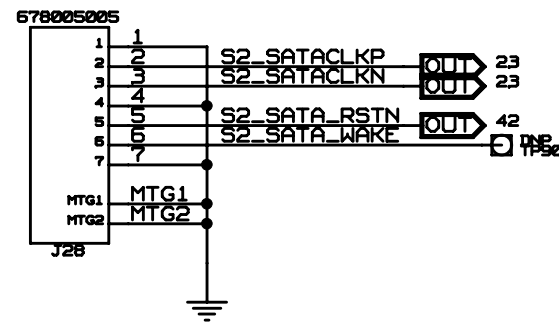
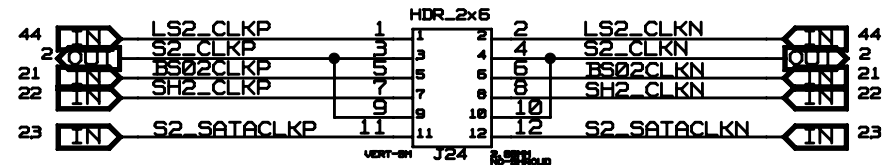
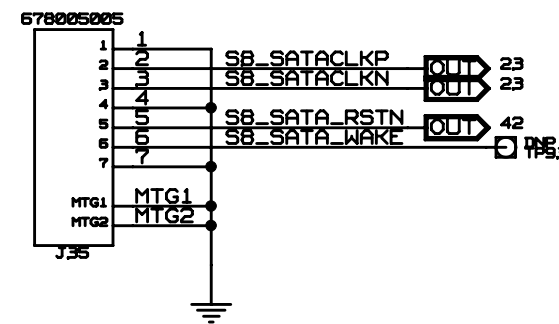
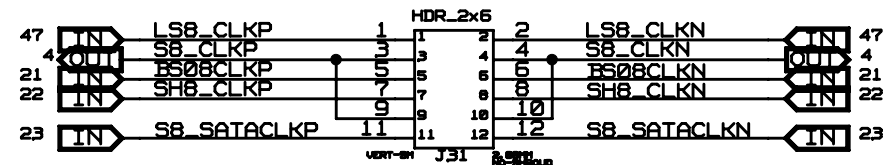
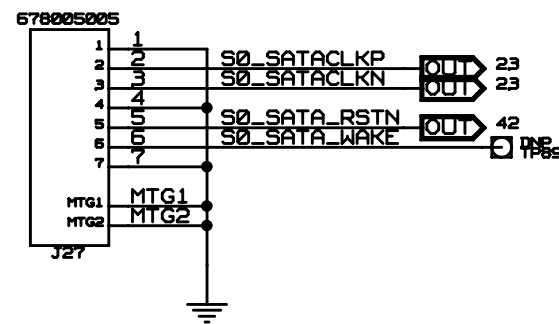
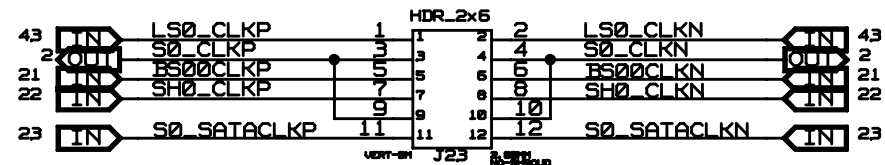
PXXCLK - DUT CLK  
 LPXXCLK - LOCAL CLOCK GEN. PORT CLK  
 SHXXCLK - SLOT HDR. CLK



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TITLE EB-LOGAN-23			
CLOCK SELECTOR DUT PCLK 0-20, GCLK 0-1			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:50 2010			SHEET 22 OF 50

SXXCLK - SLOT CLK  
 BSXXCLK - BUFFER SLOT CLK  
 LSXXCLK - LOCAL CLOCK GEN. SLOT CLK  
 SHXXCLK - SLOT HDR. CLK



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TITLE EB-LOGAN-23

CLOCK SELECTOR SLOTS 0-20

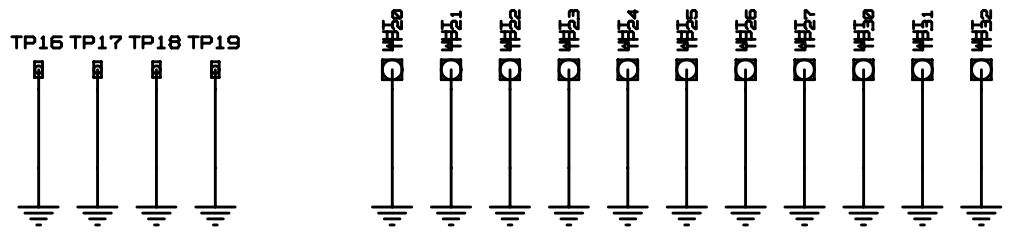
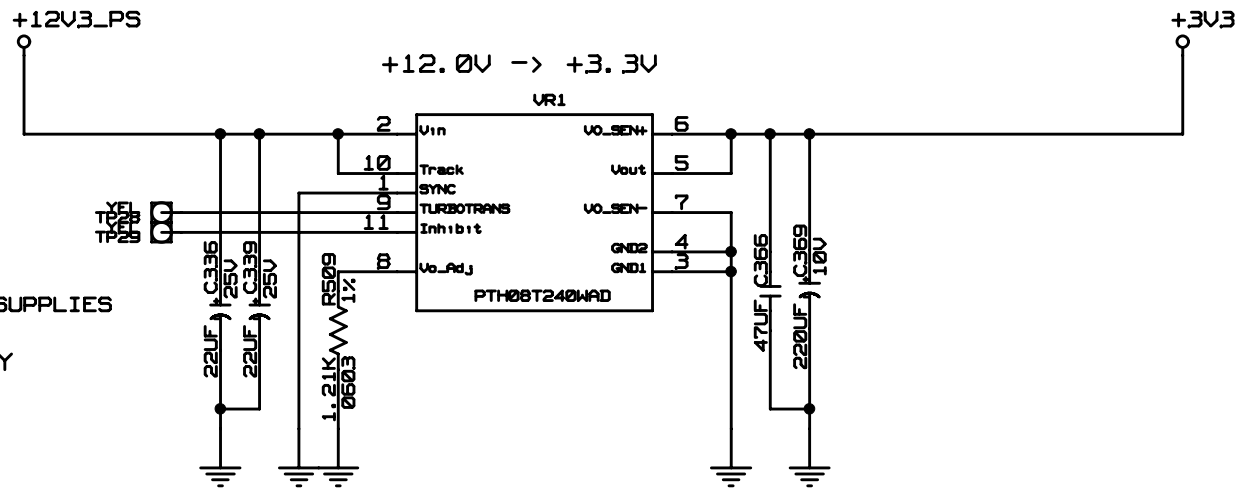
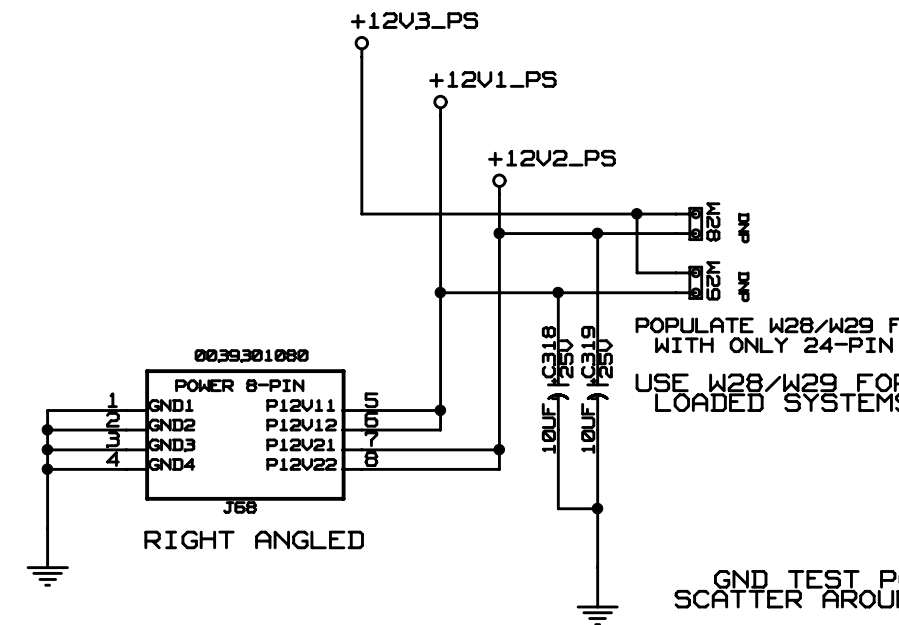
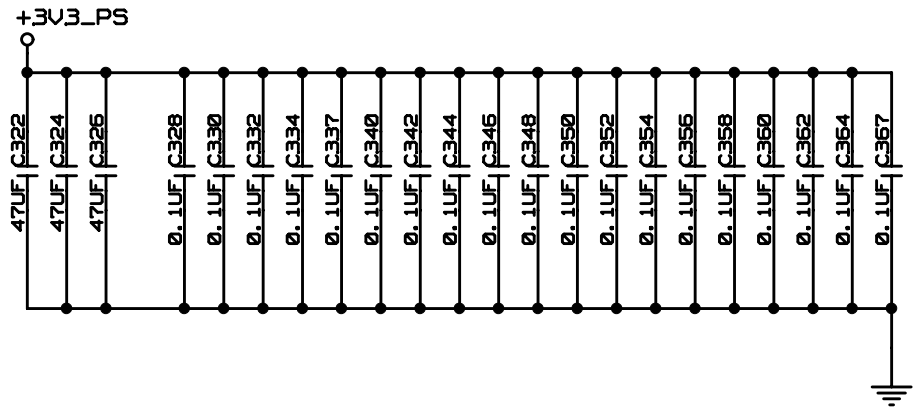
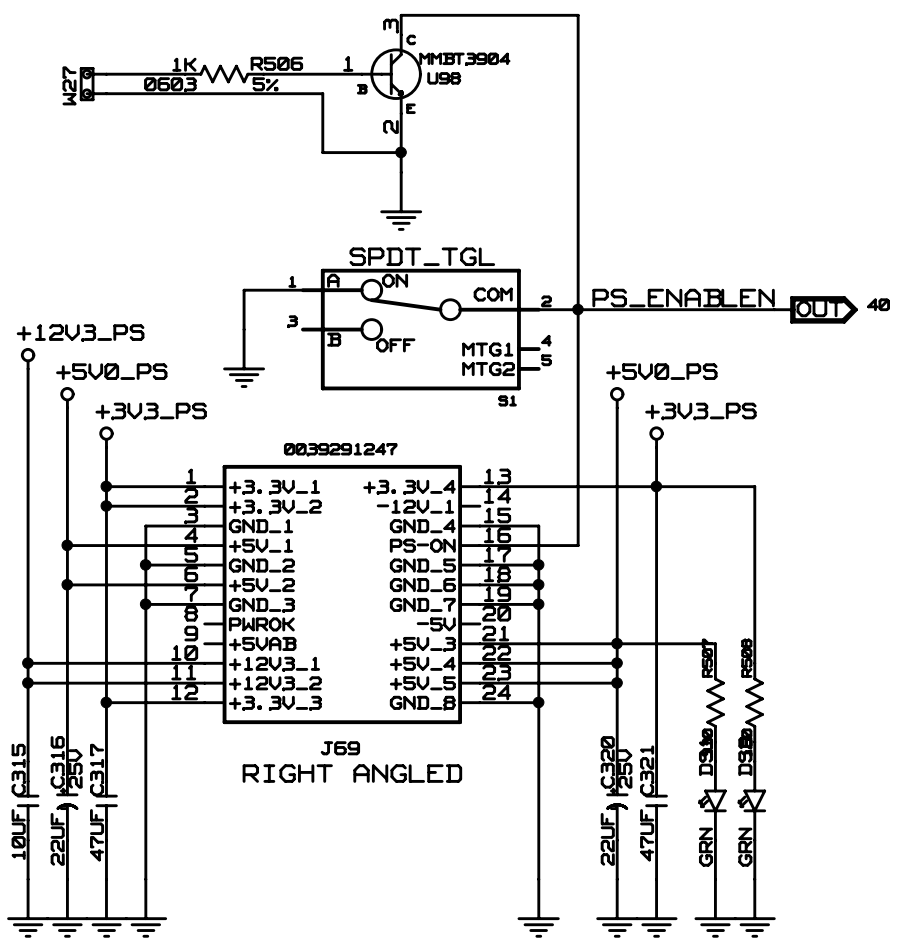
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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AUTHOR Tony Tran	CHECKED BY Derek Huang
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Mon Apr 19 14:30:58 2010

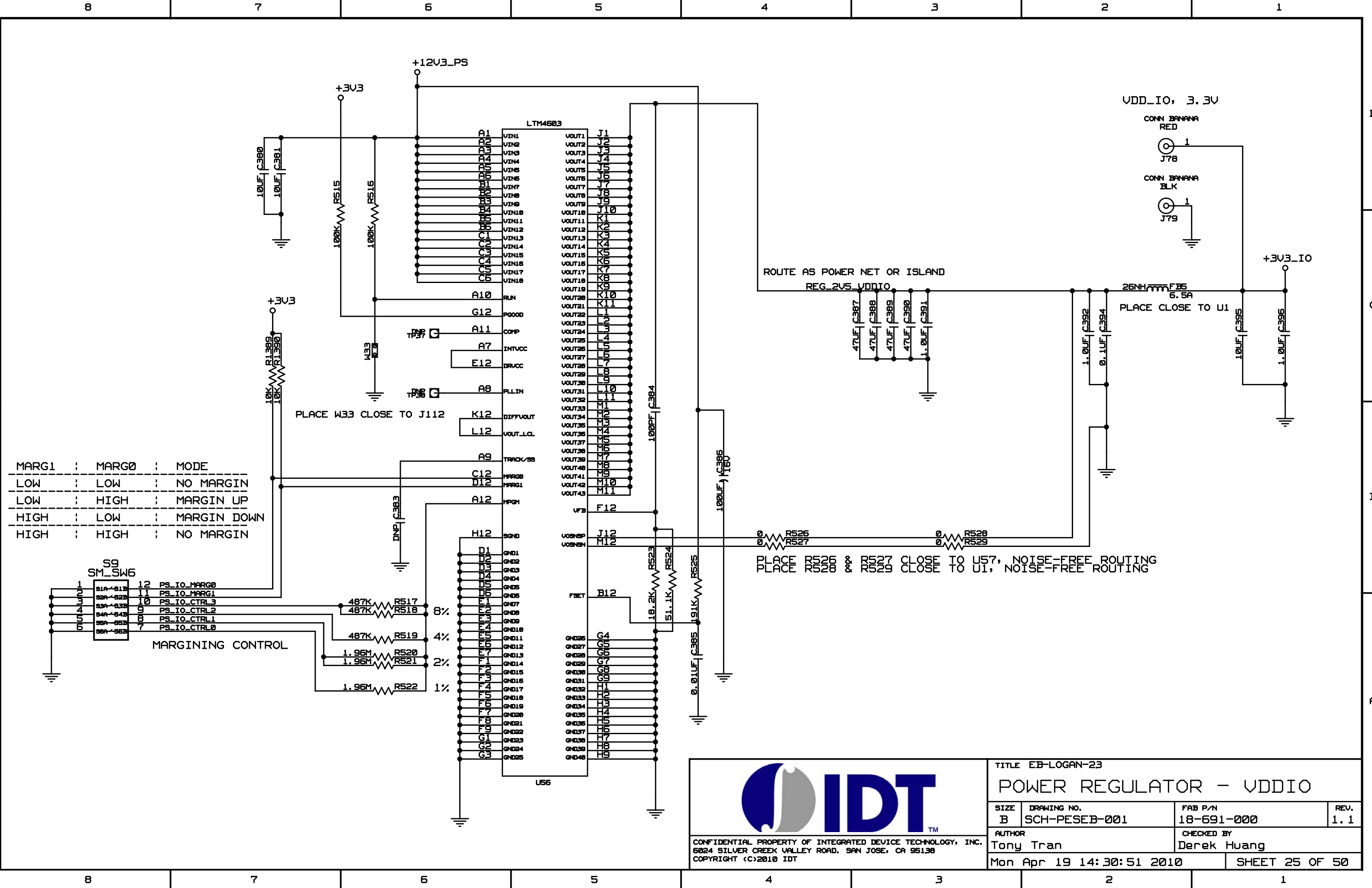
SHEET 23 OF 50





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TITLE EB-LOGAN-23			
POWER CONNECTORS			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:50 2010			SHEET 24 OF 50

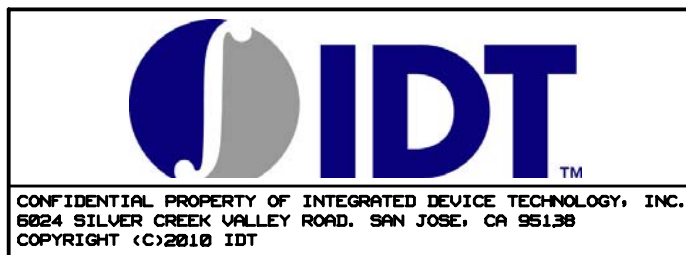
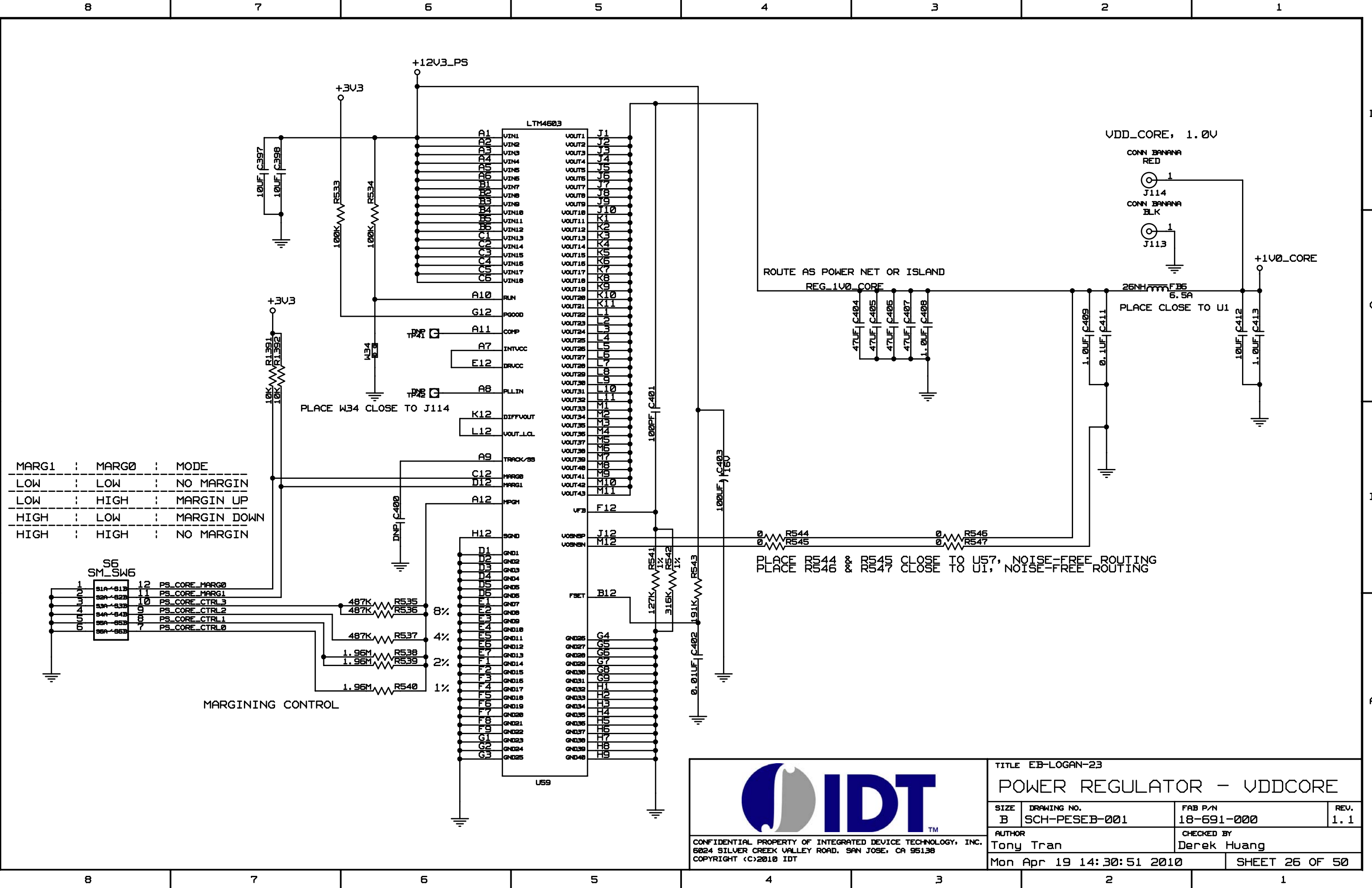


MARG1	MARG0	MODE
LOW	LOW	NO MARGIN
LOW	HIGH	MARGIN UP
HIGH	LOW	MARGIN DOWN
HIGH	HIGH	NO MARGIN

**MARGINING CONTROL**

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TITLE EB-LOGAN-23			
POWER REGULATOR - VDDIO			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:51 2010			SHEET 25 OF 50



TITLE EB-LOGAN-23			
POWER REGULATOR - VDDCORE			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:51 2010			SHEET 26 OF 50

D

D

C

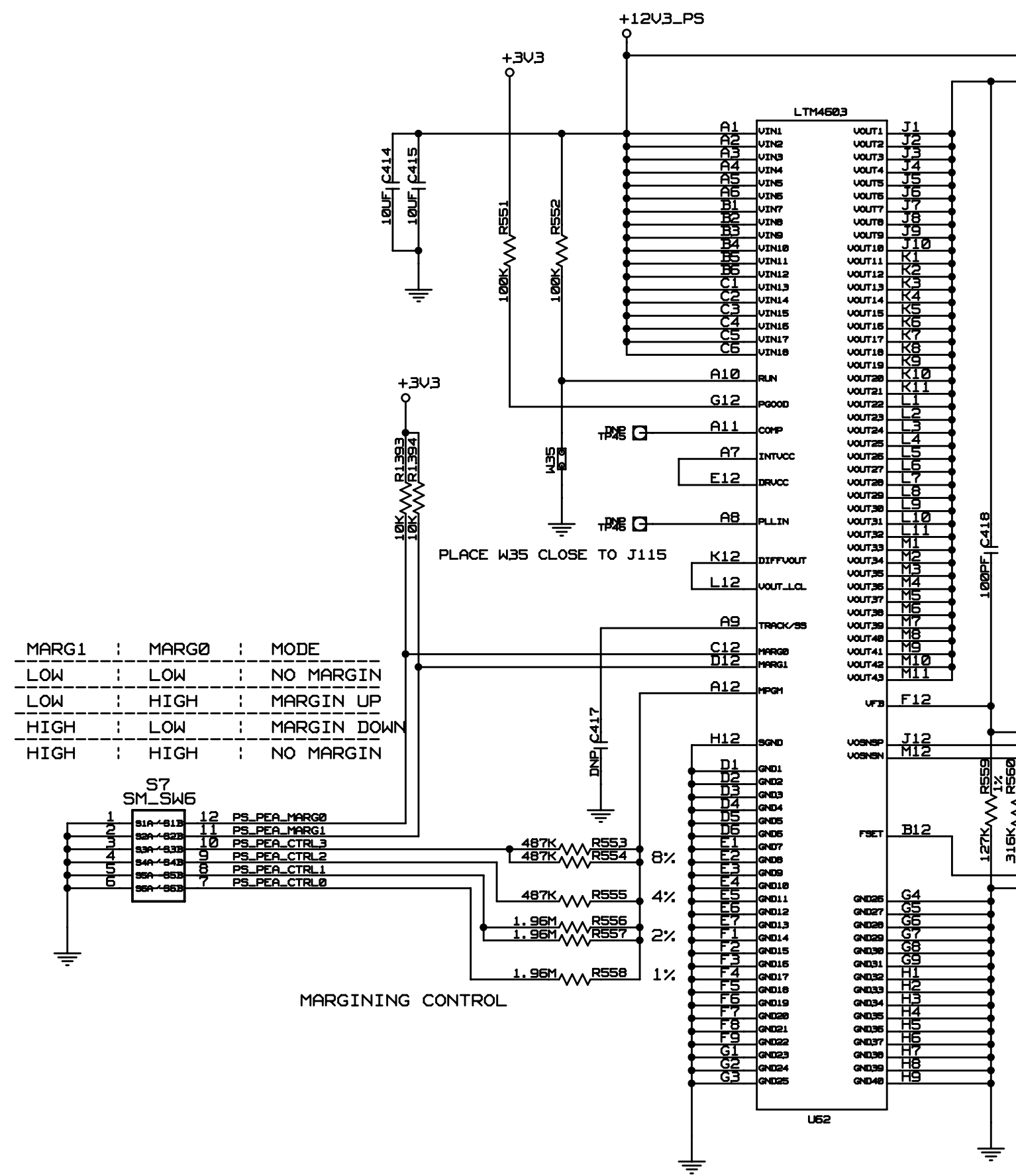
C

B

B

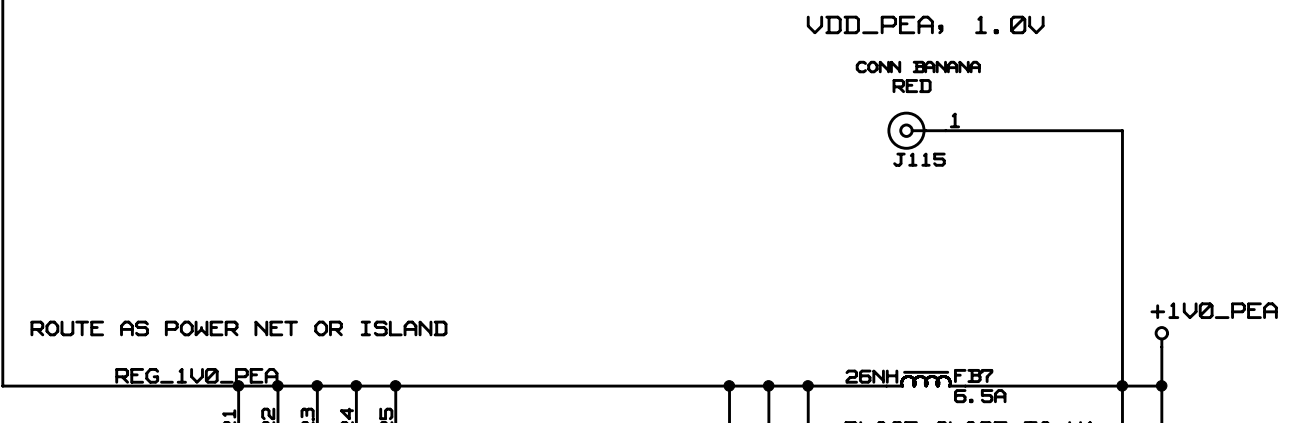
A

A



MARG1	MARG0	MODE
LOW	LOW	NO MARGIN
LOW	HIGH	MARGIN UP
HIGH	LOW	MARGIN DOWN
HIGH	HIGH	NO MARGIN

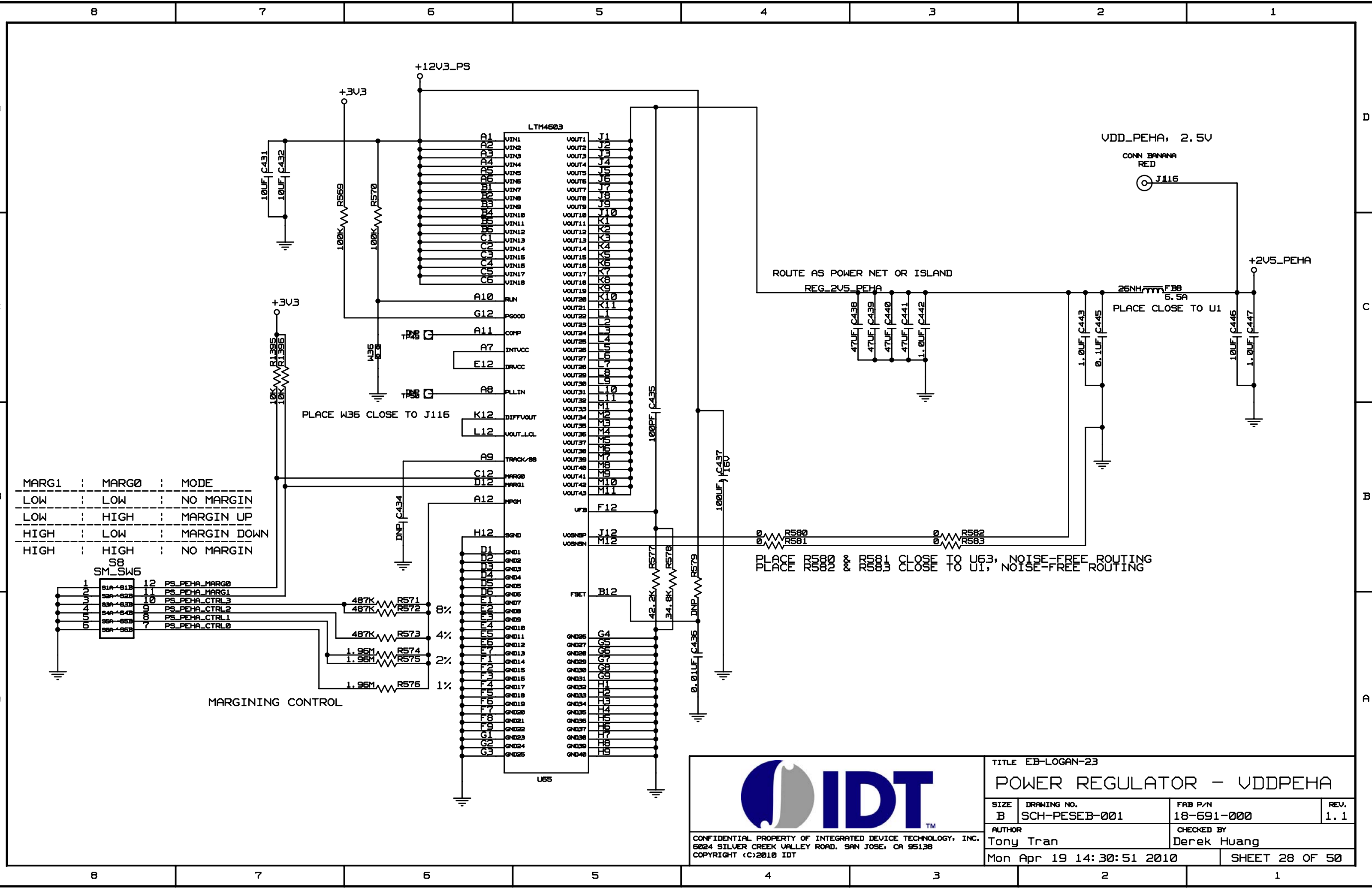
MARGINING CONTROL



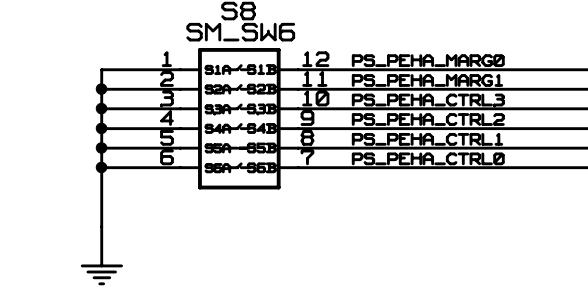
PLACE R562 & R563 CLOSE TO U50, NOISE-FREE ROUTING  
PLACE R564 & R565 CLOSE TO U1, NOISE-FREE ROUTING

**IDT**  
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TITLE EB-LOGAN-23			
POWER REGULATOR - VDDPEA			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:51 2010			SHEET 27 OF 50



MARG1	MARG0	MODE
LOW	LOW	NO MARGIN
LOW	HIGH	MARGIN UP
HIGH	LOW	MARGIN DOWN
HIGH	HIGH	NO MARGIN



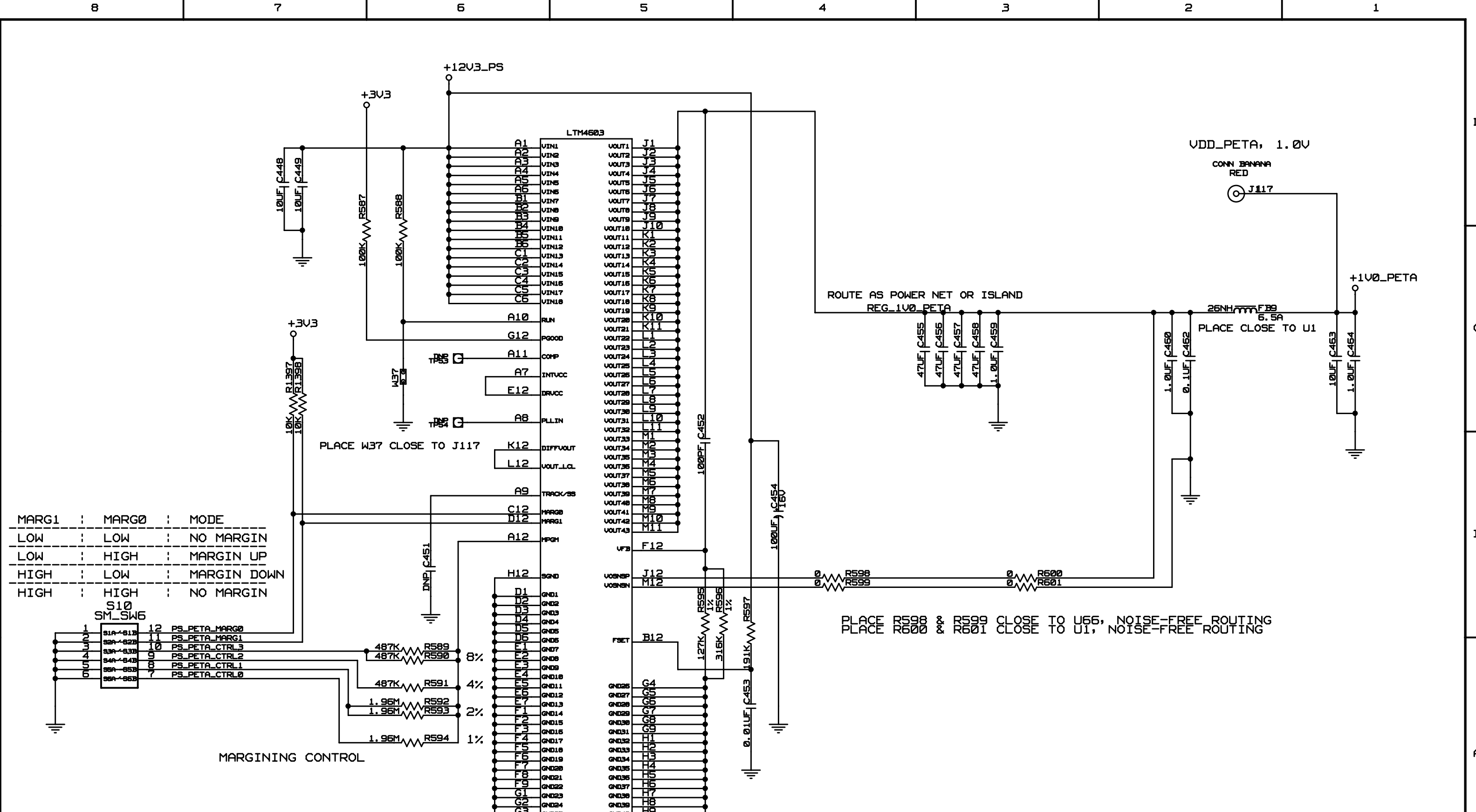
MARGINING CONTROL



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TITLE EB-LOGAN-23			
POWER REGULATOR - VDDPEHA			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:51 2010			SHEET 28 OF 50





MARG1	MARG0	MODE
LOW	LOW	NO MARGIN
LOW	HIGH	MARGIN UP
HIGH	LOW	MARGIN DOWN
HIGH	HIGH	NO MARGIN

SM_SW6	PS_PETA_MARG0	PS_PETA_MARG1	PS_PETA_CTRL3	PS_PETA_CTRL2	PS_PETA_CTRL1	PS_PETA_CTRL0
81A-81B	12	11	10	9	8	7
82A-82B						
83A-83B						
84A-84B						
85A-85B						
86A-86B						

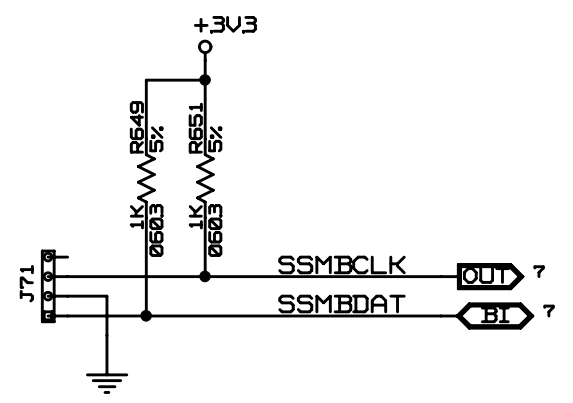
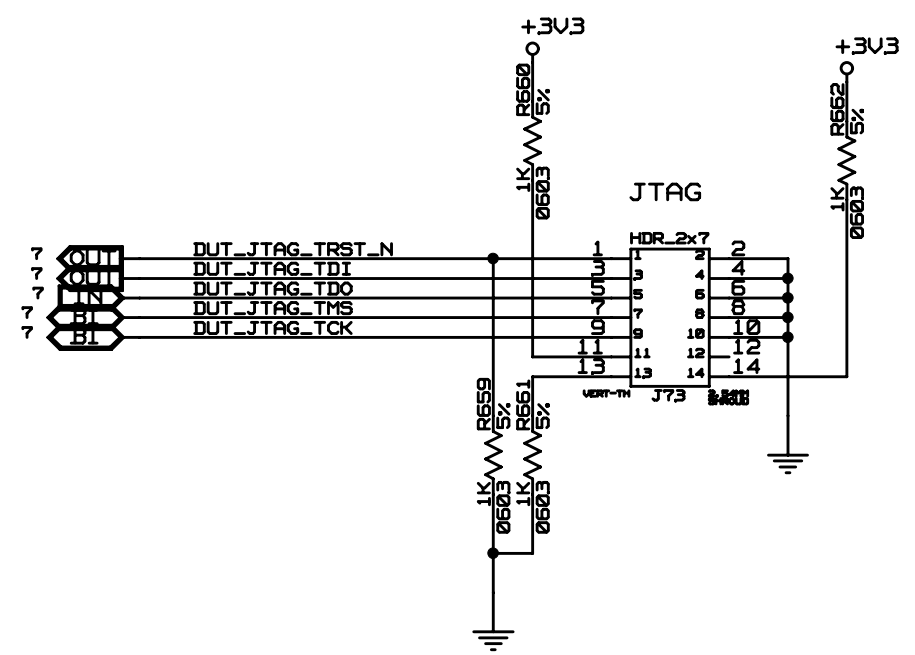
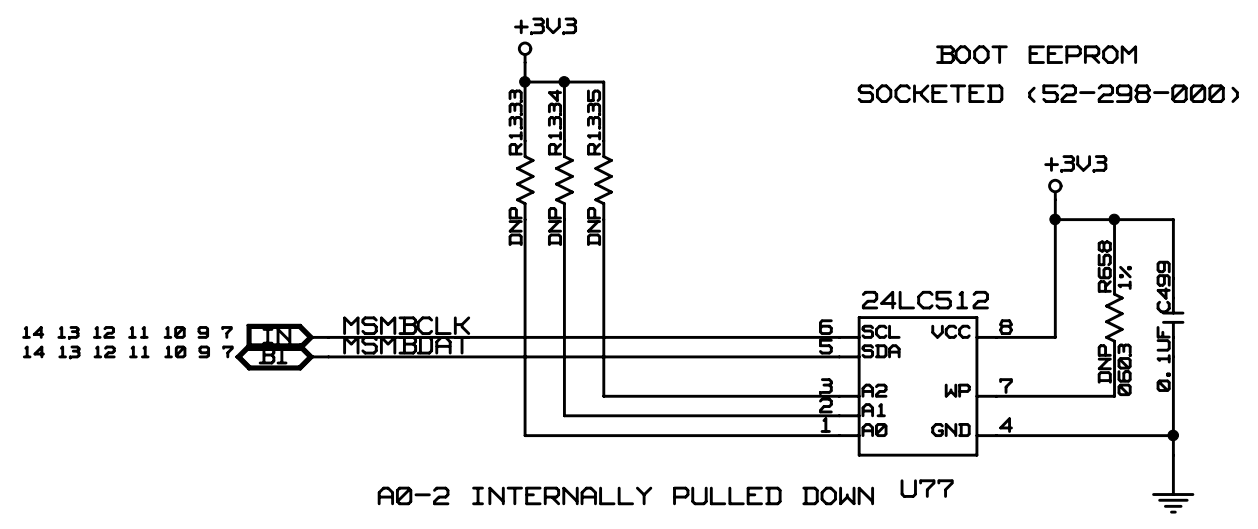
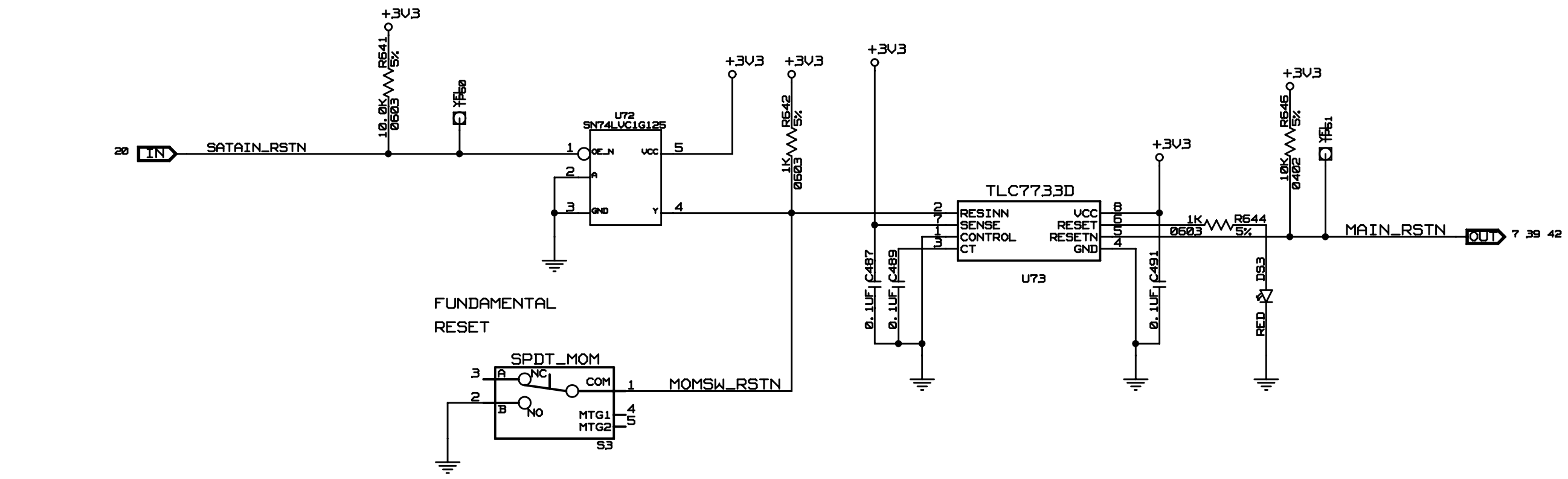
MARGINING CONTROL

PLACE R598 & R599 CLOSE TO U66, NOISE-FREE ROUTING  
PLACE R600 & R601 CLOSE TO U1, NOISE-FREE ROUTING



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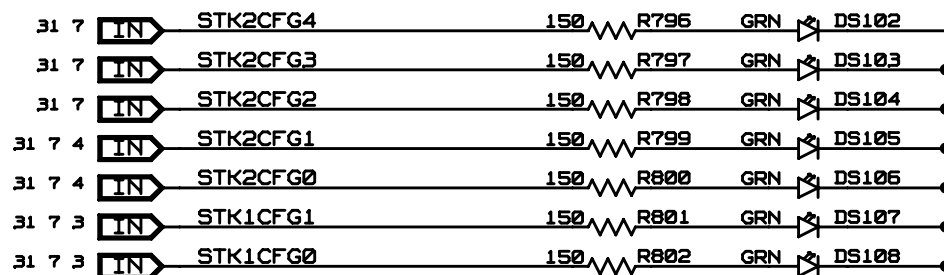
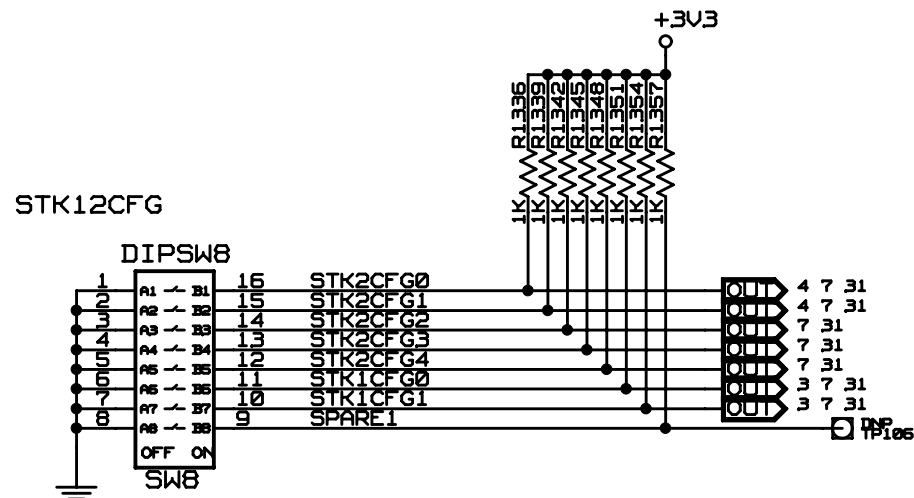
TITLE EB-LOGAN-23			
POWER REGULATOR - VDDPETA			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:52 2010			SHEET 29 OF 50



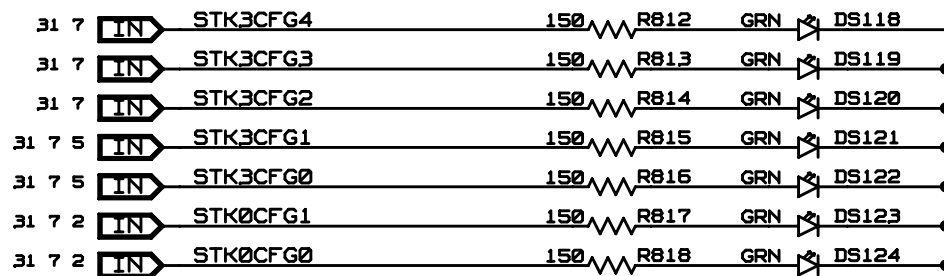
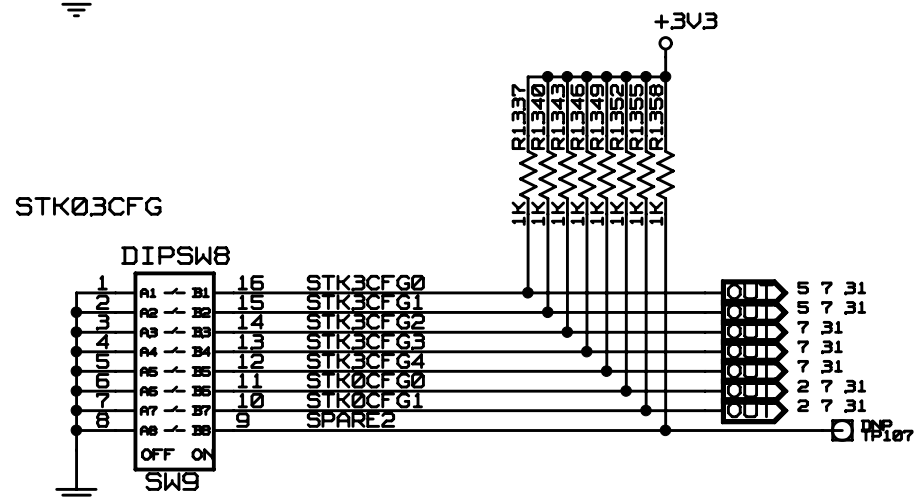
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TITLE EB-LOGAN-23			
RESET, SMBUS, EEPROM, JTAG			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:52 2010			SHEET 30 OF 50

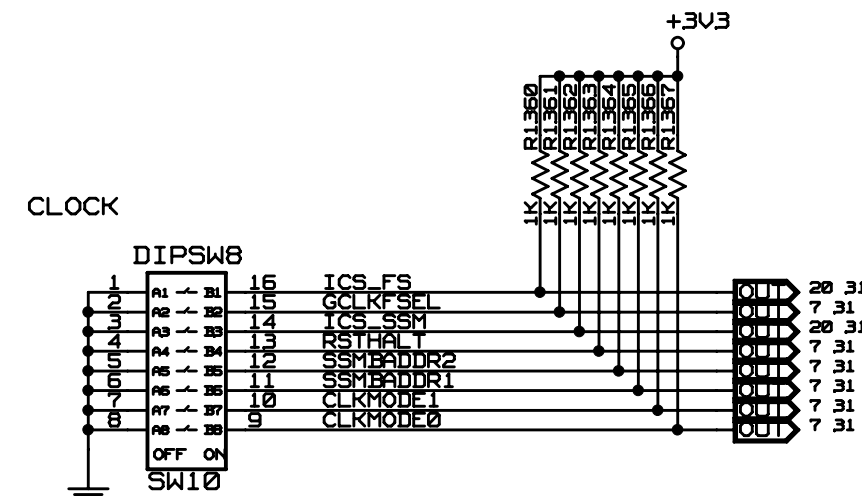
(GREEN) ACTIVE HIGH - DIP STK12CFG



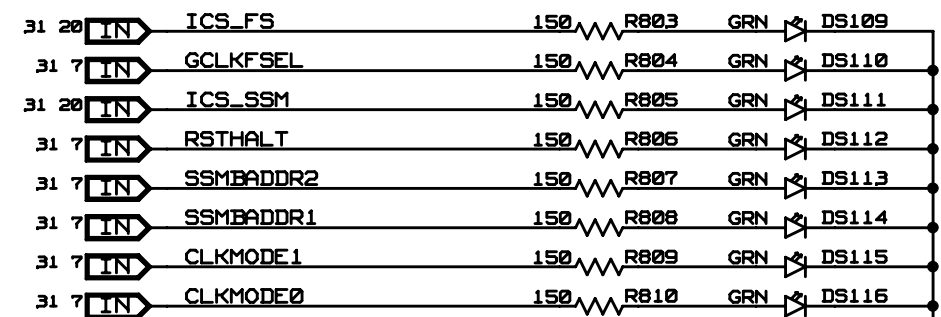
(GREEN) ACTIVE HIGH - DIP STK03CFG



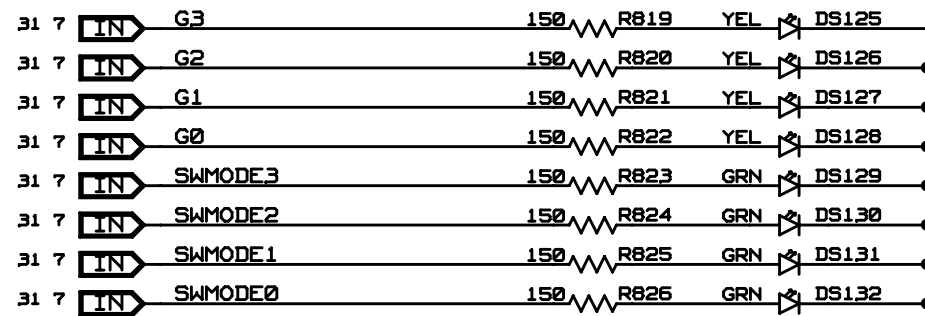
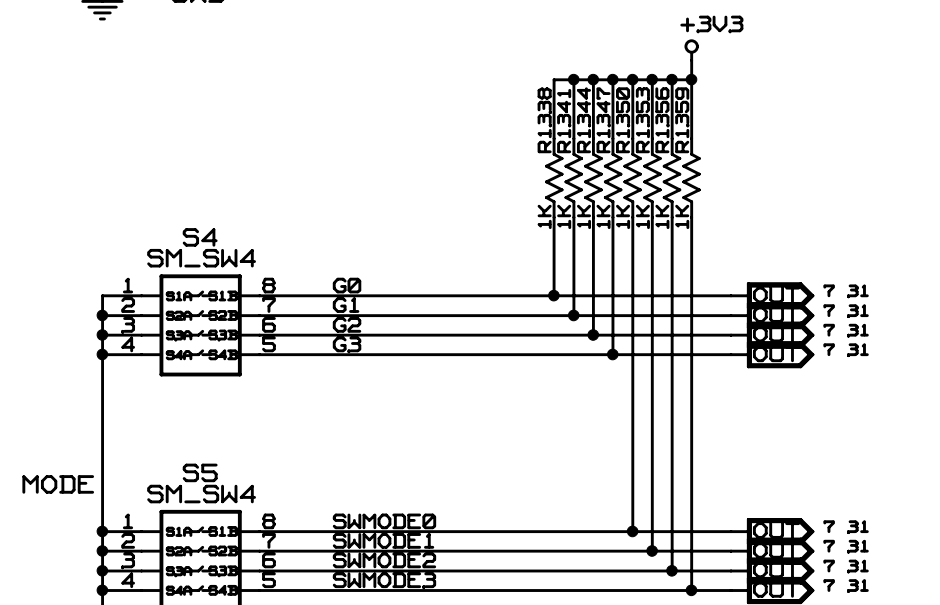
CLOCK



(GREEN) ACTIVE HIGH - DIP CLOCK



(GREEN) ACTIVE HIGH - DIP MODE

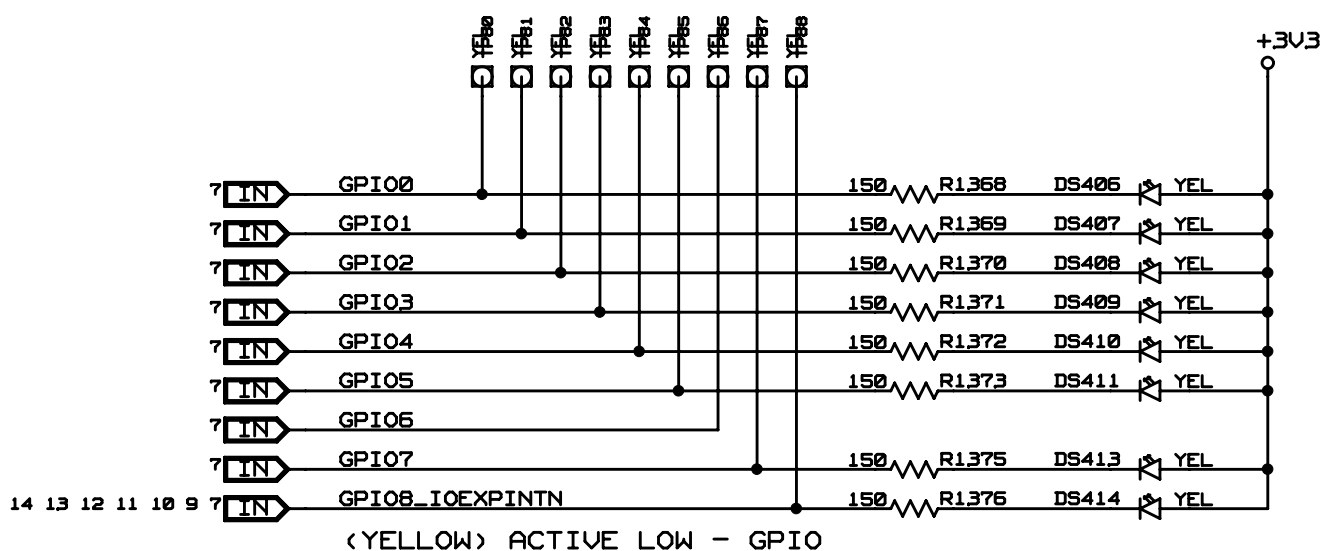


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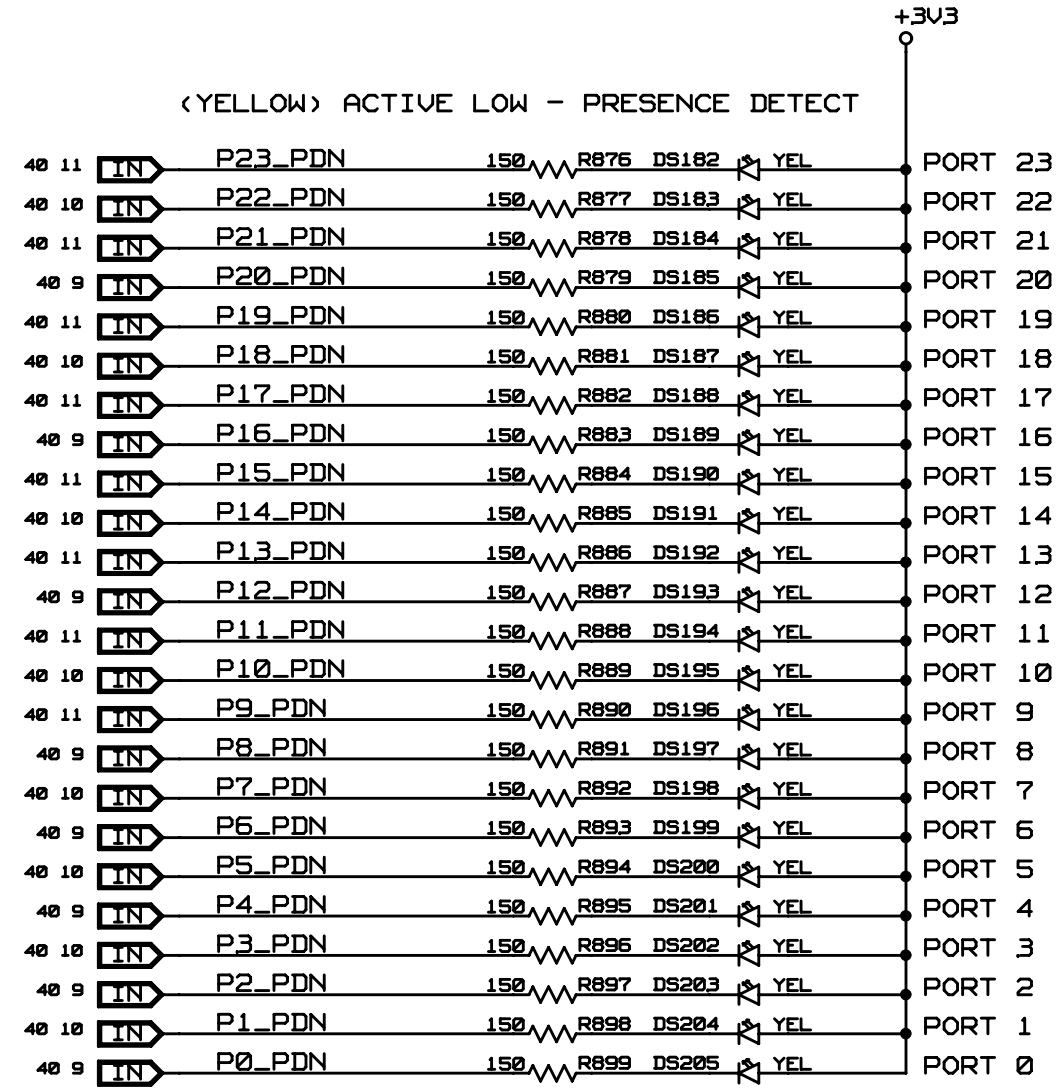
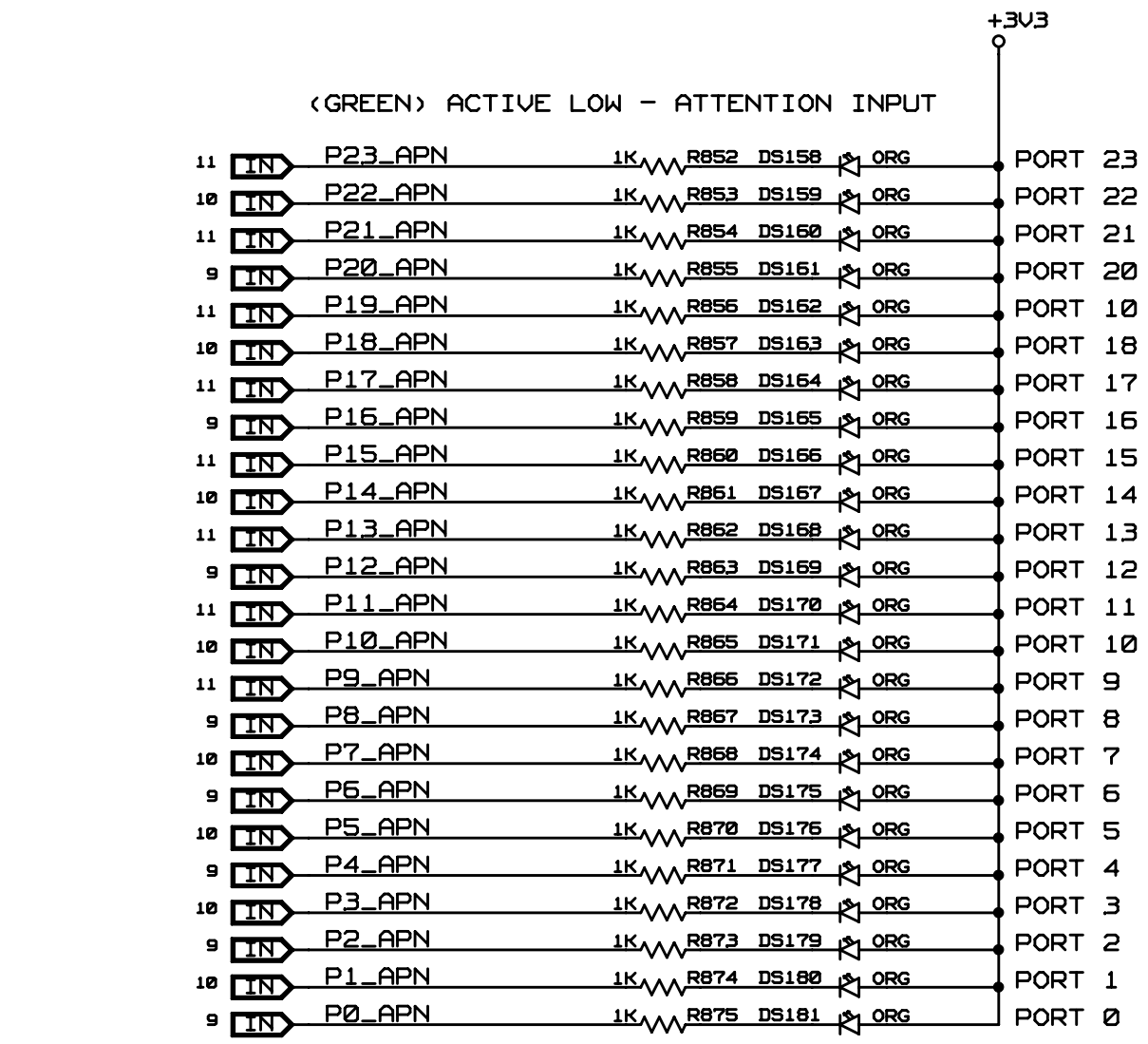
TITLE EB-LOGAN-23

DIP SWITCHES

SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:52 2010			SHEET 31 OF 50

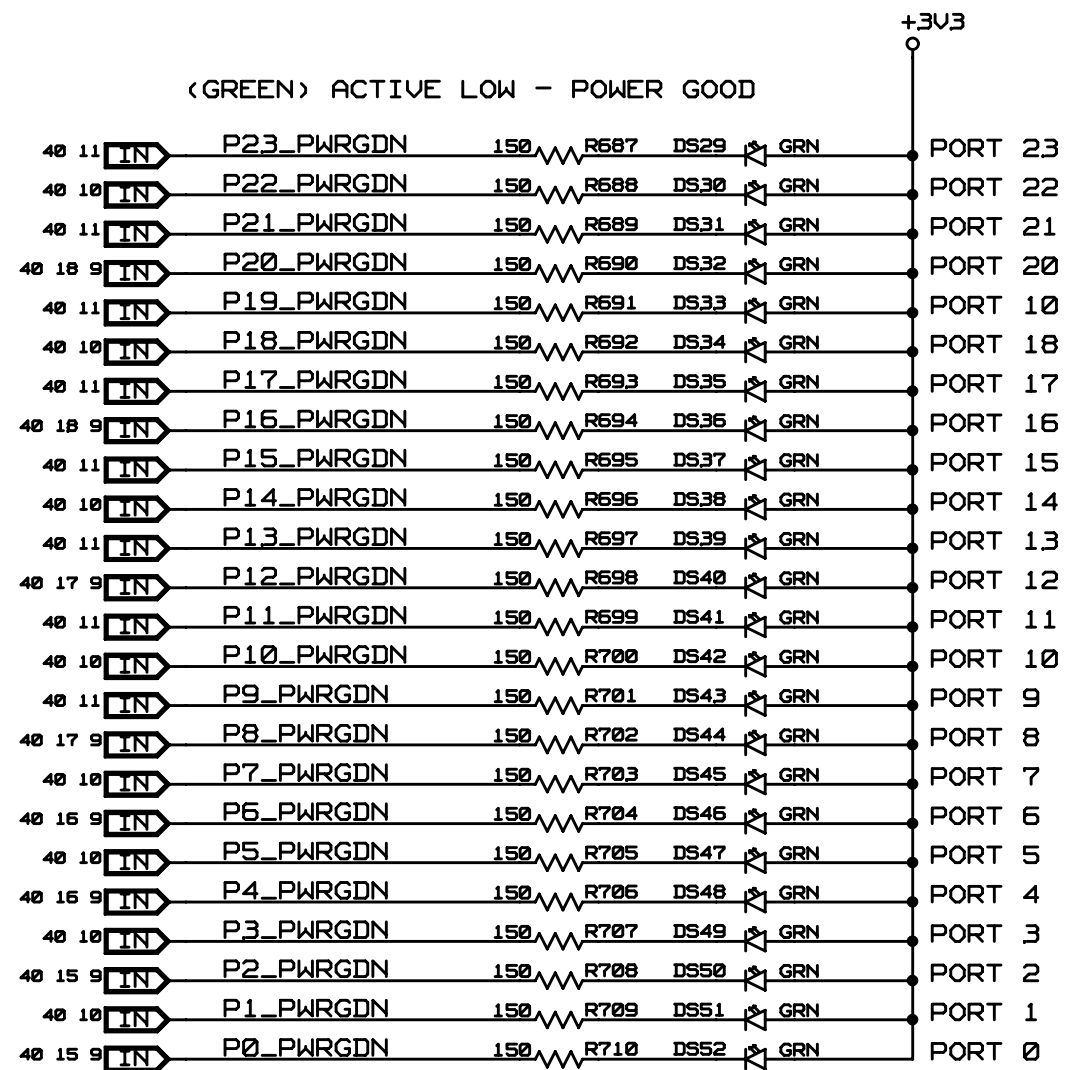
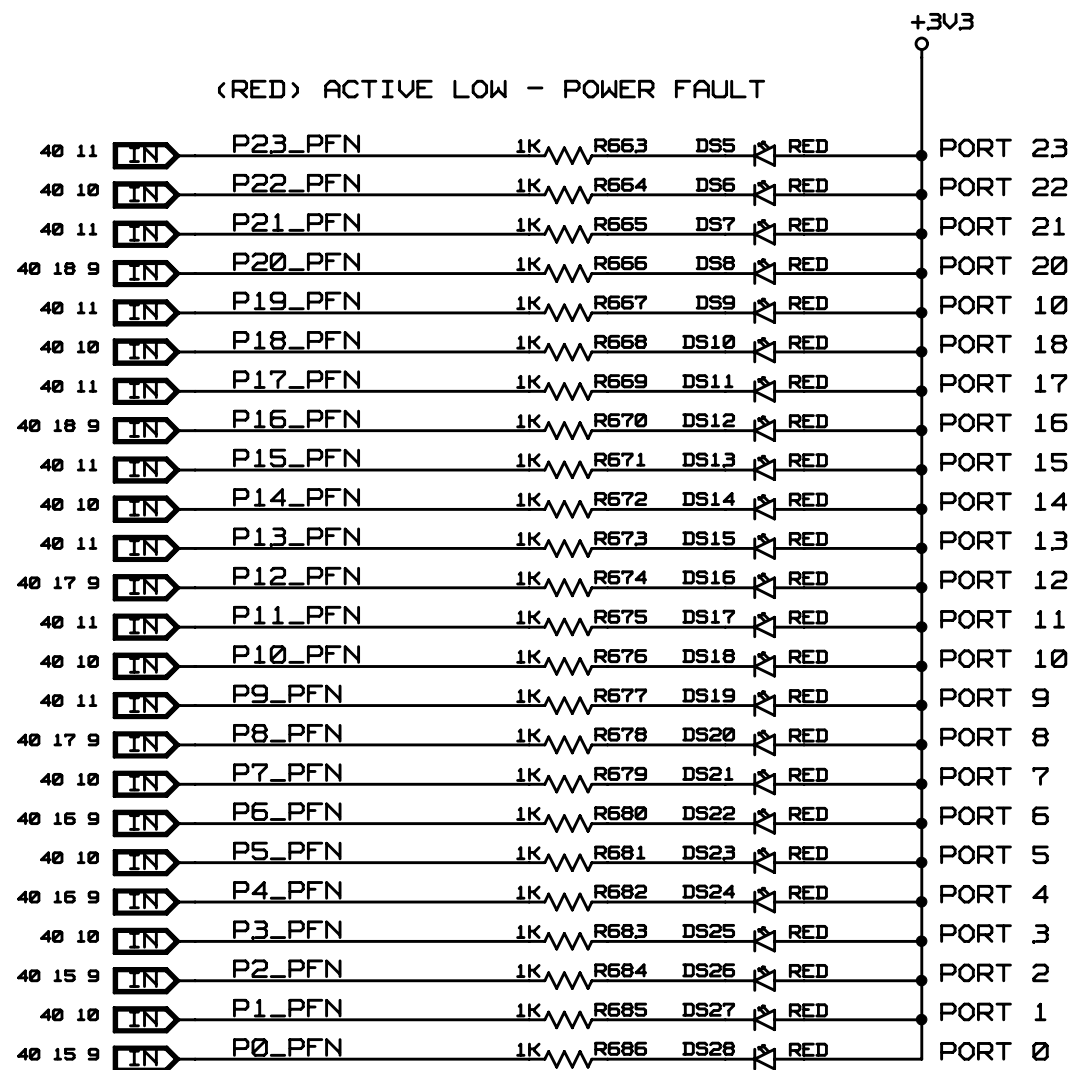


GPIO	ALT0	ALT1
0	PART0PERSTN	P16LINKUPN
1	PART1PERSTN	P16ACTIVEN
2	PART2PERSTN	P4LINKUPN
3	PART3PERSTN	P4ACTIVEN
4	FAILOVER0	P0LINKUPN
5	GPEN	P0ACTIVEN
7	FAILOVER2	P8LINKUPN
8	IOEXPINTN	P8ACTIVEN



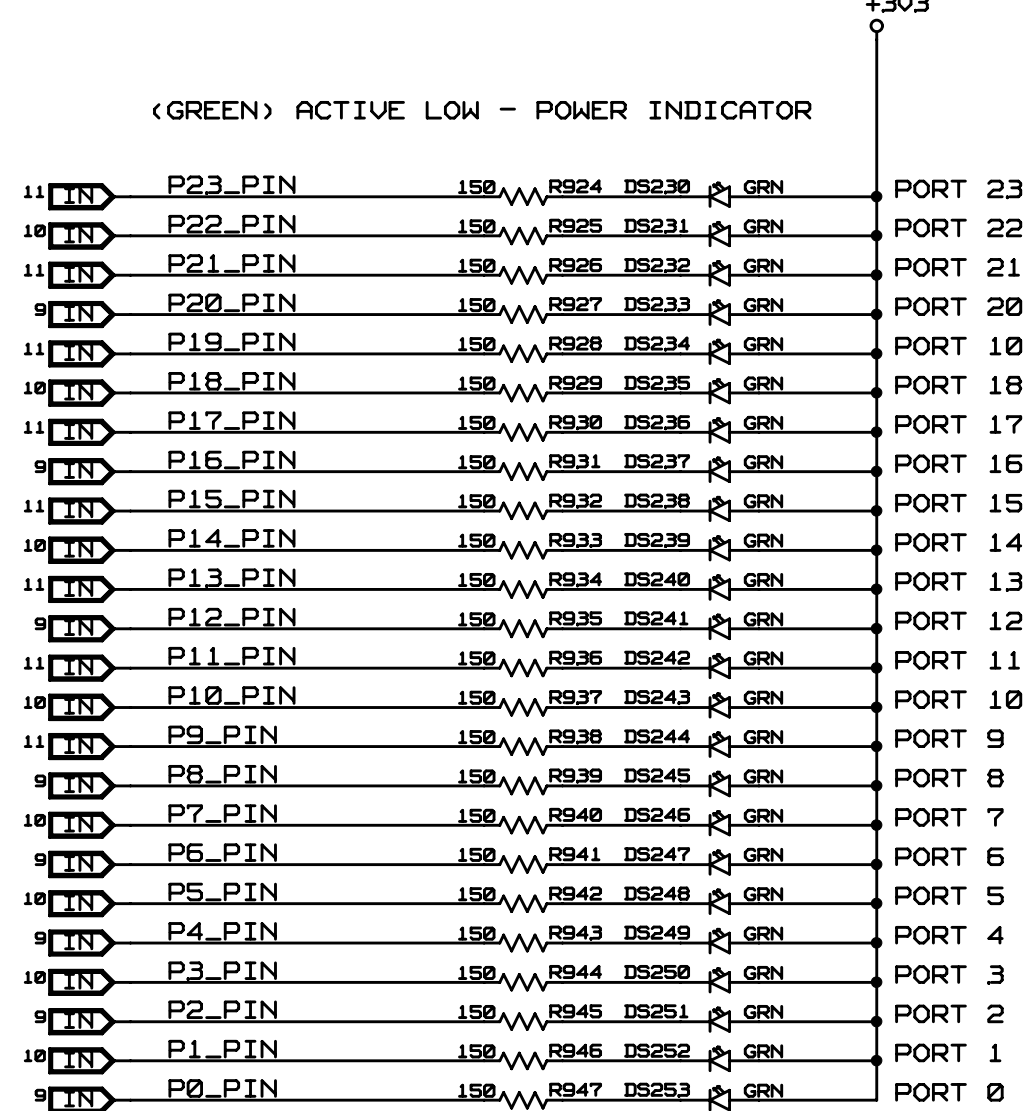
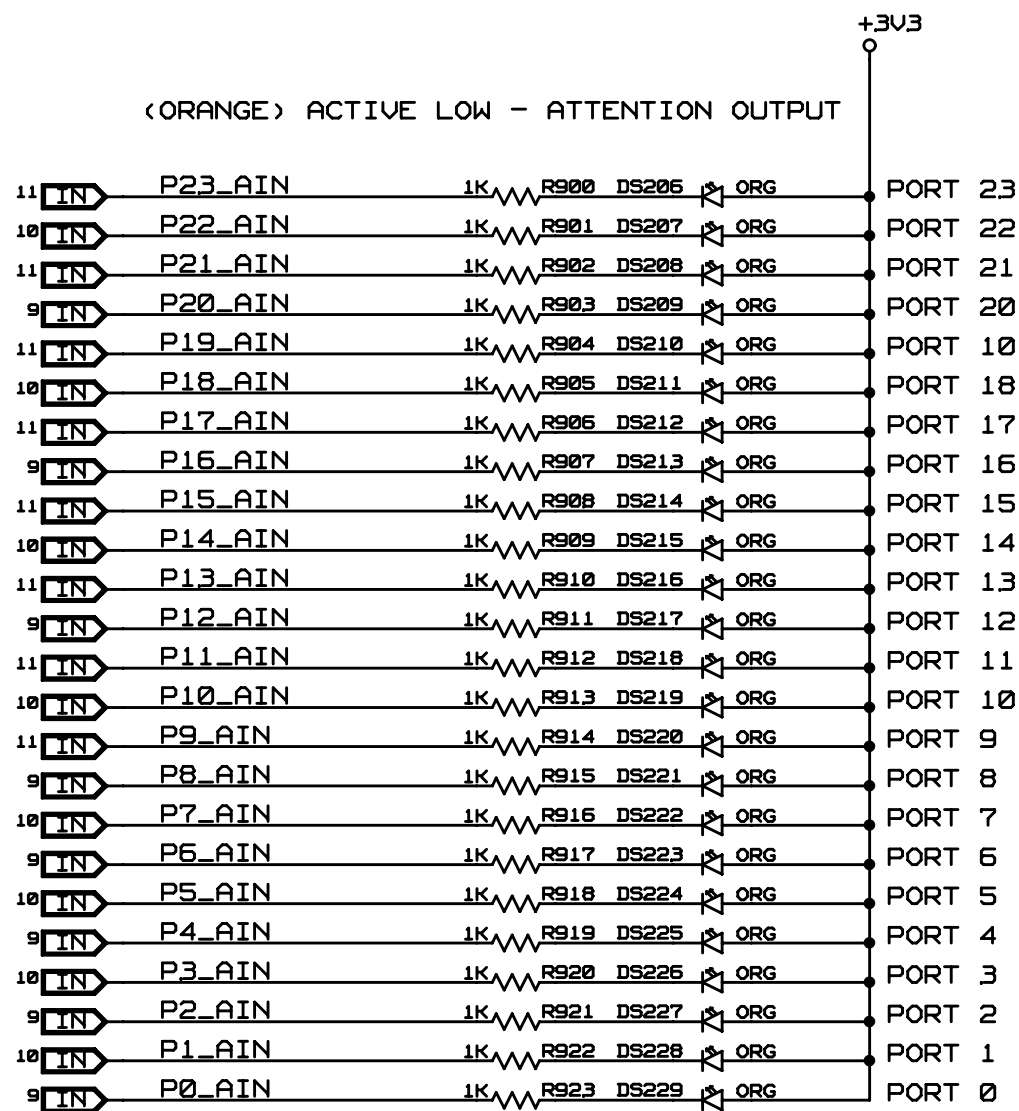
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TITLE EB-LOGAN-23			
LED - PORT STATUS (1 OF 7)			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:52 2010			SHEET 32 OF 50



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TITLE EB-LOGAN-23			
LED - PORT STATUS (2 OF 7)			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:53 2010			SHEET 33 OF 50



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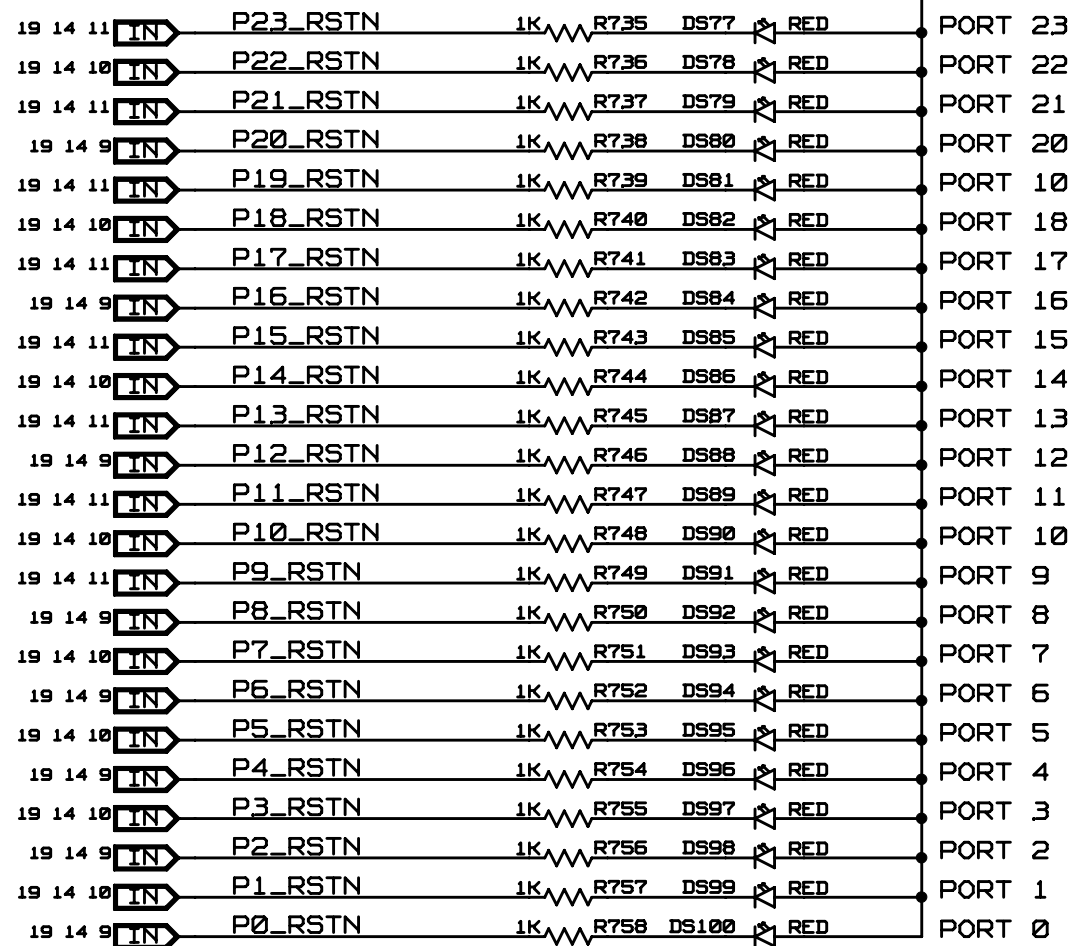
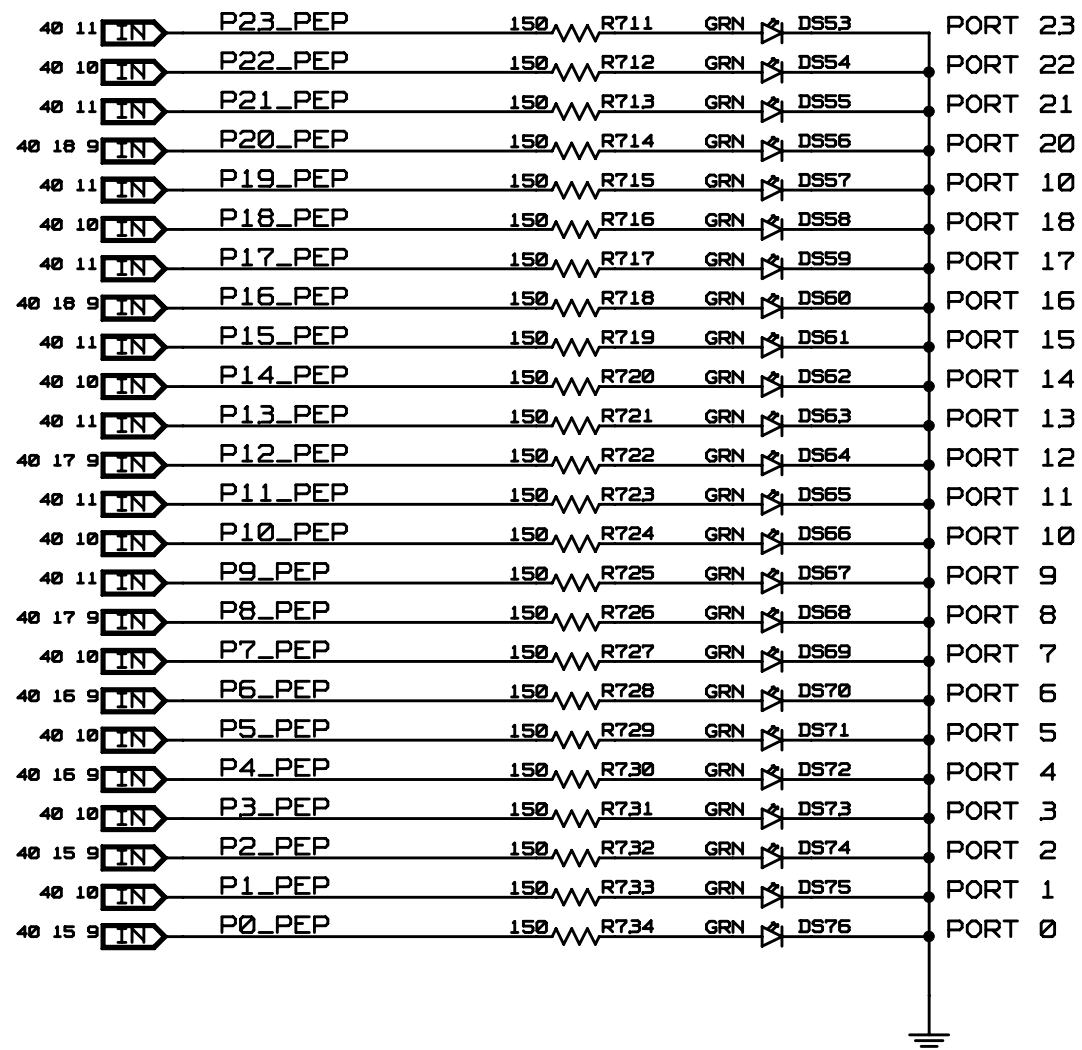
TITLE EB-LOGAN-23			
LED - PORT STATUS (3 OF 7)			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
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<GREEN> ACTIVE HIGH - POWER ENABLE

<RED> ACTIVE LOW - HP SLOT RST

+3V3



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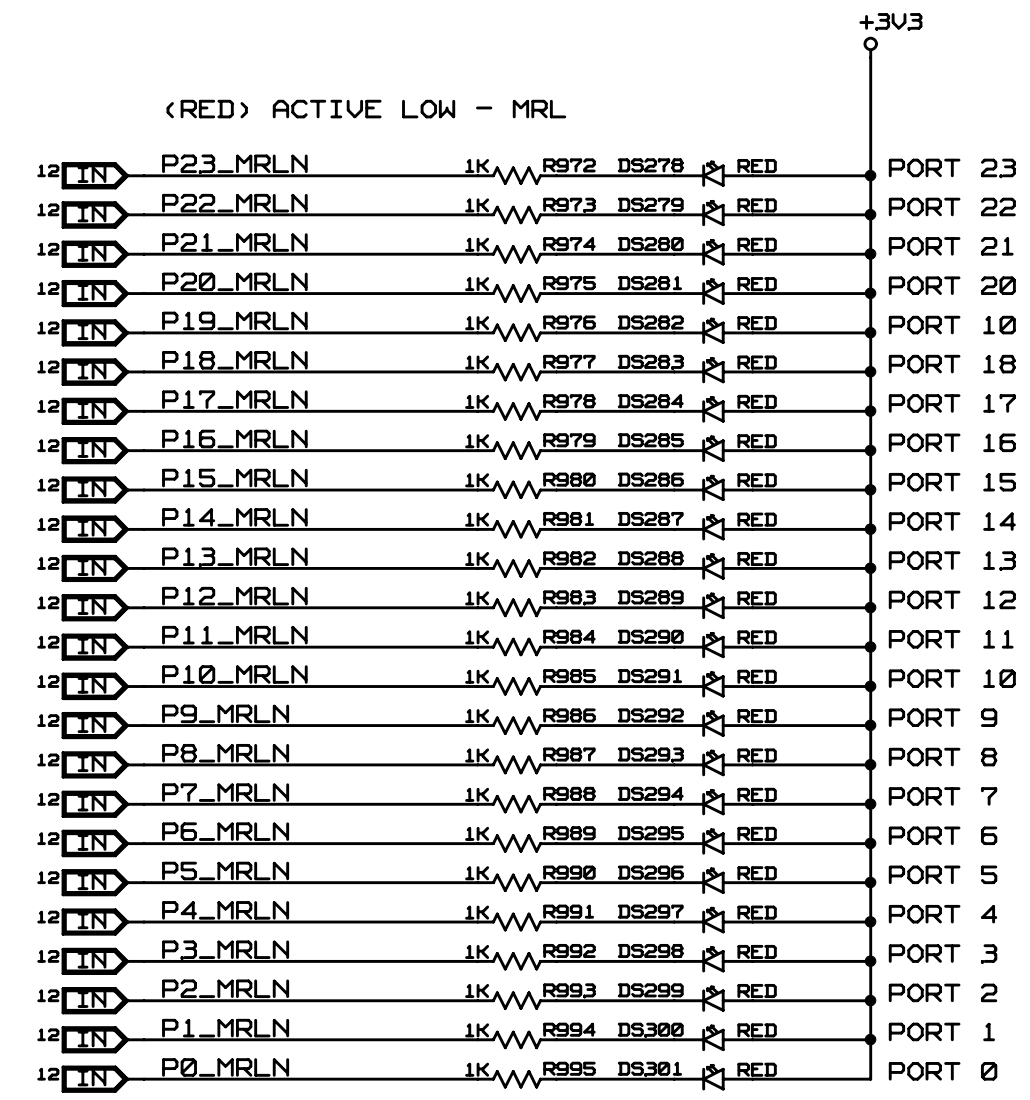
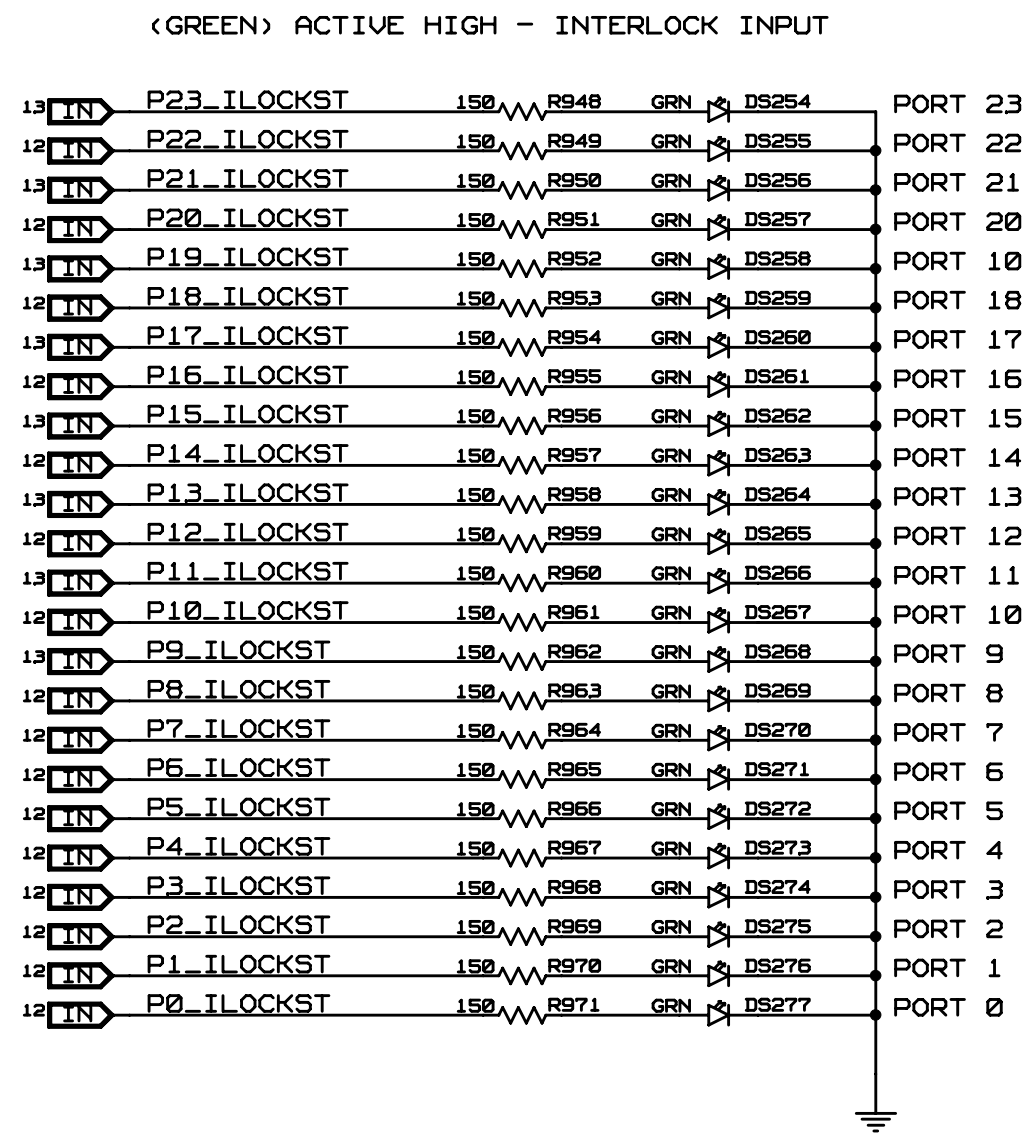
TITLE EB-LOGAN-23

LED - PORT STATUS < 4 OF 7 >

SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
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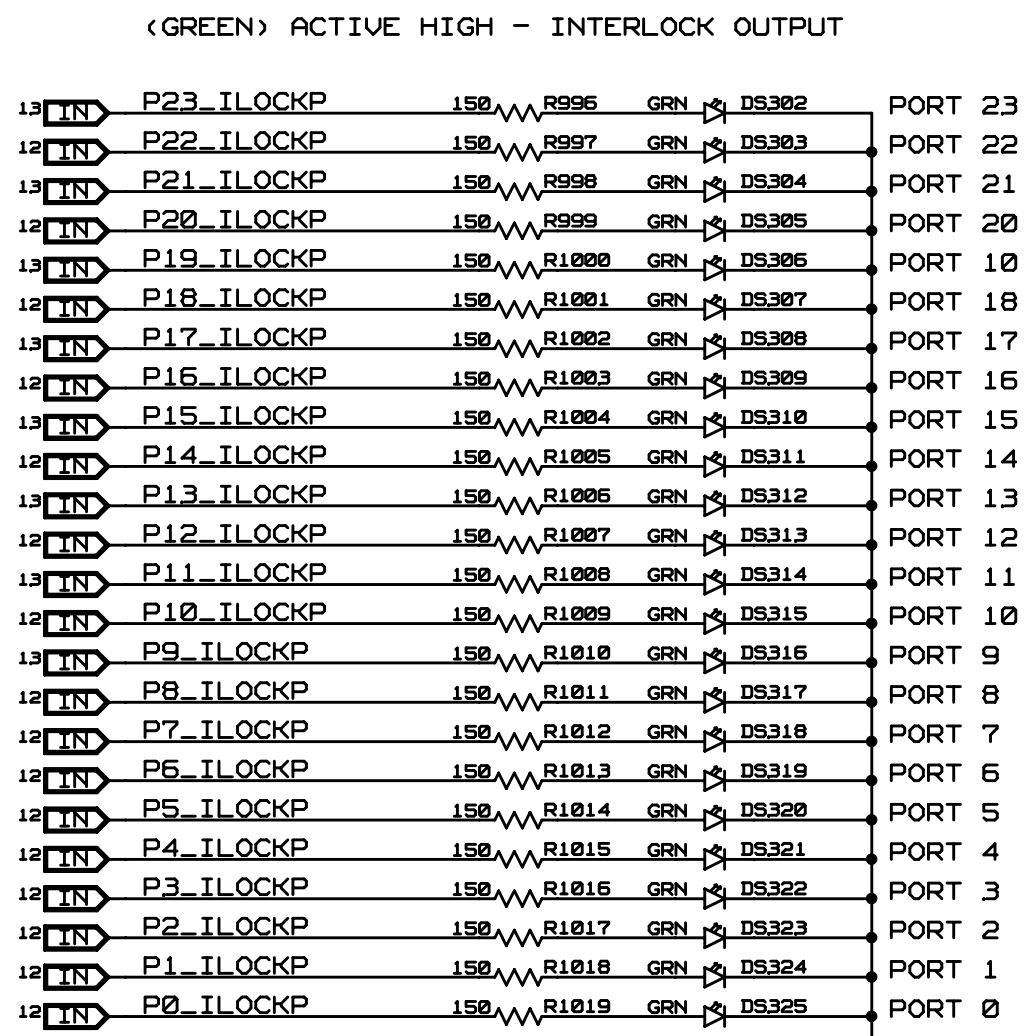
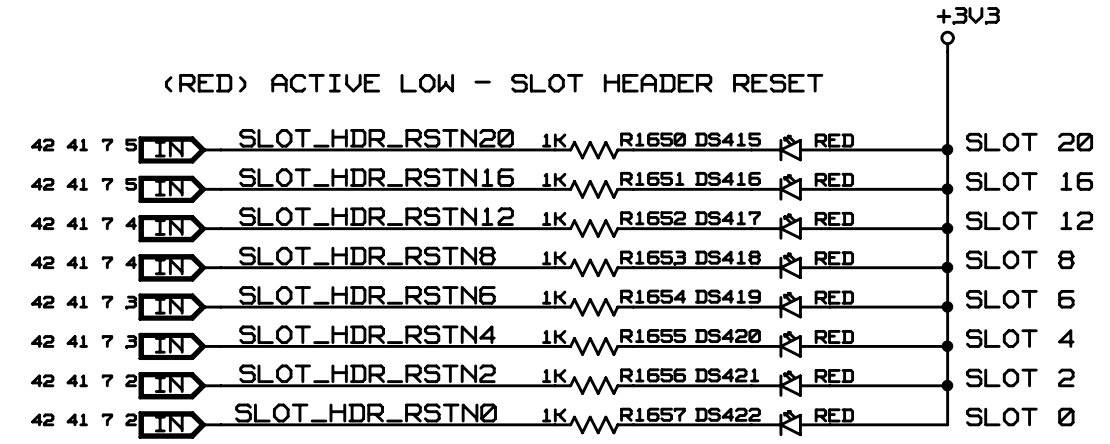
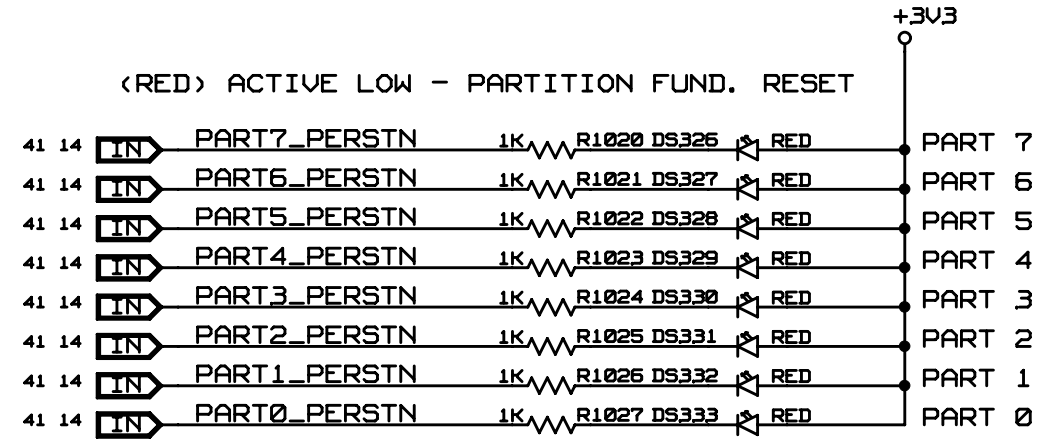
AUTHOR Tony Tran	CHECKED BY Derek Huang
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Mon Apr 19 14:30:53 2010 SHEET 35 OF 50



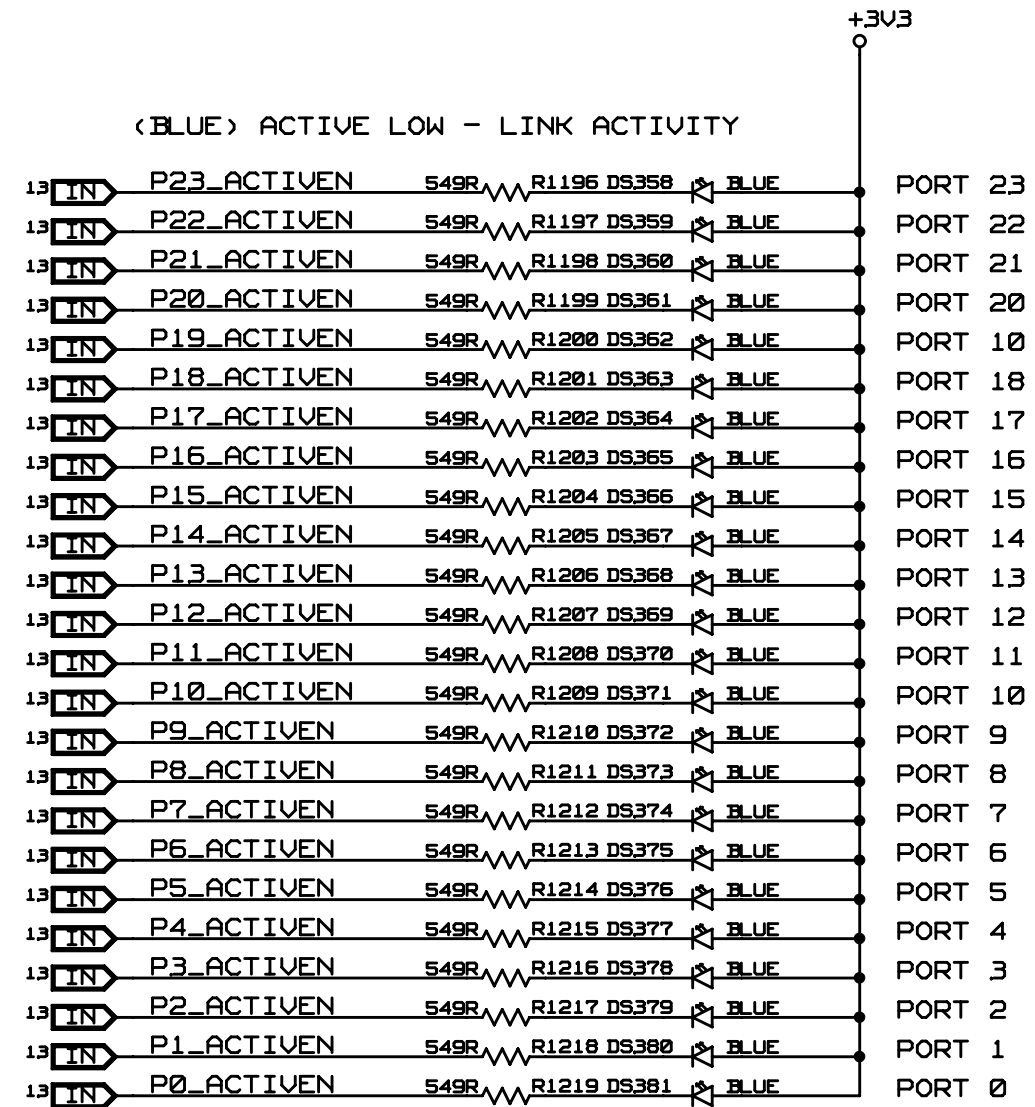
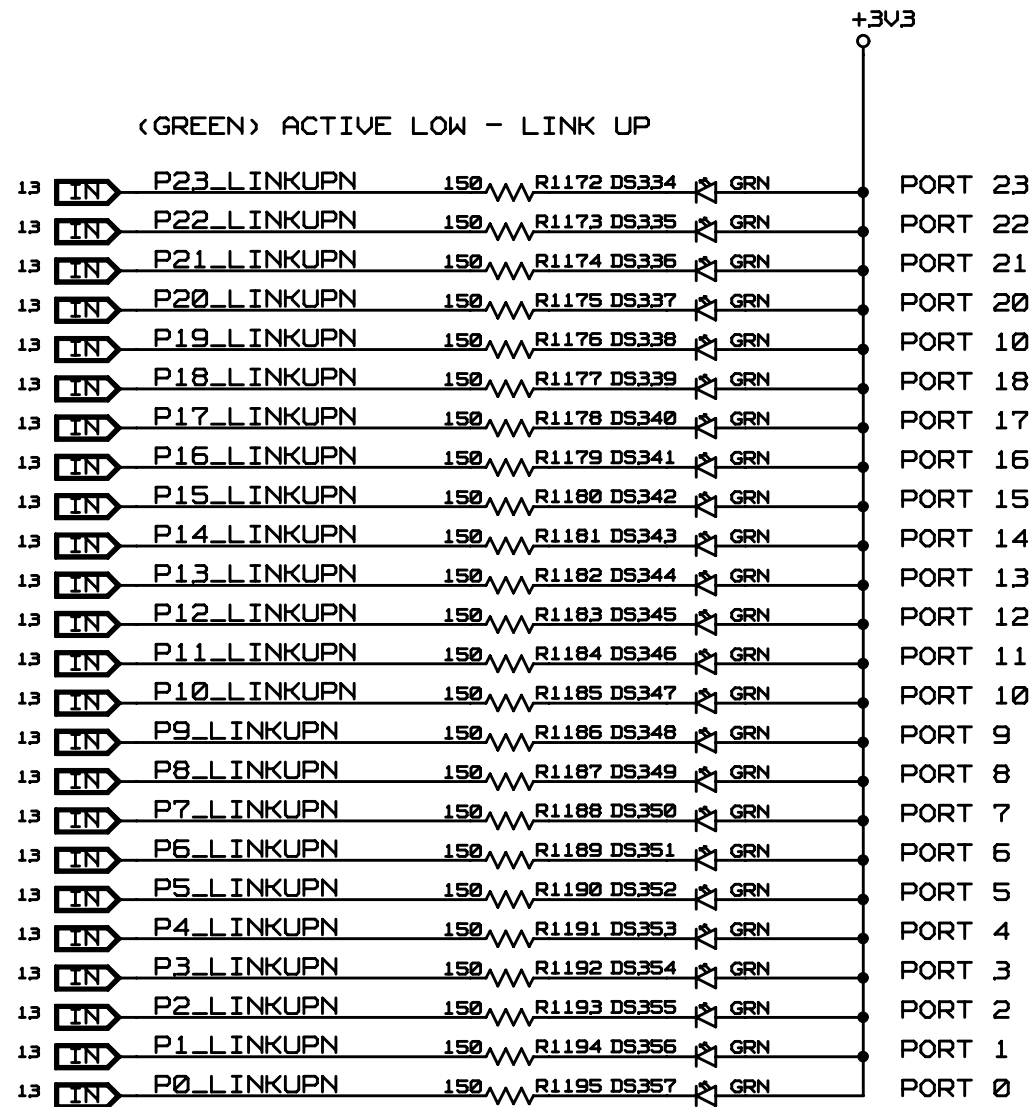
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TITLE EB-LOGAN-23			
LED - PORT STATUS (5 OF 7)			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:54 2010			SHEET 36 OF 50



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TITLE EB-LOGAN-23			
LED - PORT STATUS (6 OF 7)			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:54 2010			SHEET 37 OF 50

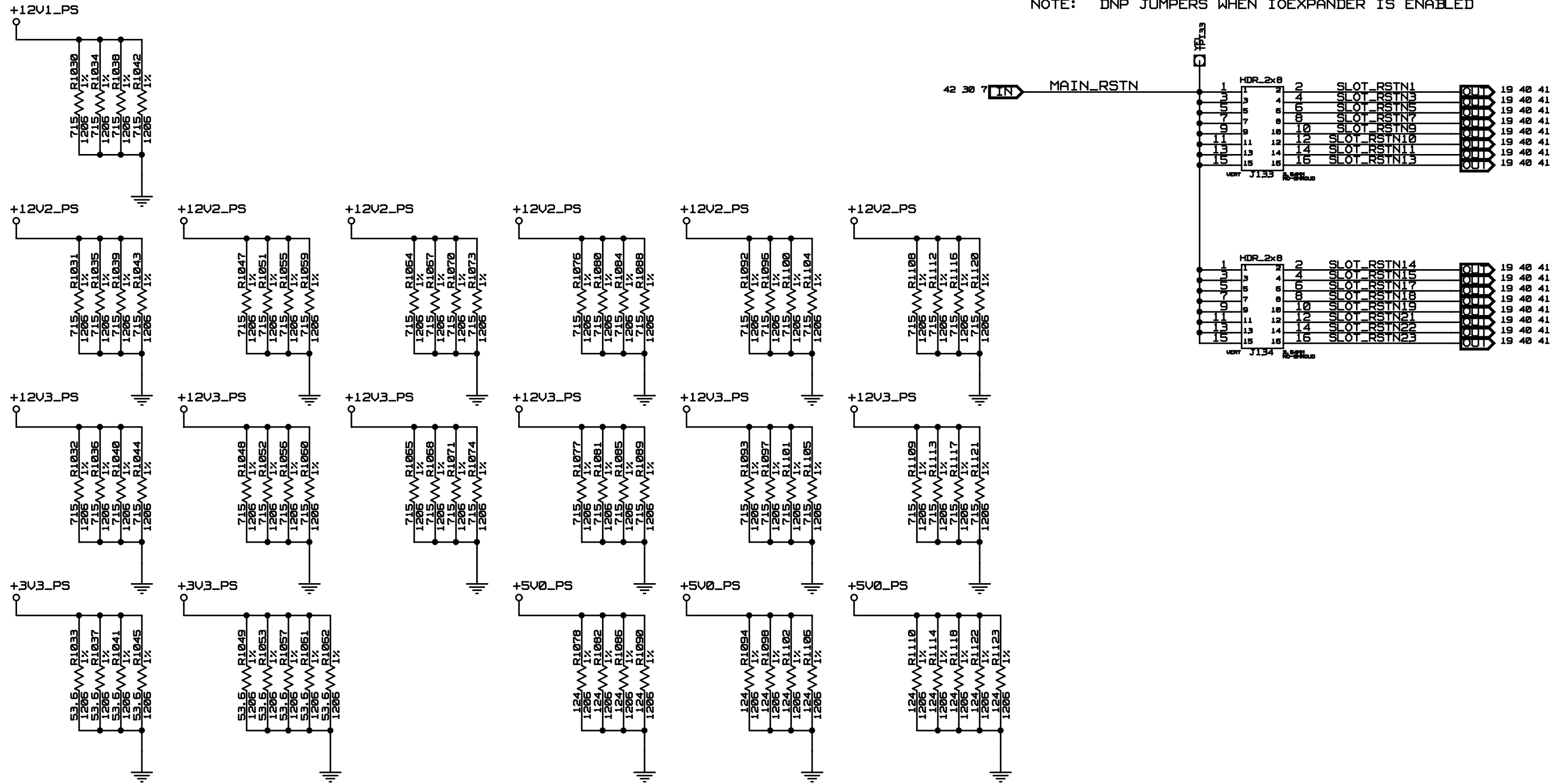


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TITLE EB-LOGAN-23			
LED - PORT STATUS (7 OF 7)			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:54 2010			SHEET 38 OF 50

# MINIMUM POWER SUPPLY LOADS

NOTE: DNP JUMPERS WHEN IOEXPANDER IS ENABLED

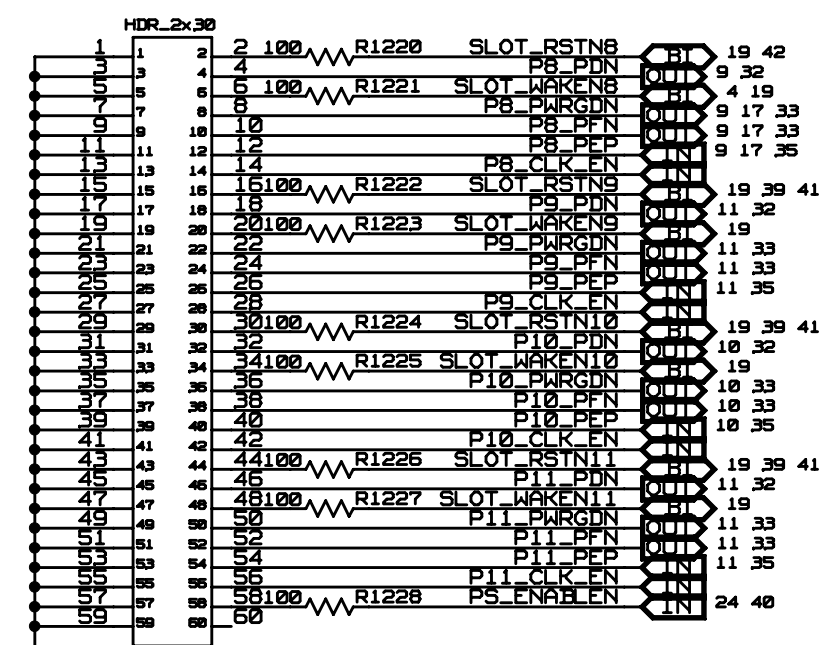


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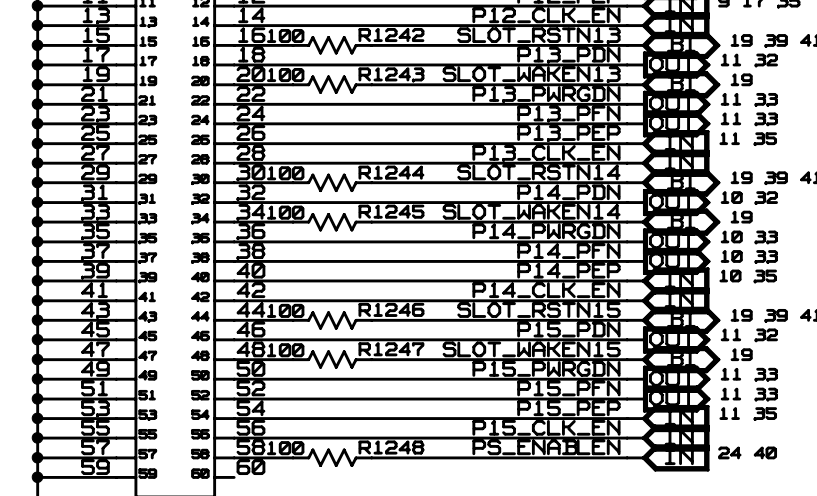
TITLE EB-LOGAN-23			
MIN LOAD RESISTORS			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
Mon Apr 19 14:30:59 2010			SHEET 39 OF 50



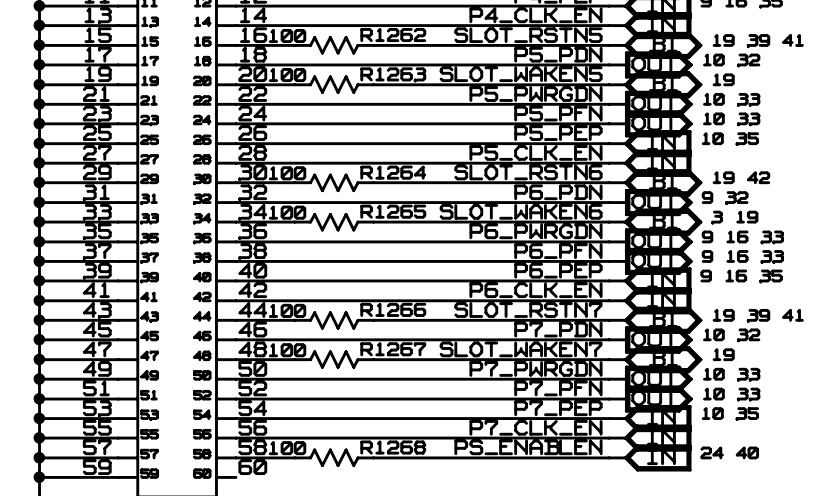
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CARD\_PRESENTN  
WAKEN  
POWERGOOD  
PWR\_FLTN  
PWR\_ENABLE  
CLOCK\_ENABLEN



PORTS 8, 9, 10, 11

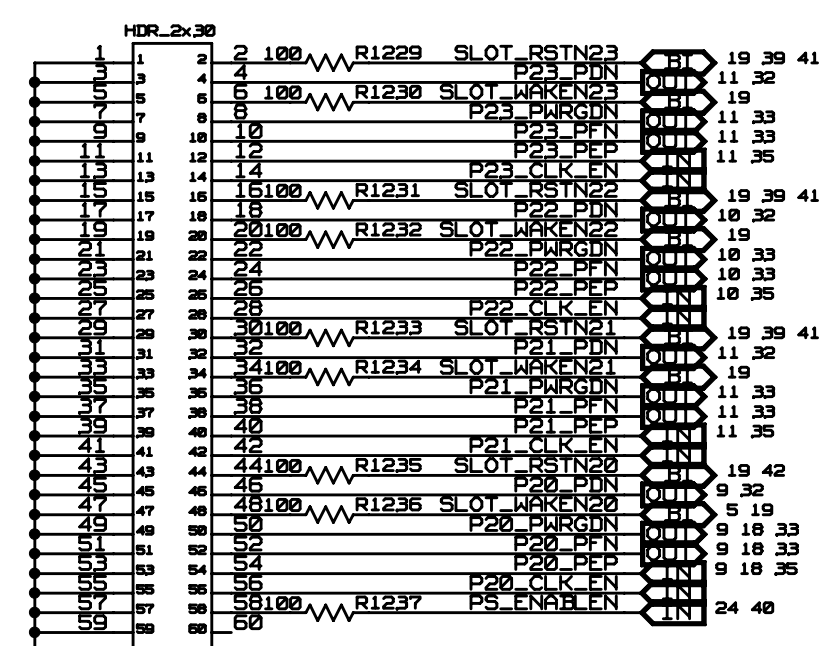


PORTS 12, 13, 14, 15

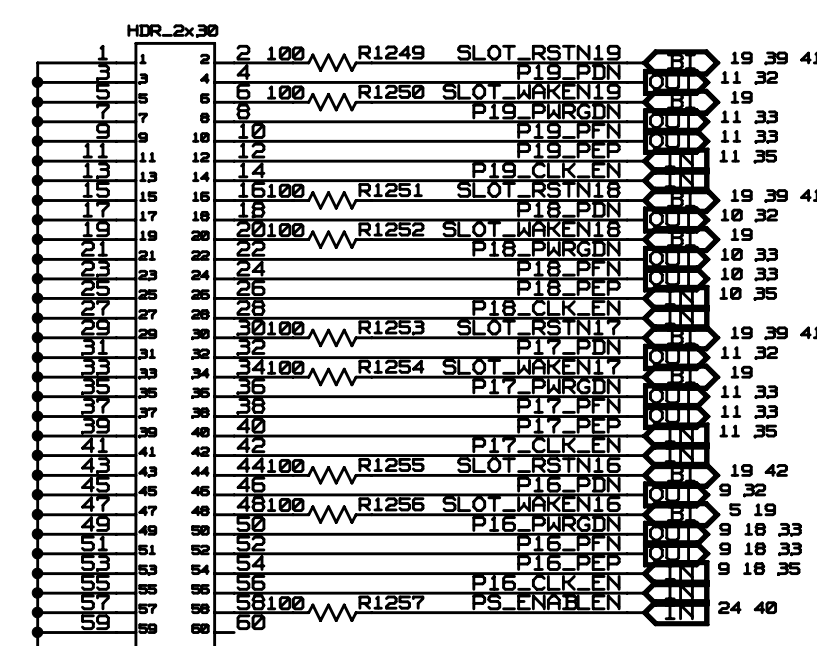


PORTS 4, 5, 6, 7

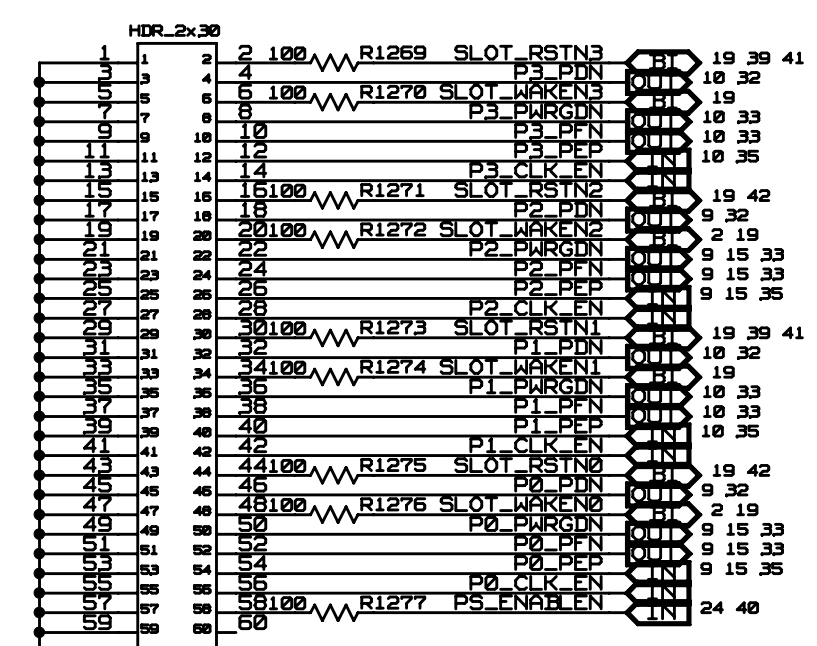
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CARD\_PRESENTN  
WAKEN  
POWERGOOD  
PWR\_FLTN  
PWR\_ENABLE  
CLOCK\_ENABLEN



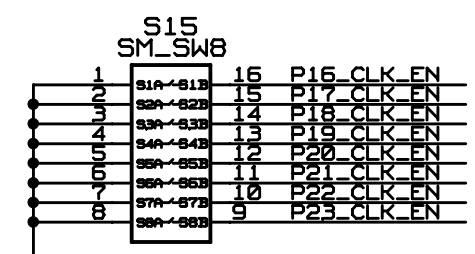
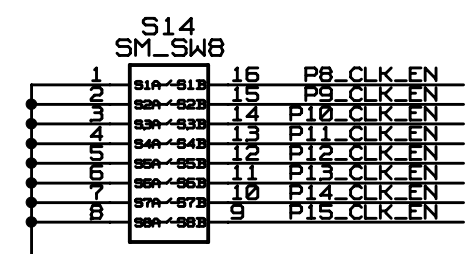
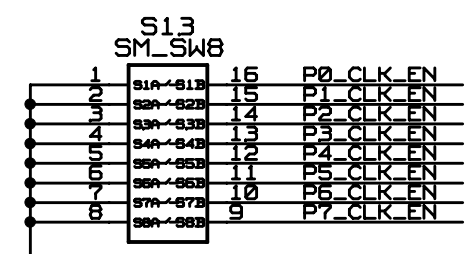
PORTS 23, 22, 21, 20



PORTS 19, 18, 17, 16



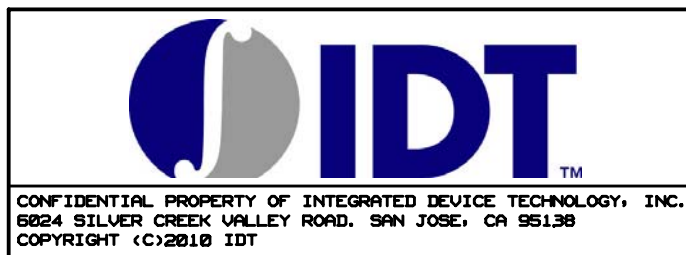
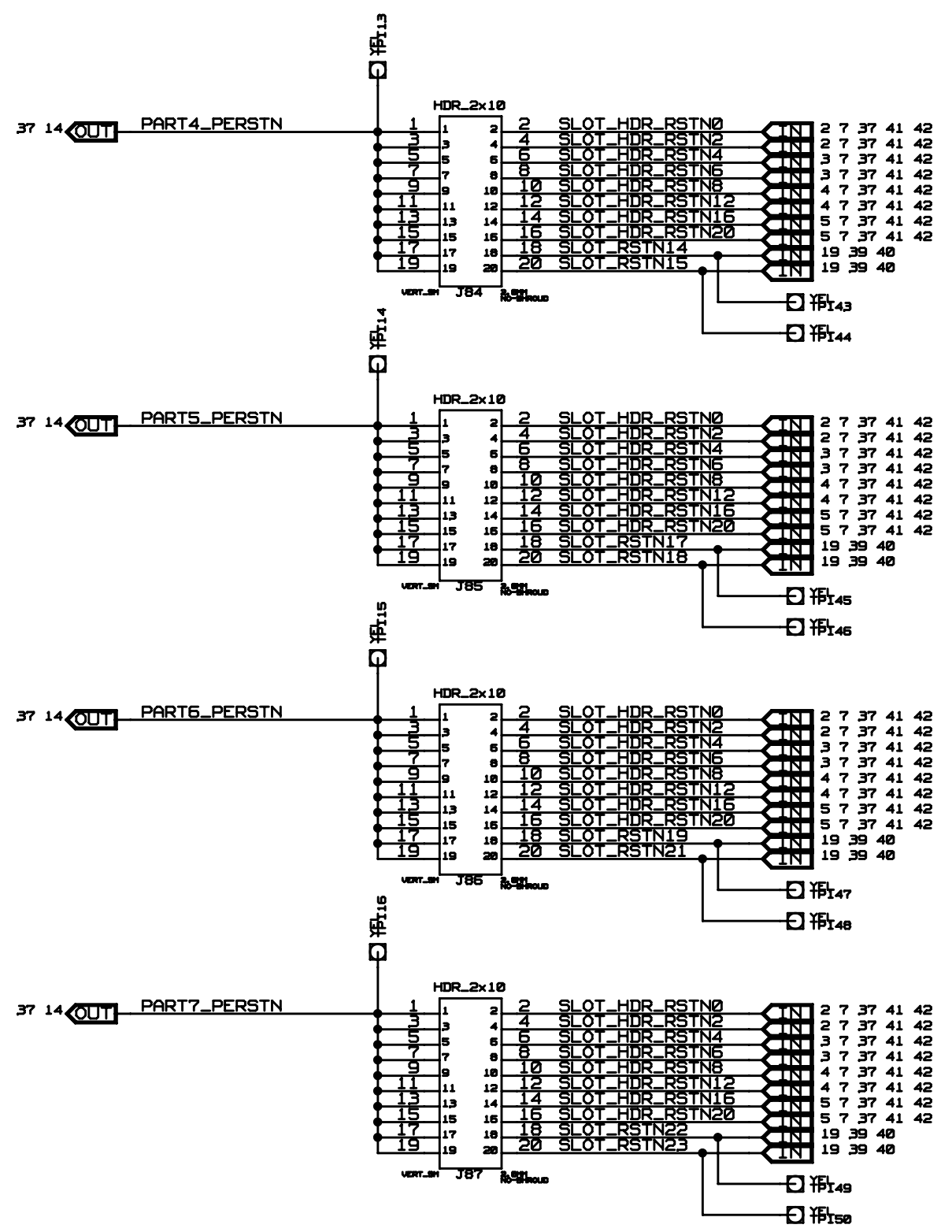
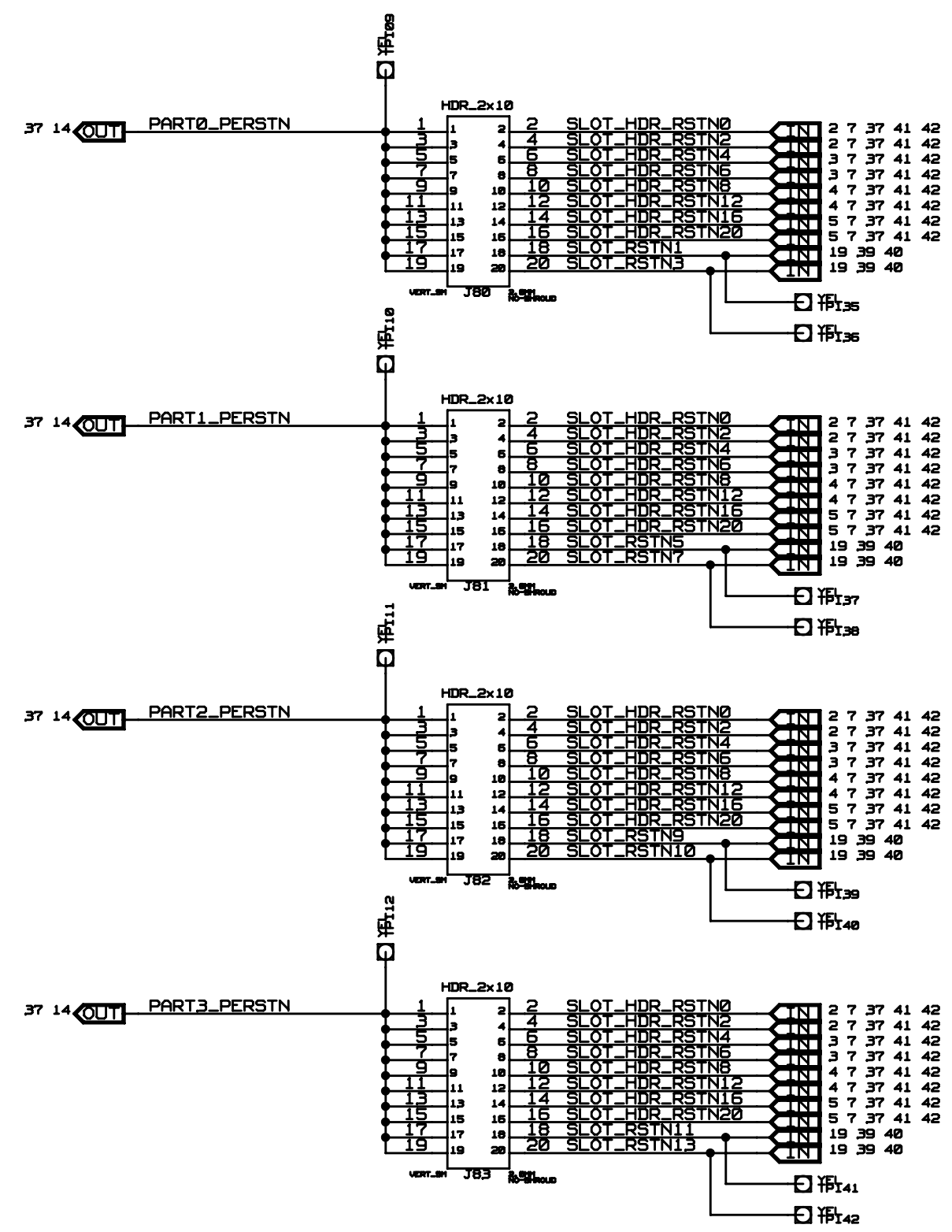
PORTS 3, 2, 1, 0



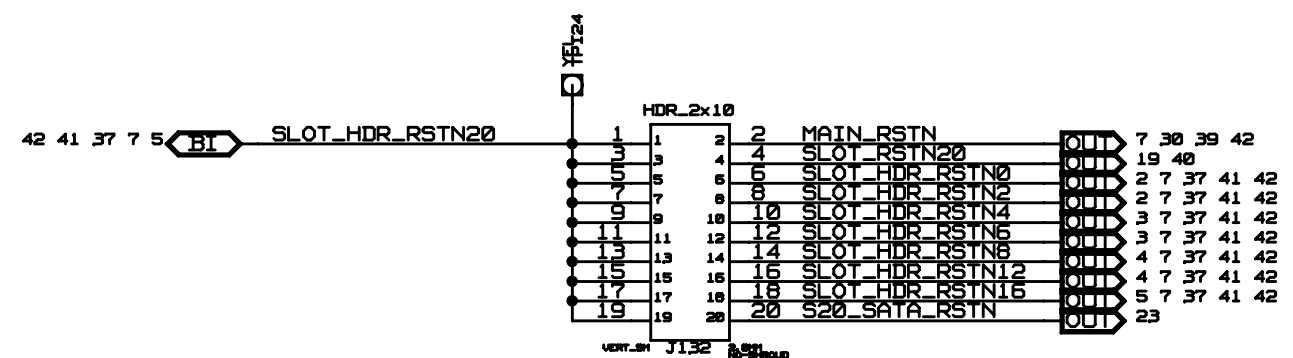
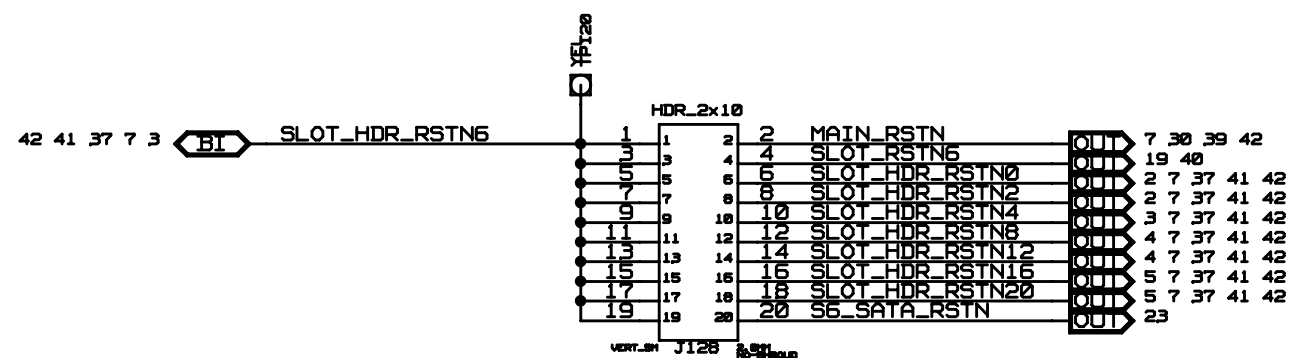
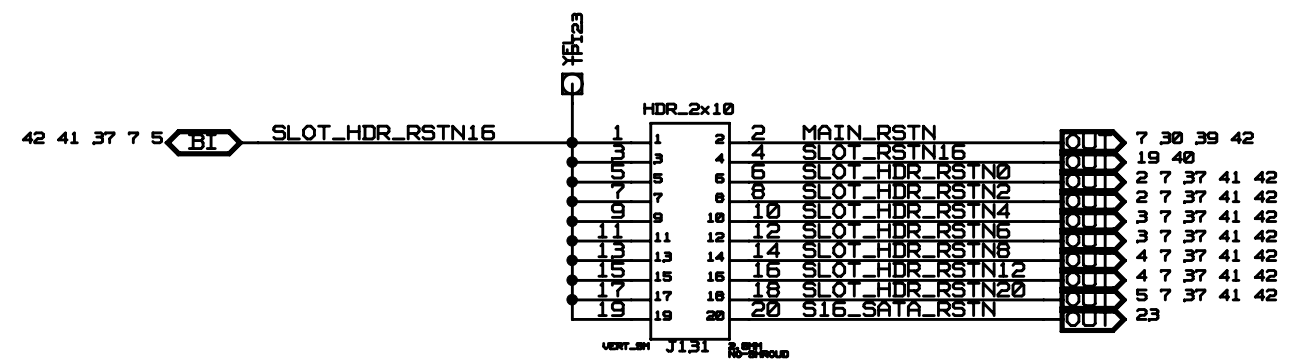
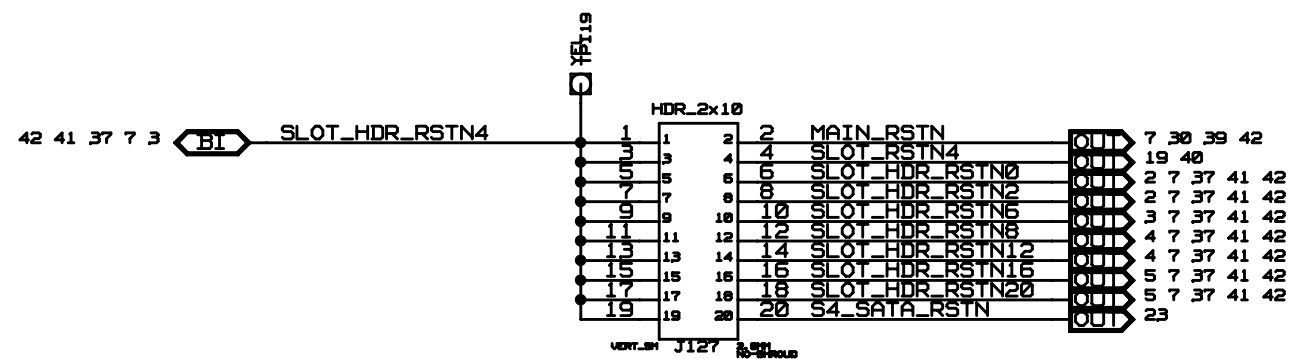
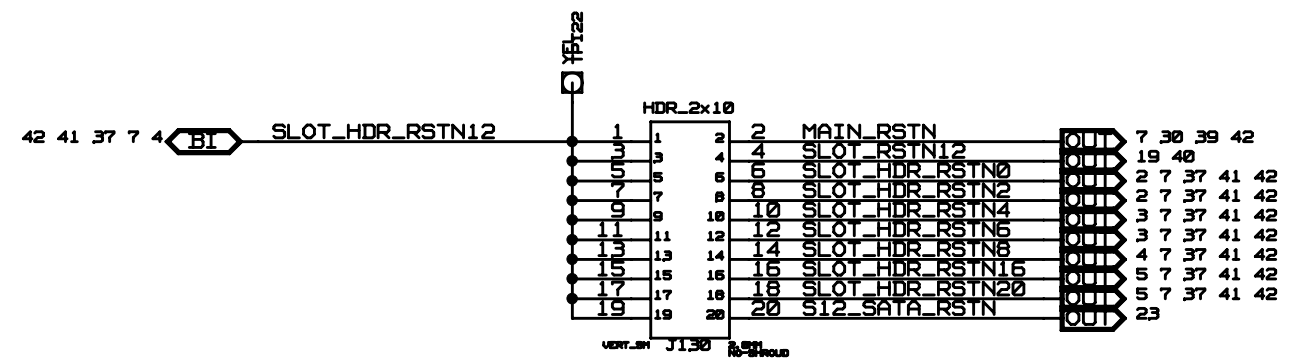
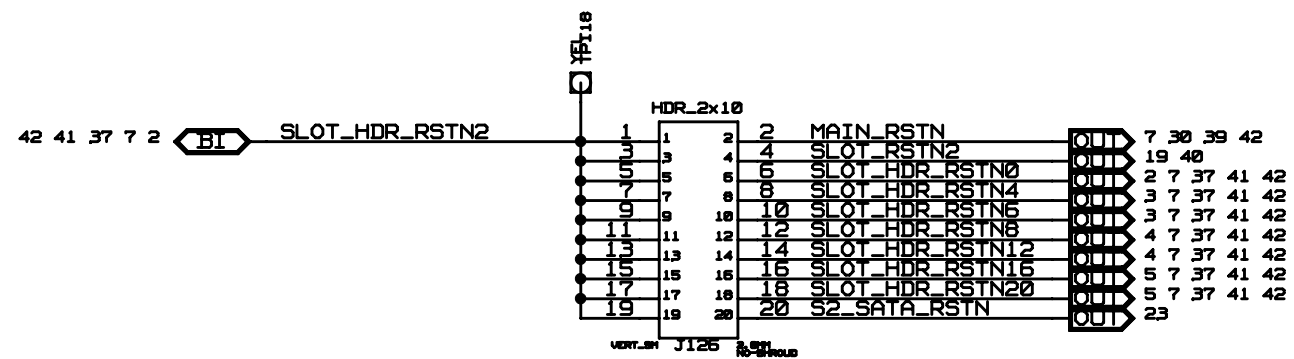
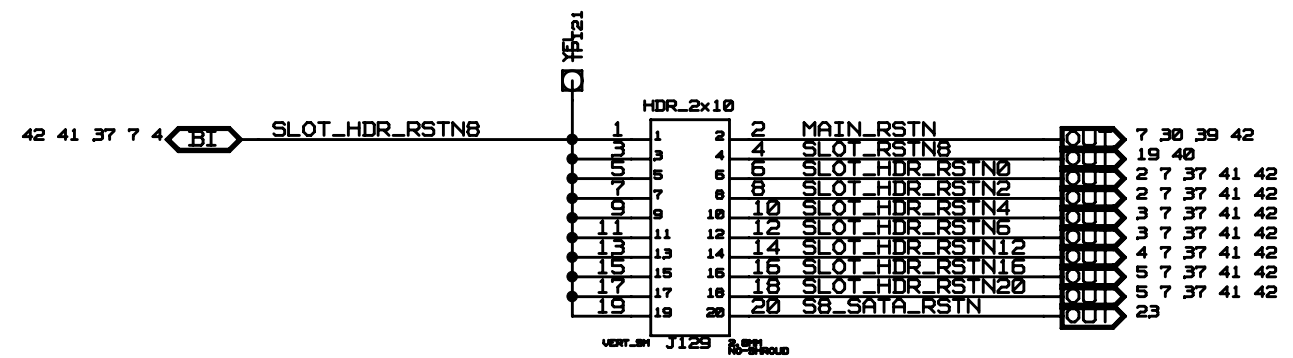
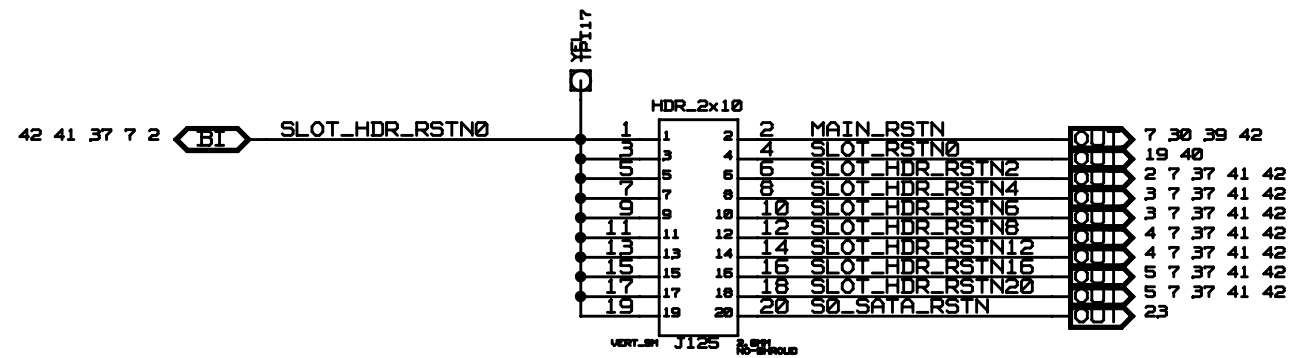
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SIDE BAND CONNECTORS			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
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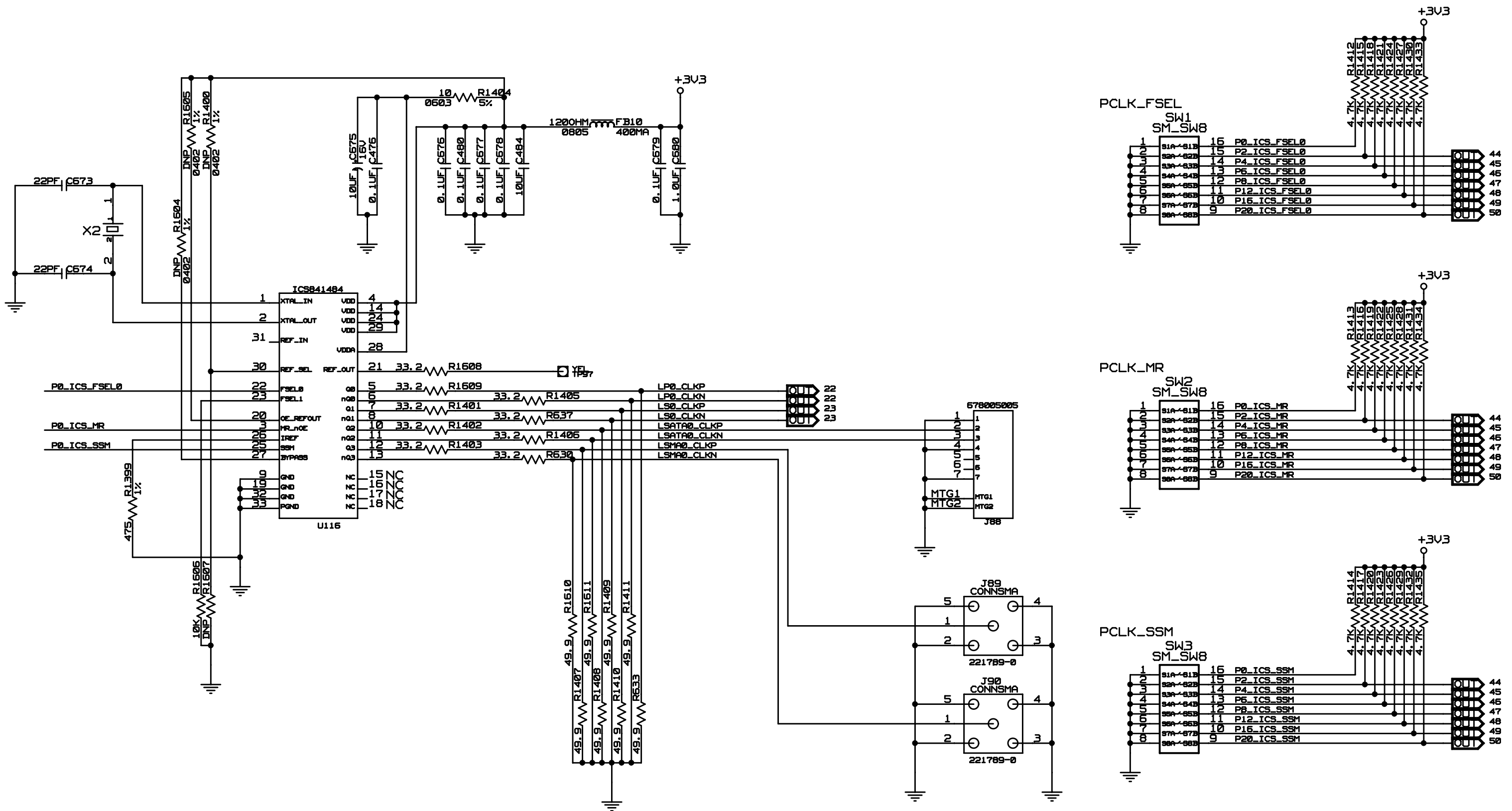


TITLE EB-LOGAN-23			
PARTITION RESET SELECT HEADERS			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:59 2010			SHEET 41 OF 50



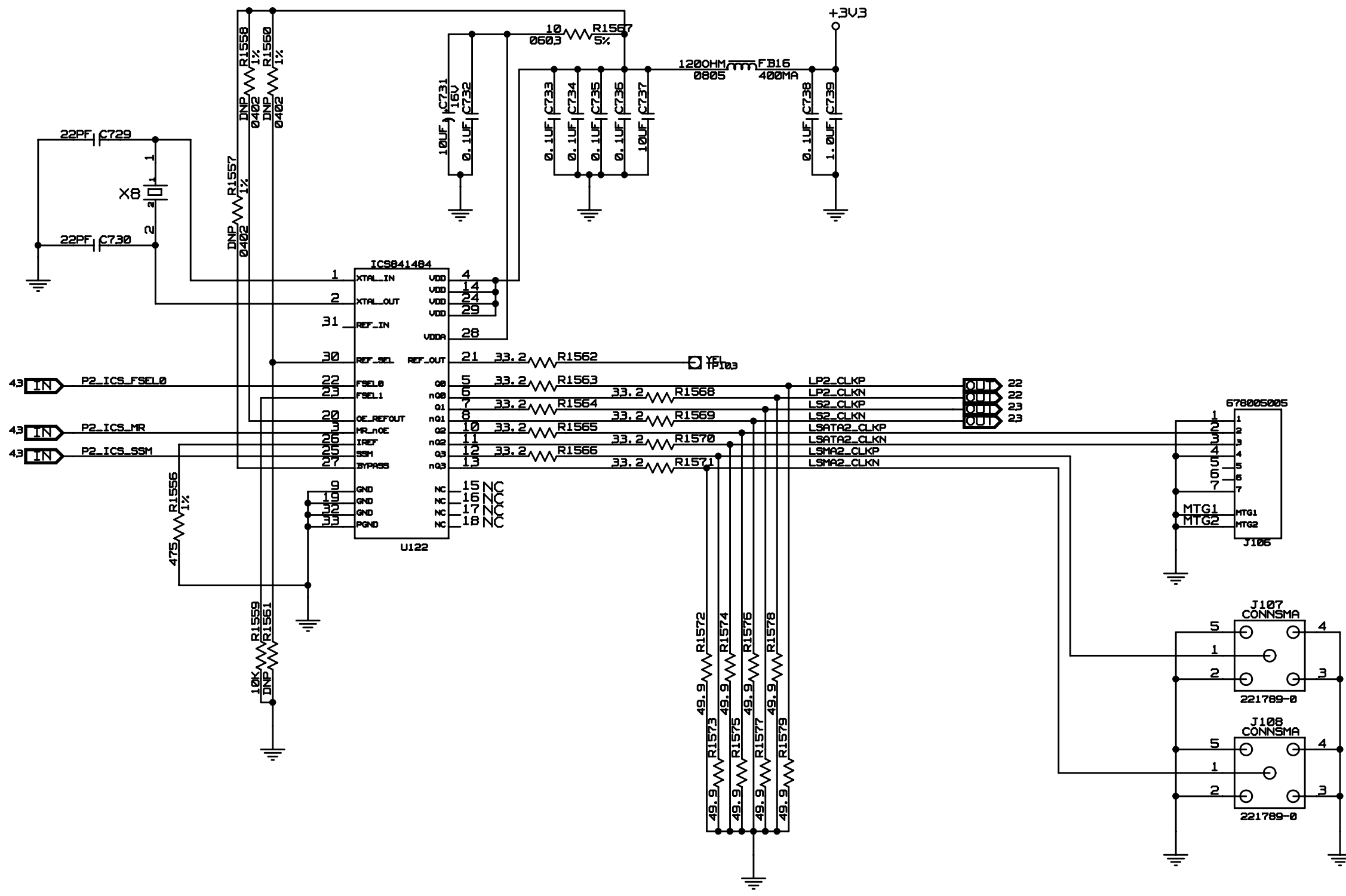
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TITLE EB-LOGAN-23			
SLOT RESET SELECT HEADERS			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:59 2010			SHEET 42 OF 50



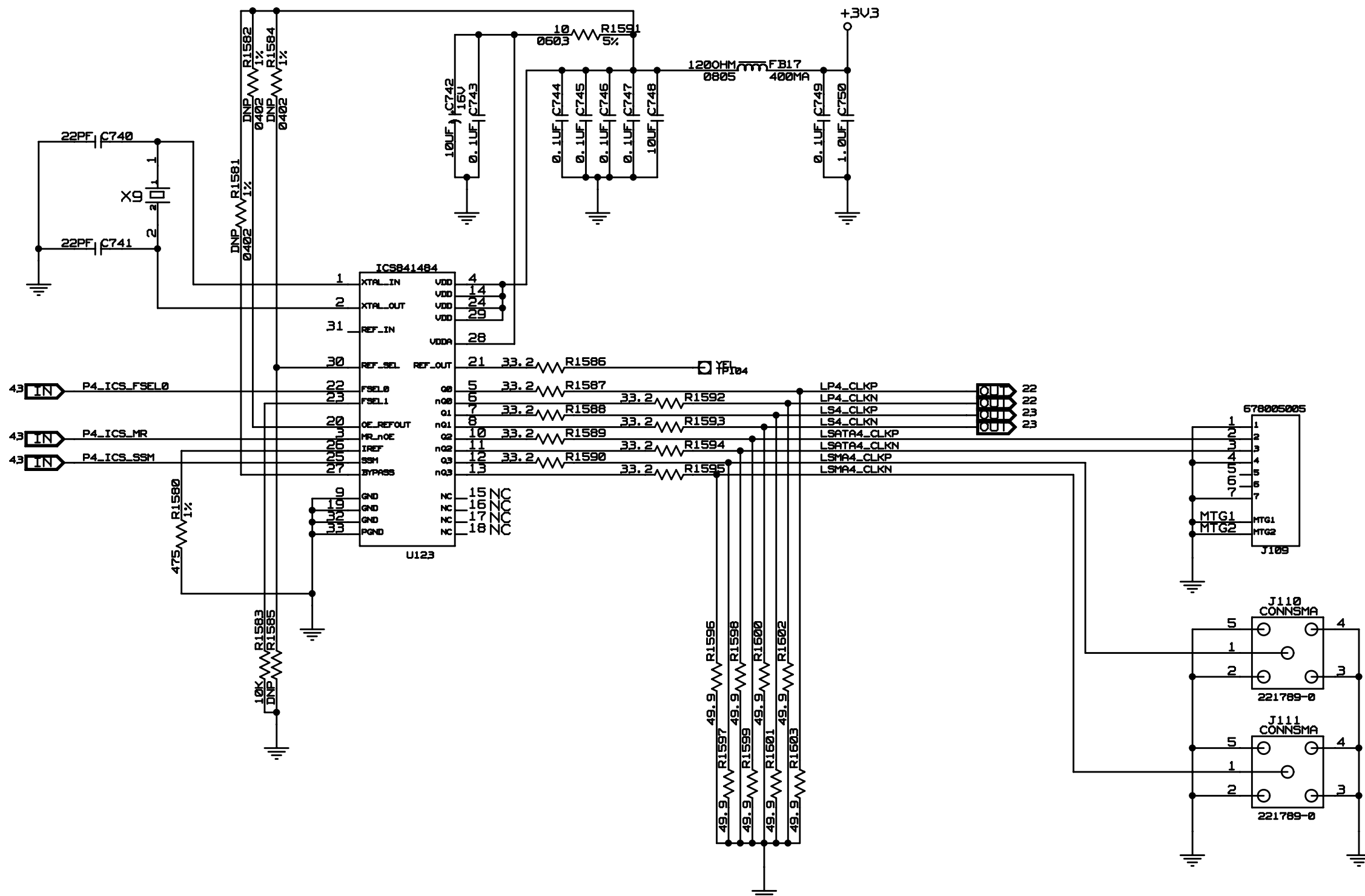
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TITLE EB-LOGAN-23			
PORT 0 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:55 2010			SHEET 43 OF 50



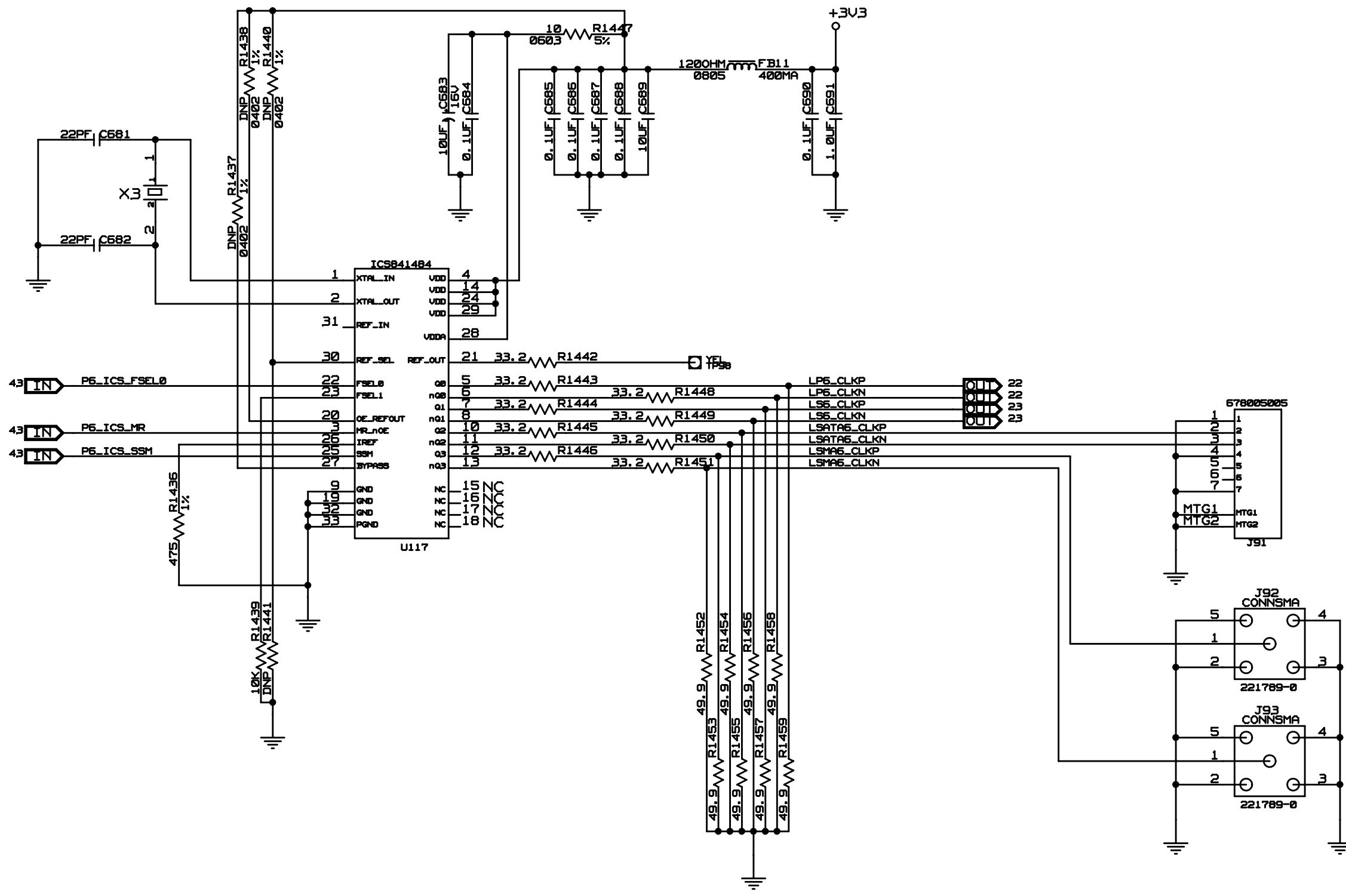

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TITLE EB-LOGAN-23			
PORT 2 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
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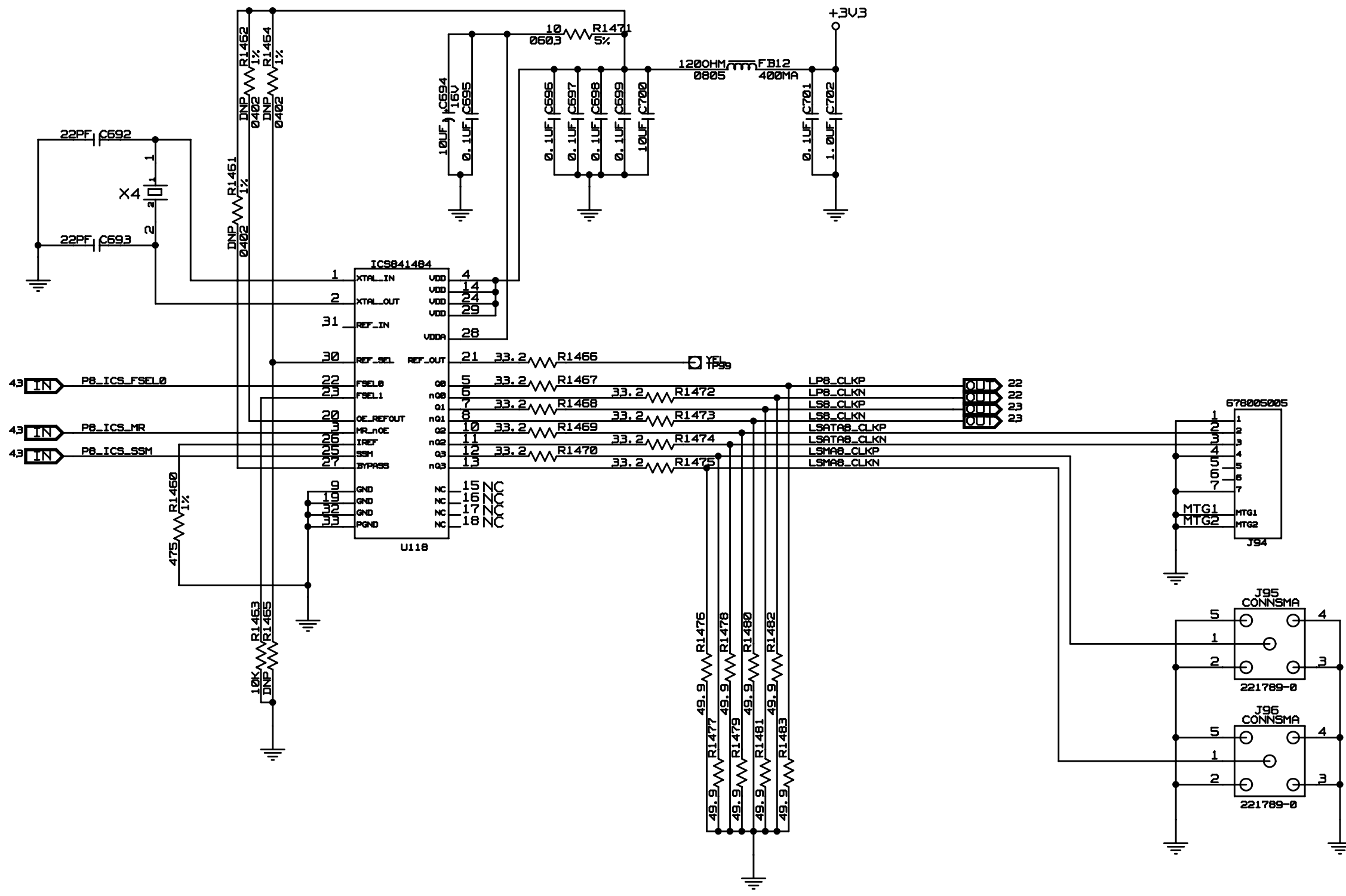

TITLE EB-LOGAN-23			
PORT 4 CLOCK GENERATOR			
SIZE B	DRAWING NO. SCH-PESEB-001	FAB P/N 18-691-000	REV. 1.1
AUTHOR Tony Tran		CHECKED BY Derek Huang	
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TITLE EB-LOGAN-23			
PORT 6 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
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TITLE EB-LOGAN-23			
PORT 8 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:56 2010			SHEET 47 OF 50

D

D

C

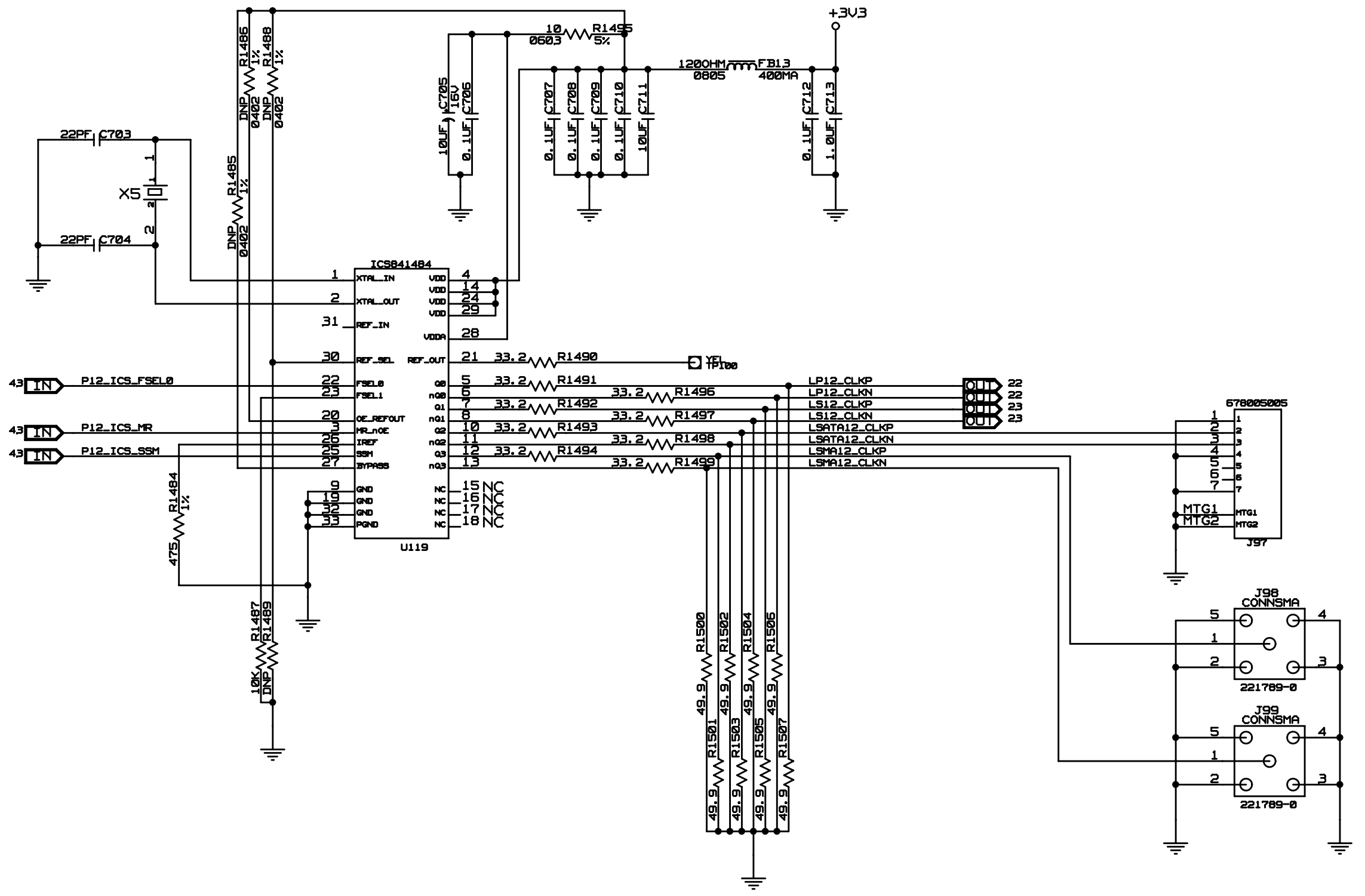
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B

B

A

A



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TITLE EB-LOGAN-23			
PORT 12 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:56 2010			SHEET 48 OF 50

D

D

C

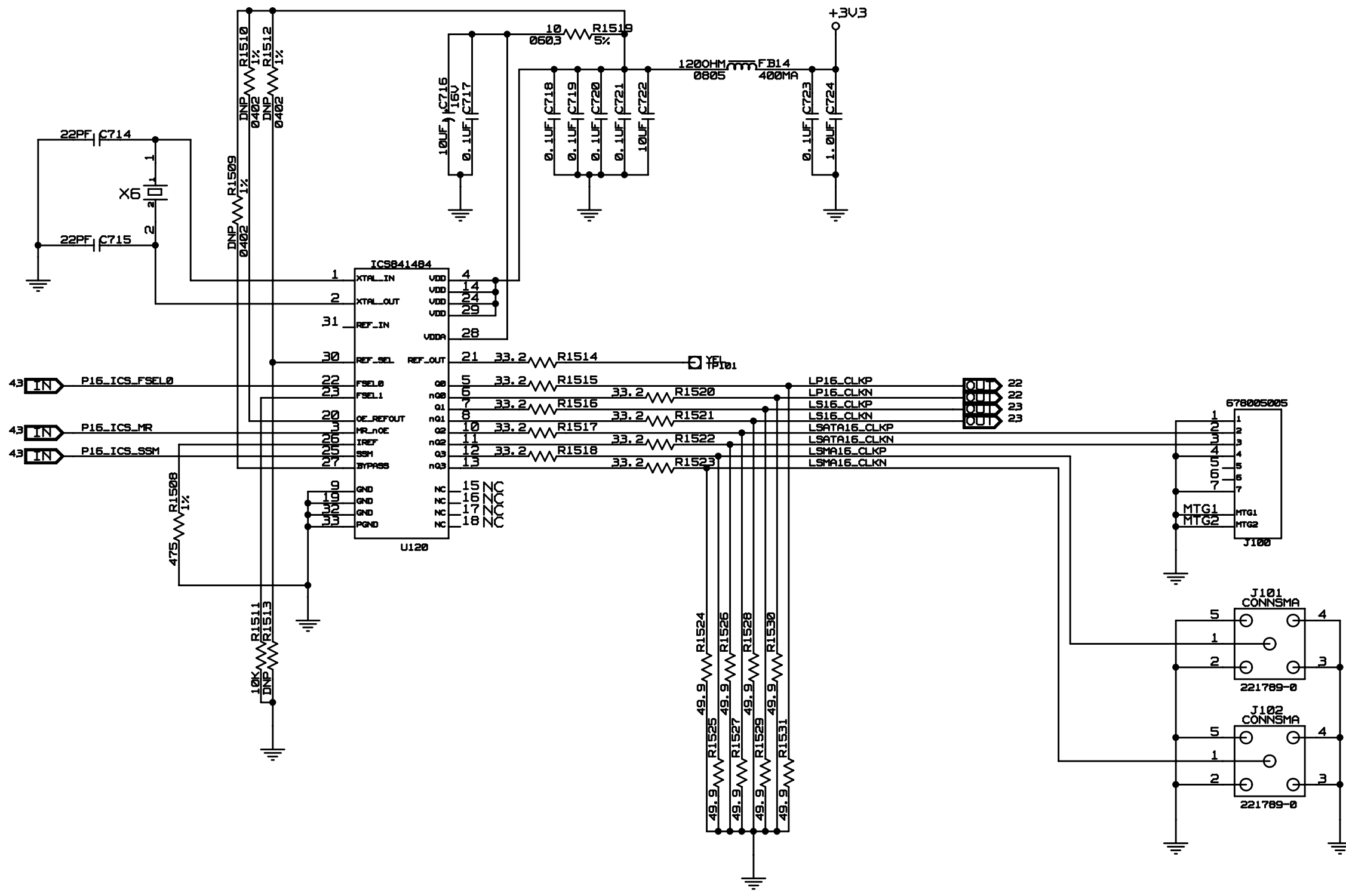
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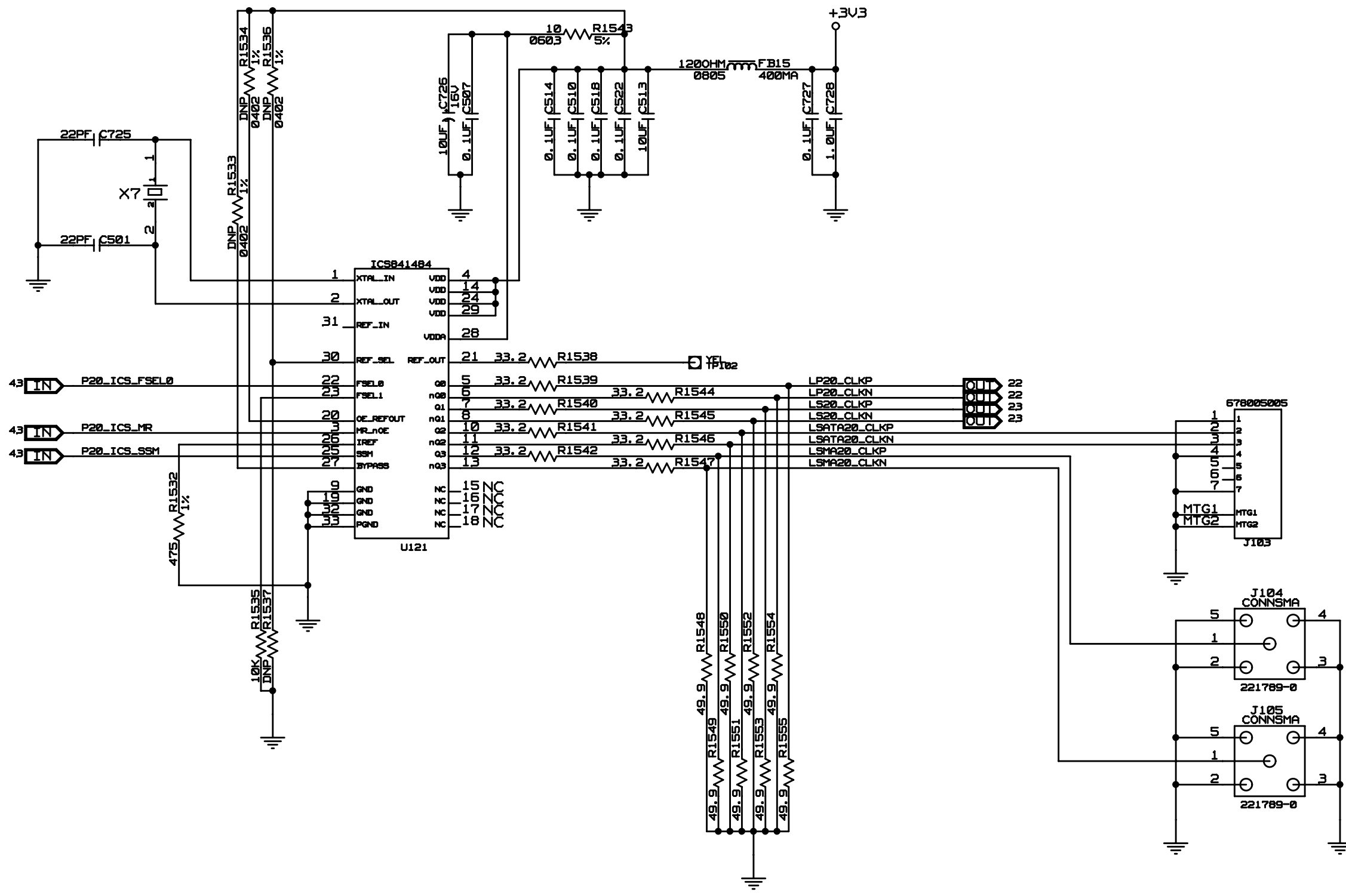

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TITLE EB-LOGAN-23			
PORT 16 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
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TITLE EB-LOGAN-23			
PORT 20 CLOCK GENERATOR			
SIZE	DRAWING NO.	FAB P/N	REV.
B	SCH-PESEB-001	18-691-000	1.1
AUTHOR		CHECKED BY	
Tony Tran		Derek Huang	
Mon Apr 19 14:30:56 2010			SHEET 50 OF 50