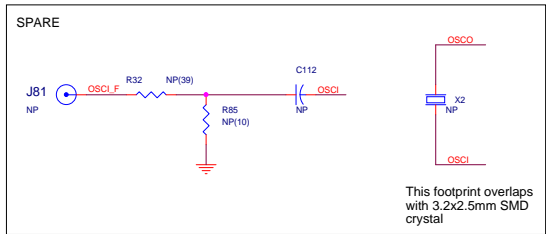
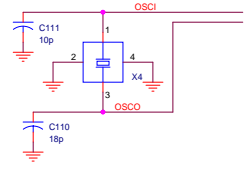
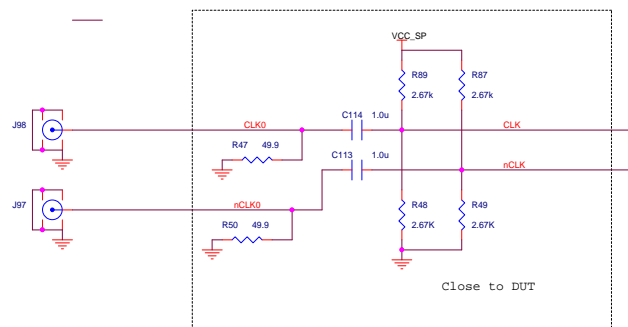


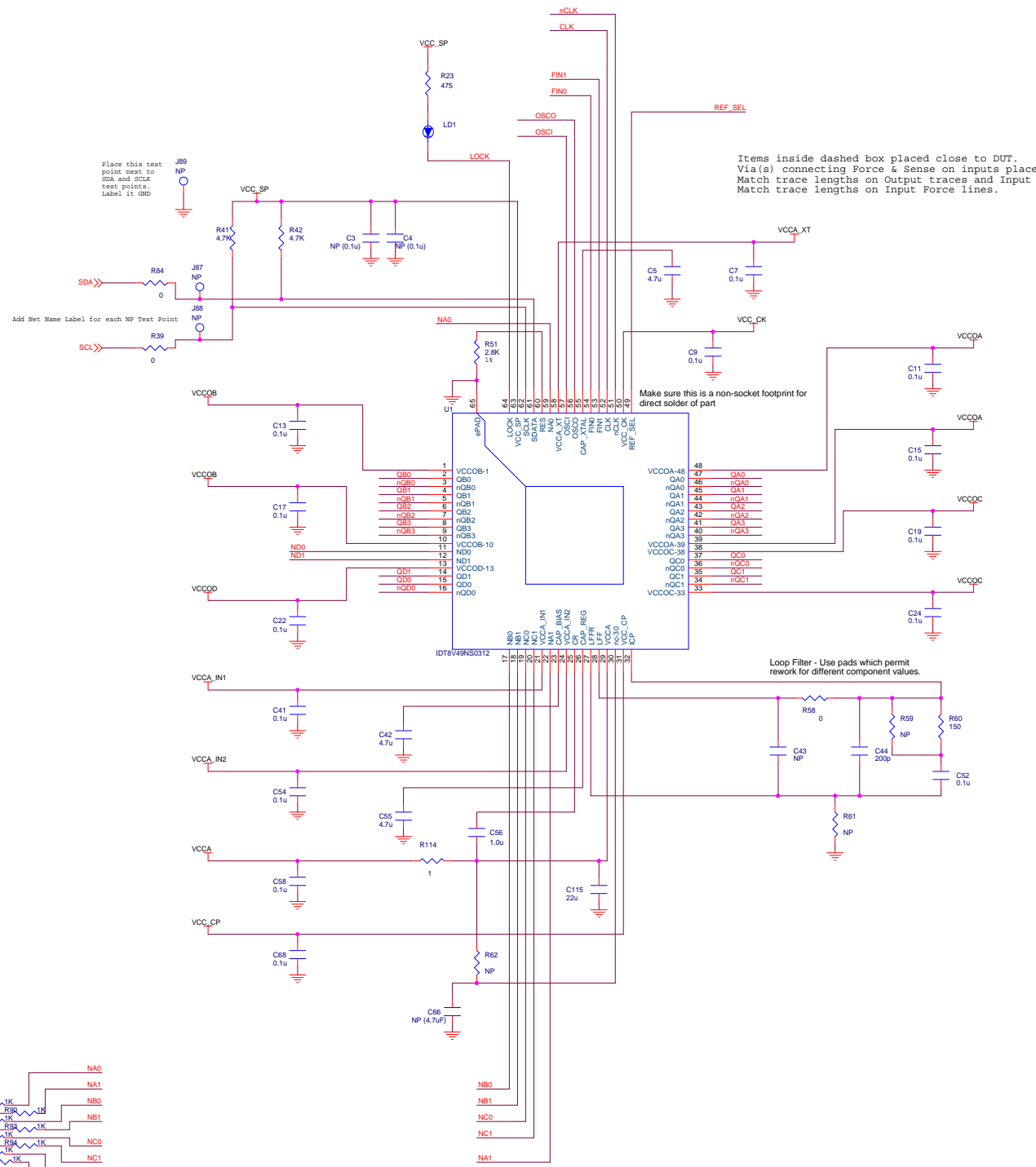
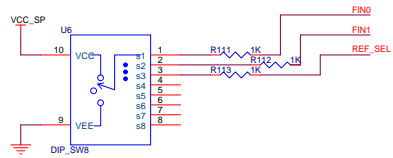
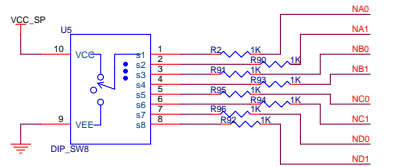
Crystal Interface



This footprint overlaps with 3.2x2.5mm SMD crystal



Close to DUT



Items inside dashed box placed close to DUT.
Via(s) connecting Force & Sense on inputs placed close to DUT.
Match trace lengths on Output traces and input sense lines.
Match trace lengths on Input Force lines.

Make sure this is a non-socket footprint for direct solder of part

Loop Filter - Use pads which permit rework for different component values.

Place 150ohm pull-down resistors near the driver.

SPARE termination near SMA's

SPARE termination near SMA's

SPARE termination near SMA's

SPARE termination near SMA's

SPARE termination near SMA's

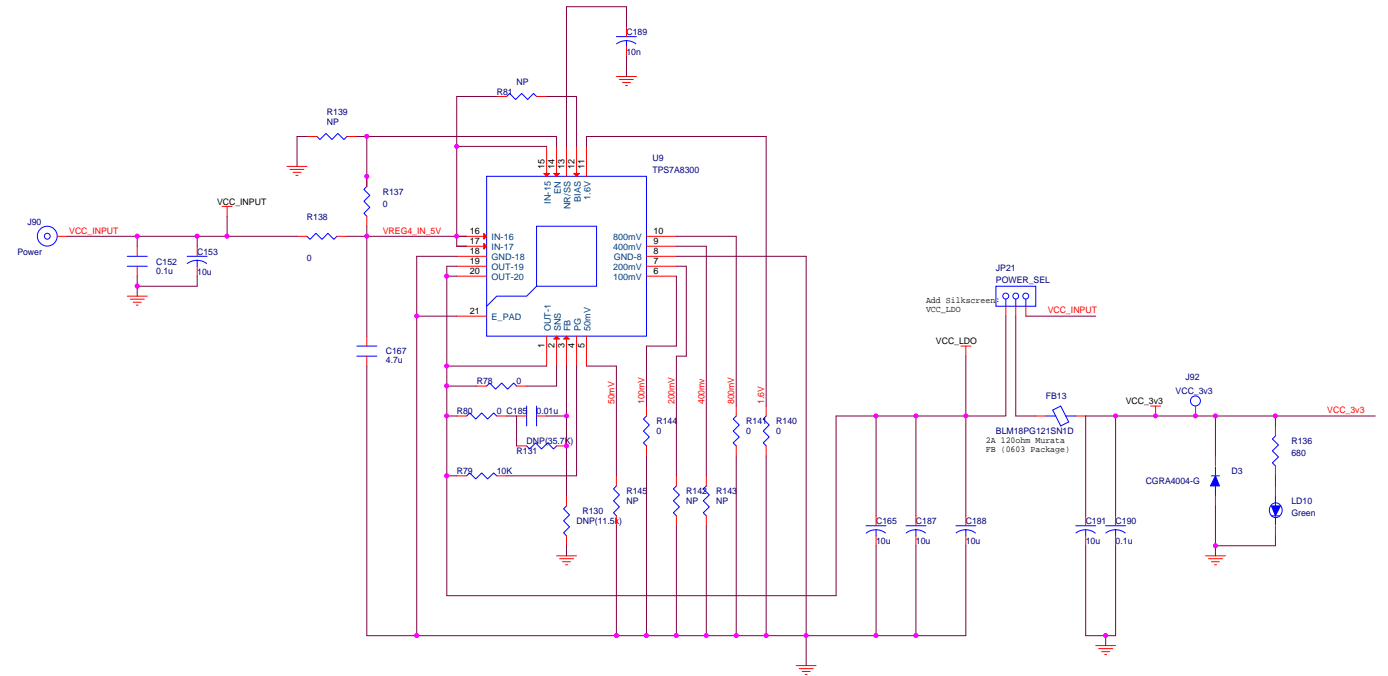
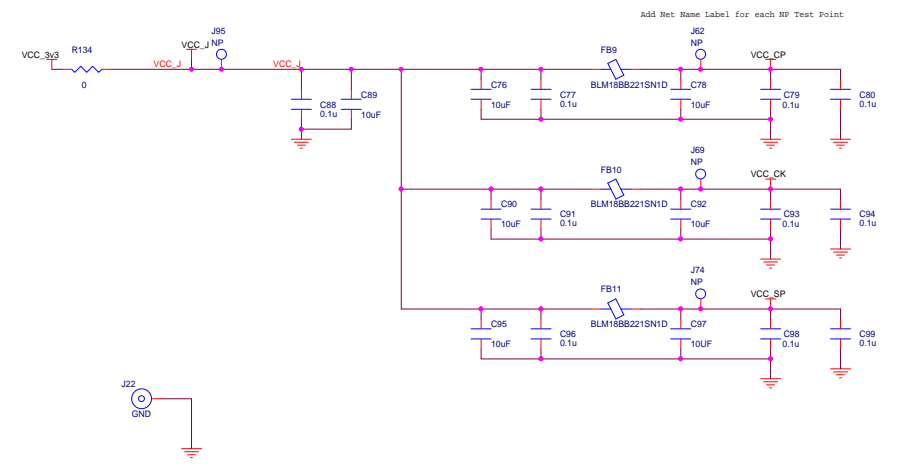
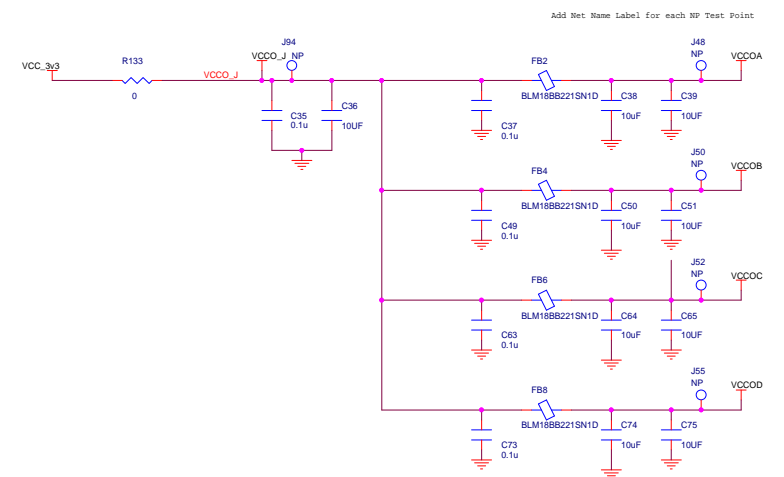
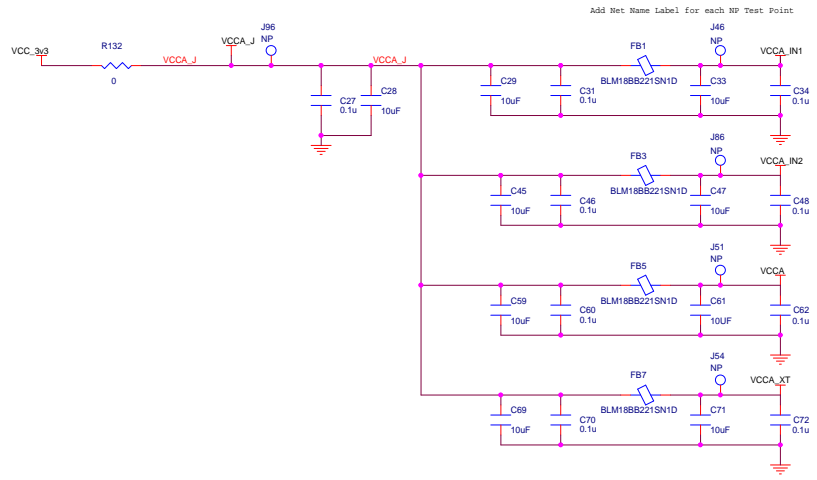
SPARE termination near SMA's

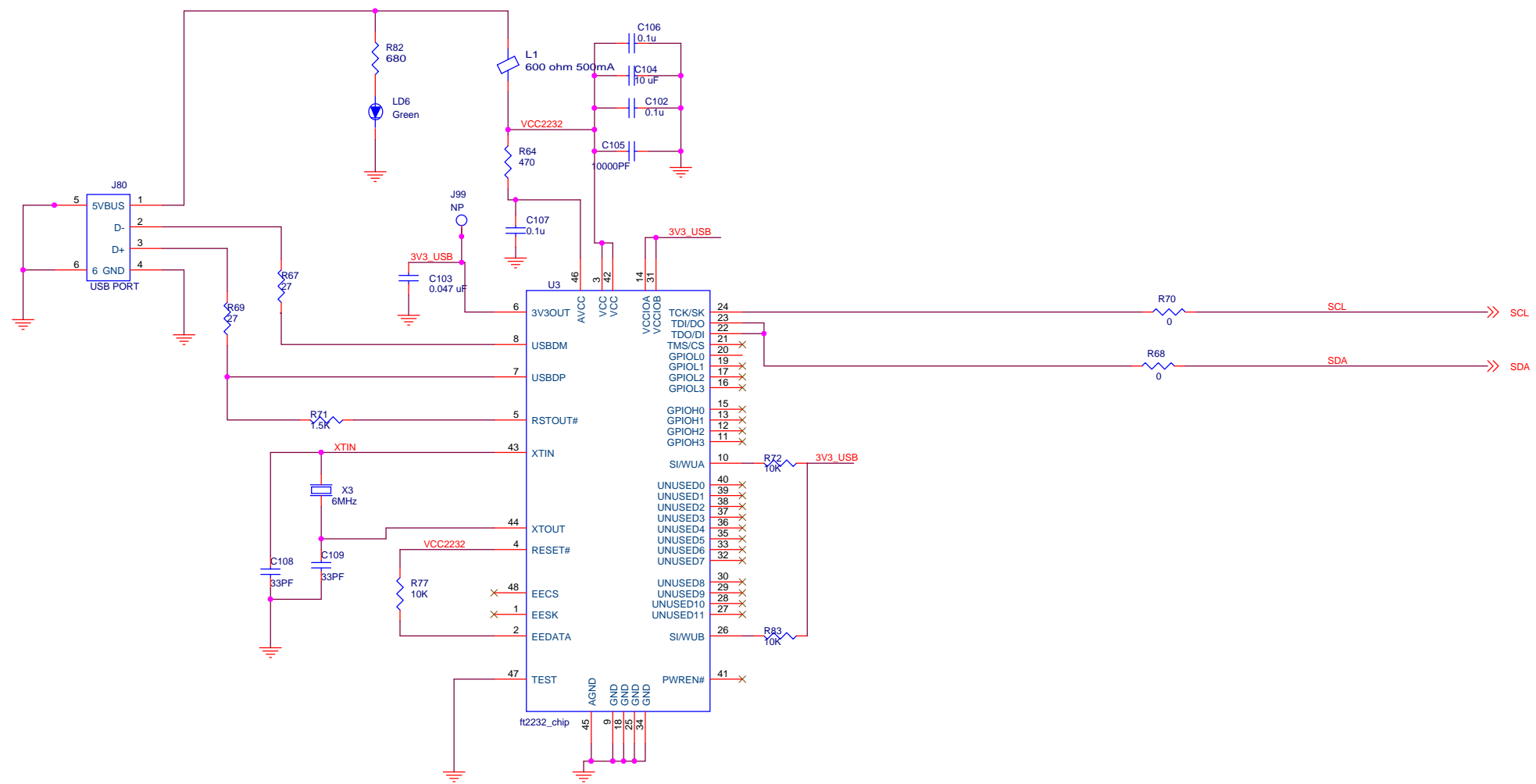
SPARE termination near SMA's

SPARE termination near SMA's

Place series resistor near the driver. Cap near the SMA

- Preferred Stack-up
- Layer 1: GND (and XTAL trace, loop filter, & caps C223, C246, C247, C248)
 - Layer 2: Signal - sense (Lock, QA & QB outputs and Clock & I/O sense)
 - Layer 3: GND
 - Layer 4: VCCOA, VCCOB, VCCA_XT, VCCA
 - Layer 5: VEE
 - Layer 6: VCCA_IN1, VCCA_IN2, VCC_CP, VCC_SP, VCC_CK
 - Layer 7: VCCO_C, VCCO_D
 - Layer 8: GND
 - Layer 9: Signal - sense (QC & QD outputs), Signal - force (CLK & I/O force)
 - Layer 10: GND (and bypass)





Title		
<Title>		
Size	Document Number	Rev
C	<Doc>	<Rev Code>
Date:	Monday, January 18, 2016	Sheet 1 of 1