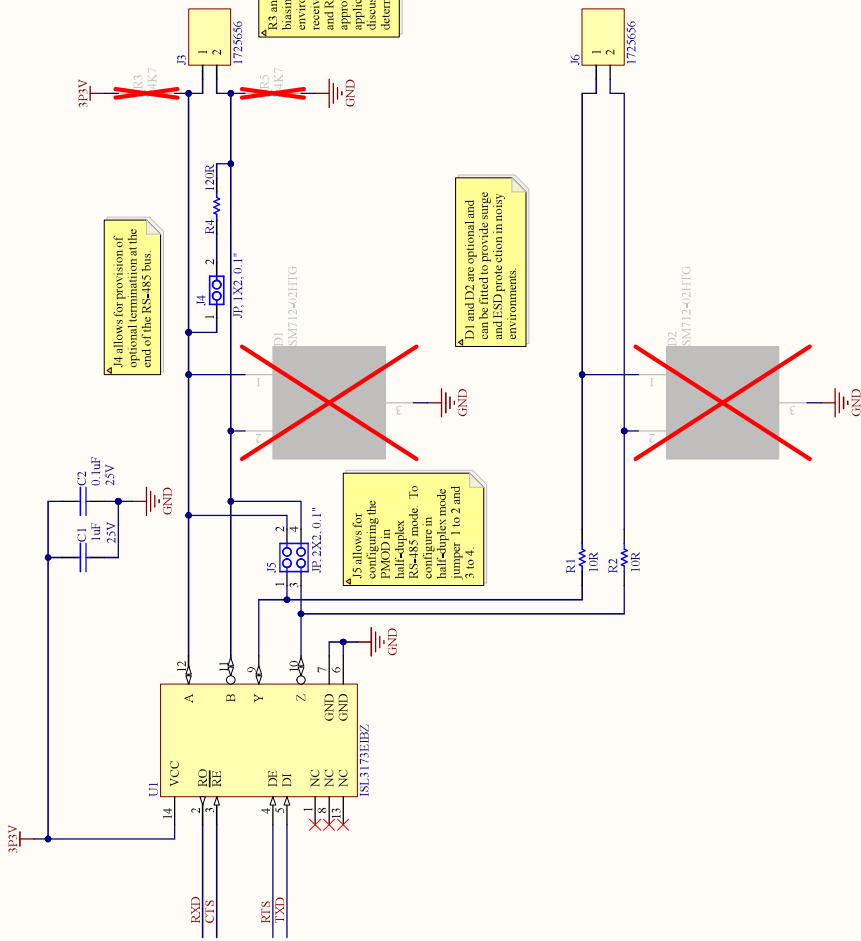


PMOD Interface Type 3 (UART)

Pin #	Signal	Direction (relative to host)	Alternate Signal	Direction (relative to host)
1	CTS	In	GPIO	In/Out
2	TXD	Out	-	-
3	RXD	In	-	-
4	RTS	Out	GPIO	In/Out
5	GND	-	-	-
6	VCC	-	-	-

PMOD Interface Type 3

R3 and R5 provide an option for fail-safe biasing of the RS-485 bus. The RS-485 "fail-safe reserves" so under normal circumstances, R3 and R5 are not required. 4K7 is an approximate value, consult Renesas application note AN1996 for a detailed discussion on fail-safe biasing and to determine the correct values for R3 and R5.



J4 allows for provision of optional termination at the end of the RS-485 bus.

J5 allows for configuring the PMOD in full-duplex, half-duplex, or tri-state mode. To configure in full-duplex mode, jumper 1 to 2 and jumper 3 to 4.

D1 and D2 are optional and can be fitted to provide surge and ESD protection in noisy environments.

- JMP2, JMP4, JMP5A, JMP5B: 881545-2
- JMP3: 881545-2

JMP2, JMP4, JMP5A and JMP5B are the shunt jumpers supplied with the board in the event they are required. They should be supplied with the board in an anti-static bag.

Rubber Foot, Black, 6.4mm dia, 2.1mm high
FOOT-400003-01



RENESAS

4117 Emperor Blvd
Suite 100
Durham, NC 27703

The
QCI0T-ISL3173POCZ

Size: B Number: Revision: A
Date: 10/2/2024 Sheet 1 of 1
File: QCI0T-ISL3173POCZ_Rev.A_SchDoc