

ISL62883C

Multiphase PWM Regulator for IMVP-6.5 Mobile CPUs and GPUs

R16DO0020EU0301

Rev.3.01

Sep 1, 2022

The **ISL62883C** is a multiphase PWM buck regulator for microprocessor or graphics processor core power supply. The multiphase buck converter uses interleaved phases to reduce the total output voltage ripple with each phase carrying a portion of the total load current, providing better system performance, superior thermal management, lower component cost, reduced power dissipation, and smaller implementation area. The ISL62883C uses two integrated gate drivers and an external gate driver to provide a complete solution. The PWM modulator is based on the Intersil Robust Ripple Regulator (R3™) technology. Compared with traditional modulators, the R3 modulator commands variable switching frequency during load transients, achieving faster transient response. With the same modulator, the switching frequency is reduced at light load, increasing the regulator efficiency.

The ISL62883C can be configured as a CPU or graphics Vcore controller and is fully compliant with Intel IMVP-6.5 specifications. It responds to PSI# and DPRSLPVR signals by adding or dropping PWM3 and Phase 2 respectively, adjusting the overcurrent protection threshold accordingly, and entering and exiting Diode Emulation mode. It reports the regulator output current through the IMON pin. It senses the current by using either a discrete resistor or inductor DCR whose variation over temperature can be thermally compensated by a single NTC thermistor. It uses differential remote voltage sensing to accurately regulate the processor die voltage. The adaptive body diode conduction time reduction function minimizes the body diode conduction loss in Diode Emulation mode. User-selectable overshoot reduction function offers an option to aggressively reduce the output capacitors as well as the option to disable it for users concerned about increased system thermal stress. In 2-phase configuration, the ISL62883C offers the FB2 function to optimize 1-Phase performance.

**Features**

- Programmable 1, 2-, or 3-phase CPU or GPU mode of operation
- Precision multiphase core voltage regulation
  - 0.5% system accuracy over-temperature
  - Enhanced load line accuracy
- Microprocessor voltage identification input
  - 7-Bit VID input, 0V to 1.500V in 12.5mV steps
  - Supports VID changes on-the-fly
- Supports multiple current sensing methods
  - Lossless inductor DCR current sensing
  - Precision resistor current sensing
- Supports PSI# and DPRSLPVR modes
- Superior noise immunity and transient response
- Current monitor and thermal monitor
- Differential remote voltage sensing
- High efficiency across entire load range
- Two integrated gate drivers
- Excellent dynamic current balance
- FB2 function optimizes 1-phase mode performance
- Adaptive body diode conduction time reduction
- User-selectable overshoot reduction function
- Small footprint 40 Ld 5x5 TQFN packages
- Pb-free (RoHS compliant)

**Applications**

- Notebook core voltage regulator
- Notebook GPU voltage regulator

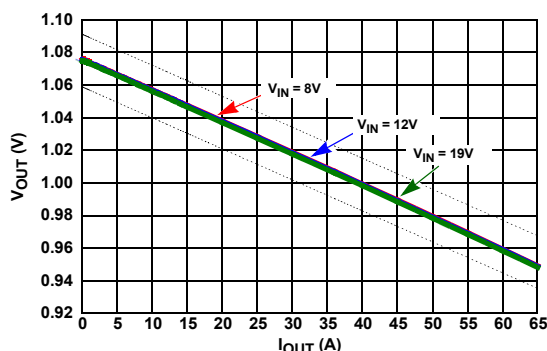


FIGURE 1. LOAD LINE REGULATION

## IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES (“RENESAS”) PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01)

### Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

### Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

### Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit [www.renesas.com/contact-us/](http://www.renesas.com/contact-us/).