[Upgrade to Revision]

Programming GUI for PG-FP6 Flash Memory Programmer

FP6 Terminal V1.13.00

Outline

FP6 Terminal, a programming GUI for PG-FP6 Flash Memory Programmer, has been updated from V1.12.00 to V1.13.00. For an overview of PG-FP6, refer to the link below.

https://www.renesas.com/pg-fp6

Connect the updated FP6 Terminal to PG-FP6 to update the firmware of PG-FP6.

1. Description

The following sections cover the main enhancements. For details, see the release note at the link below.

(The note is applicable as of July 22.)

https://www.renesas.com/software-tool/pg-fp6#document

1.1 Addition of Supported Devices

- RH850 Family

Group	Part number
RH850/U2B10	R7F70254x
RH850/U2B-FCC	R7F702Z21, R7F702Z26

1.2 Improved Functionality and Change

- Program files for flash options selected in the FP6 gang programmer

Previously, program files for flash options selected in the FP6 gang programmer were specified at the time of creating a setting file in the FP6 Terminal. The specification has now been changed so that the Renesas Flash Programmer is used to generate an RPI file and that file is then downloaded to the FP6.

- SWD interface signal

The stability of communications of the SWD signal has now been improved for RA devices.

- Increasing the speed of verification in RH850 devices

The speed of verification by using the read command has now been improved for some RH850 devices.

2. Updating the Product

FP6 Terminal can be updated free of charge. Download the installer from the link below.

(The installer is available as of July 22.)

https://www.renesas.com/software-tool/pg-fp6#download

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Revision History

Rev.	Date	Description	
		Page	**Summary**
1.00	Jul.20.24	-	First edition issued

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Contact Information

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