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# VersaClock<sup>®</sup> 7 Single and Static Multi Config Setup using RICBox

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This document provides the steps needed to understand Single and Static Multi Config setup on VersaClock 7 (VC7) using Renesas IC Toolbox (RICBox).

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## 1. Installation

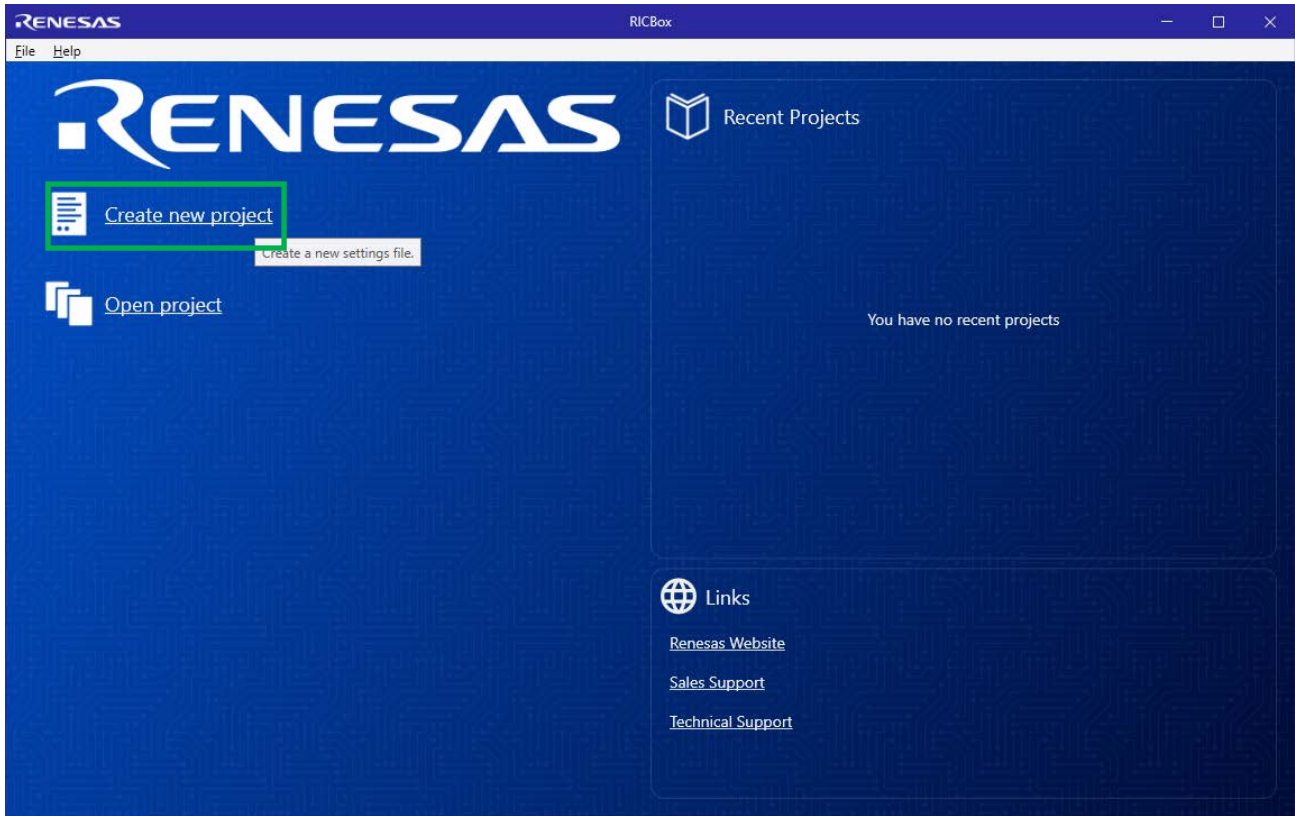
Install the RICBox software “R\_VersaClock7” package. Refer to the [Renesas IC Toolbox Software Manual](#) for more information on installing RICBox software for VC7.

## 2. Creating and Loading Settings Files

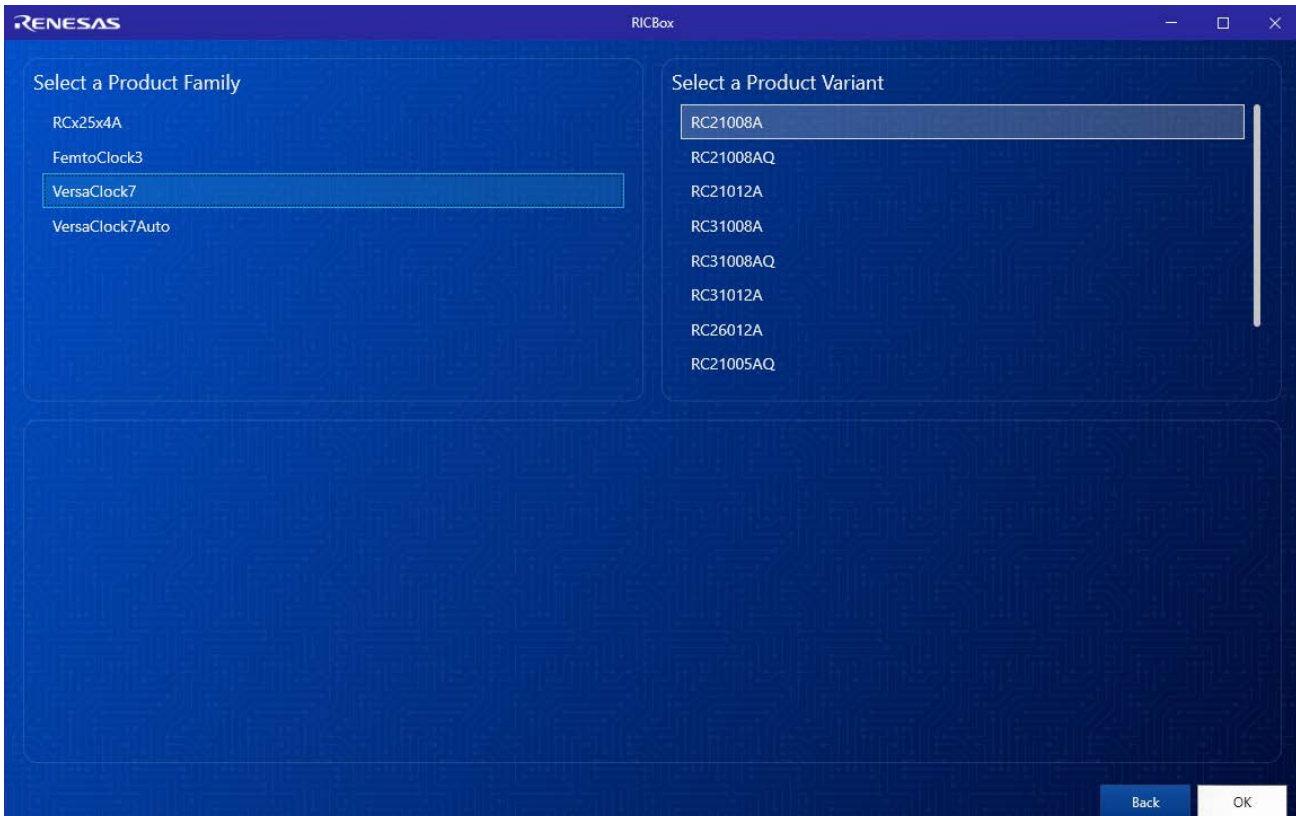
RICBox settings files or .rbs files are used to save and distribute custom device configurations. Each settings file contains all of the register settings for a given device. This application note will focus on how to create a single configuration and how to add additional configurations. For more information regarding VC7 RICBox GUI and software, refer to the [RICBox GUI Software for VersaClock 7 User Guide](#).

## 2.1 Creating a New Configuration

To create a new configuration, open RICBox and click on the *Create new project* button.

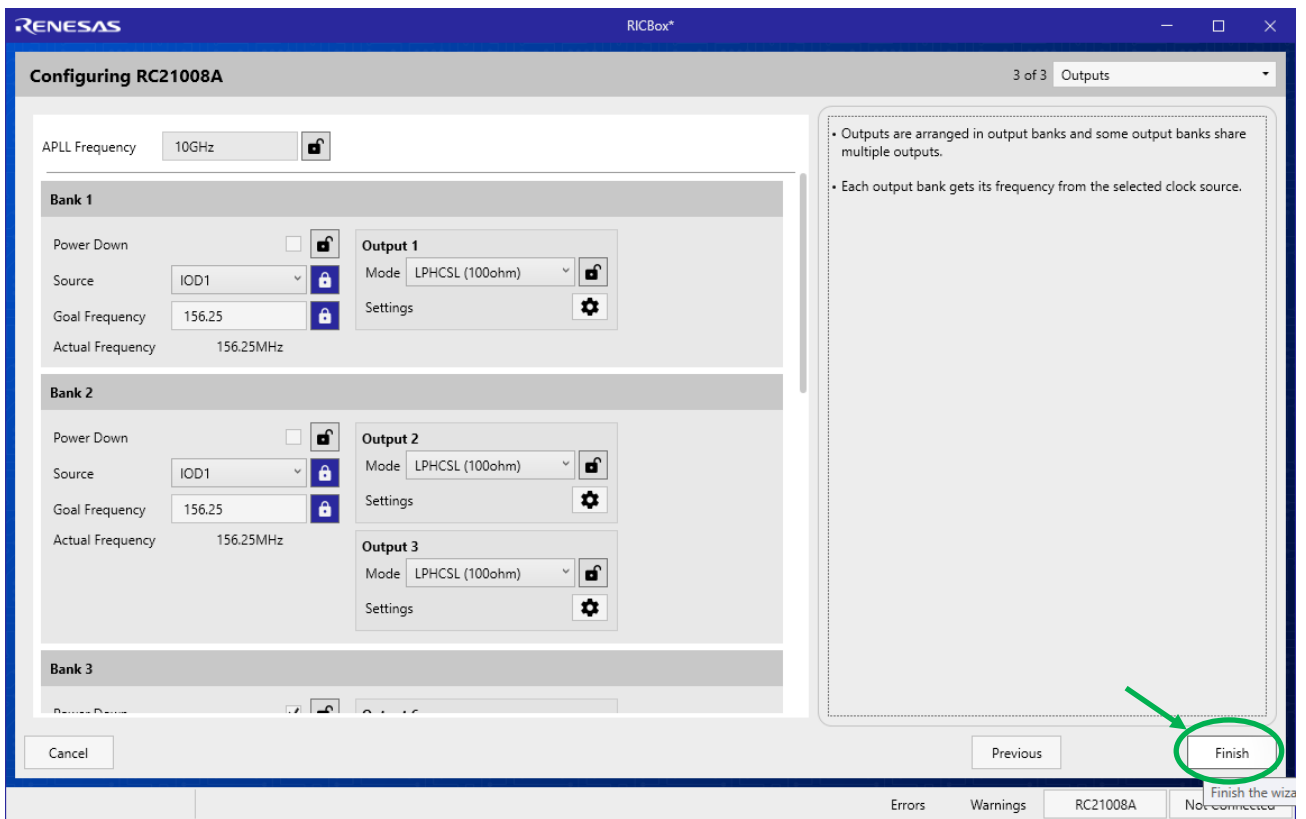


Select the VersaClock 7 product variant to be configured and click the *OK* button.



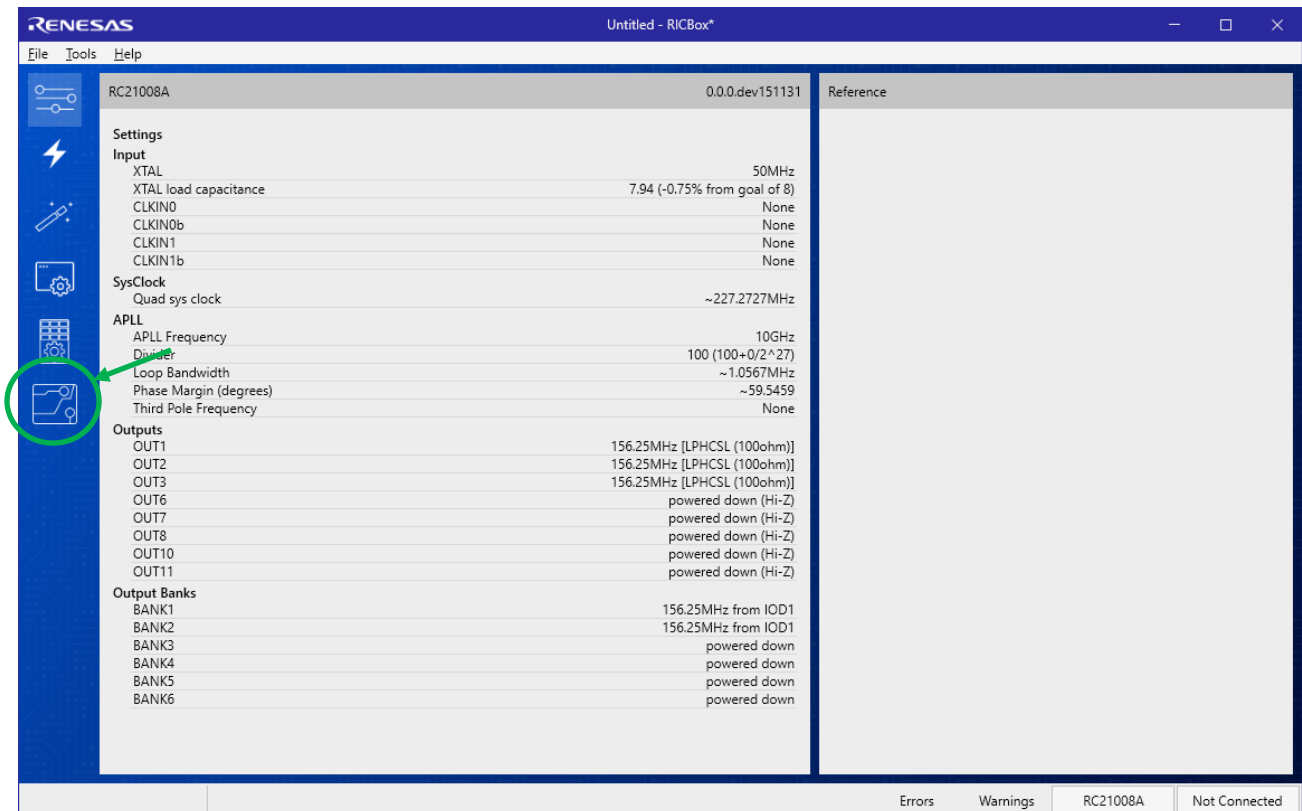
## VersaClock 7 Single and Static Multi Config Setup using RICBox

Use the wizard to configure VC7 to match the application. Click on the *Finish* button when done.



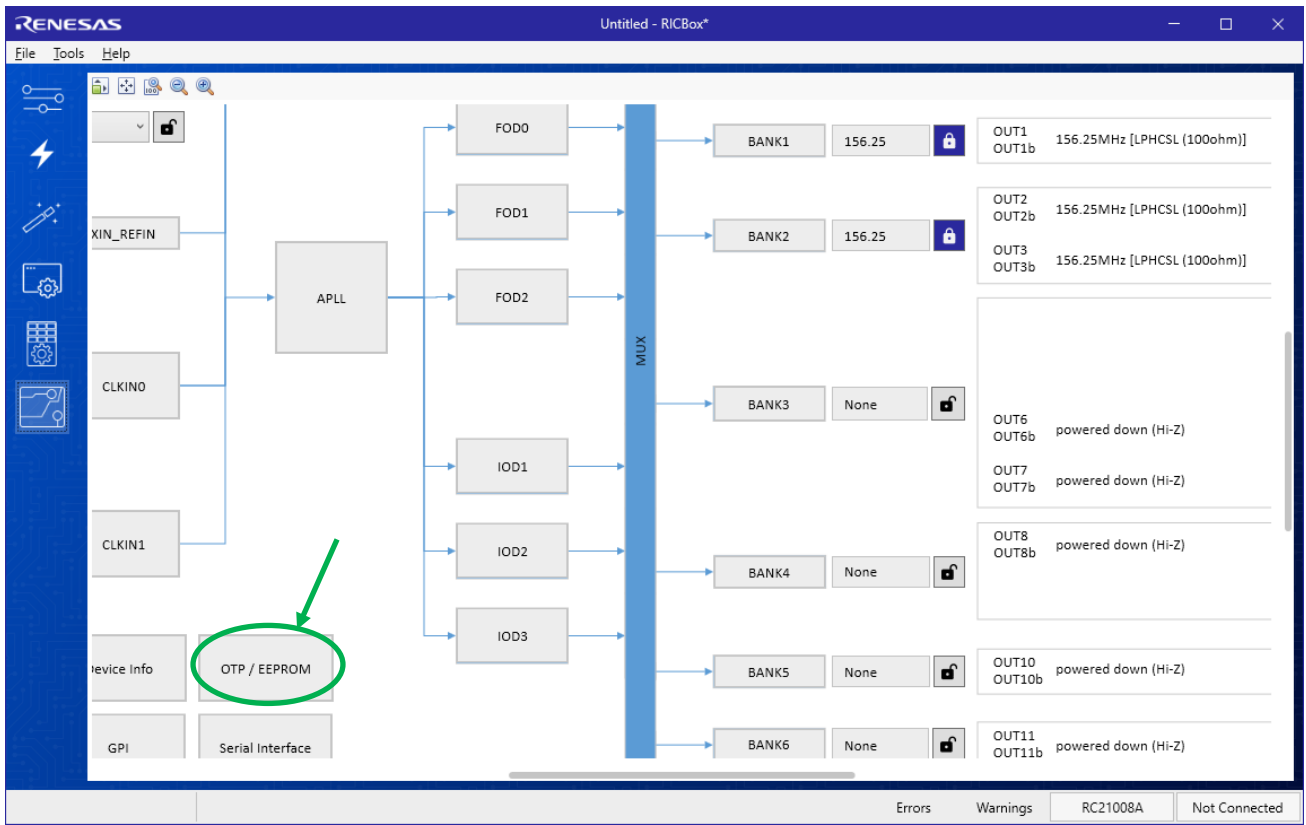
### 3. Enable Single Config

Click on the block diagram icon to view the VC7 block diagram.

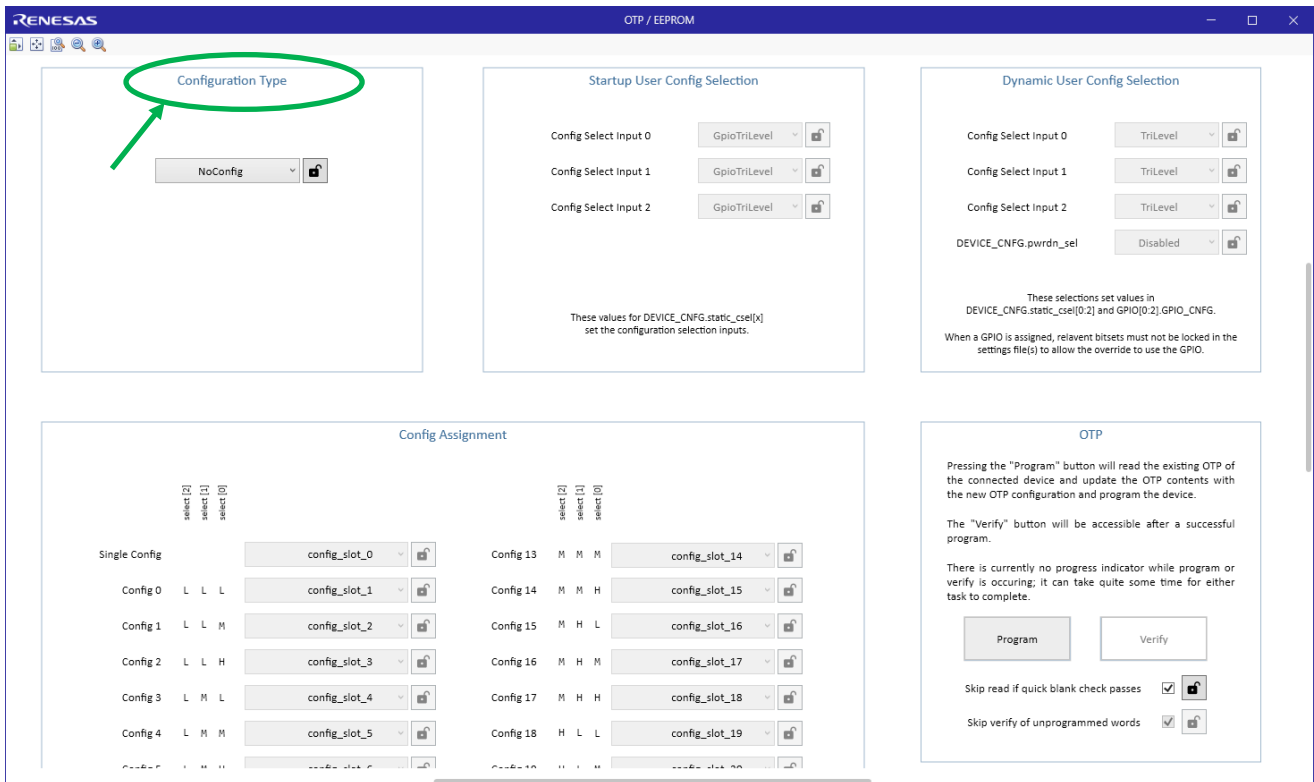


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Click on the *OTP / EEPROM* button.

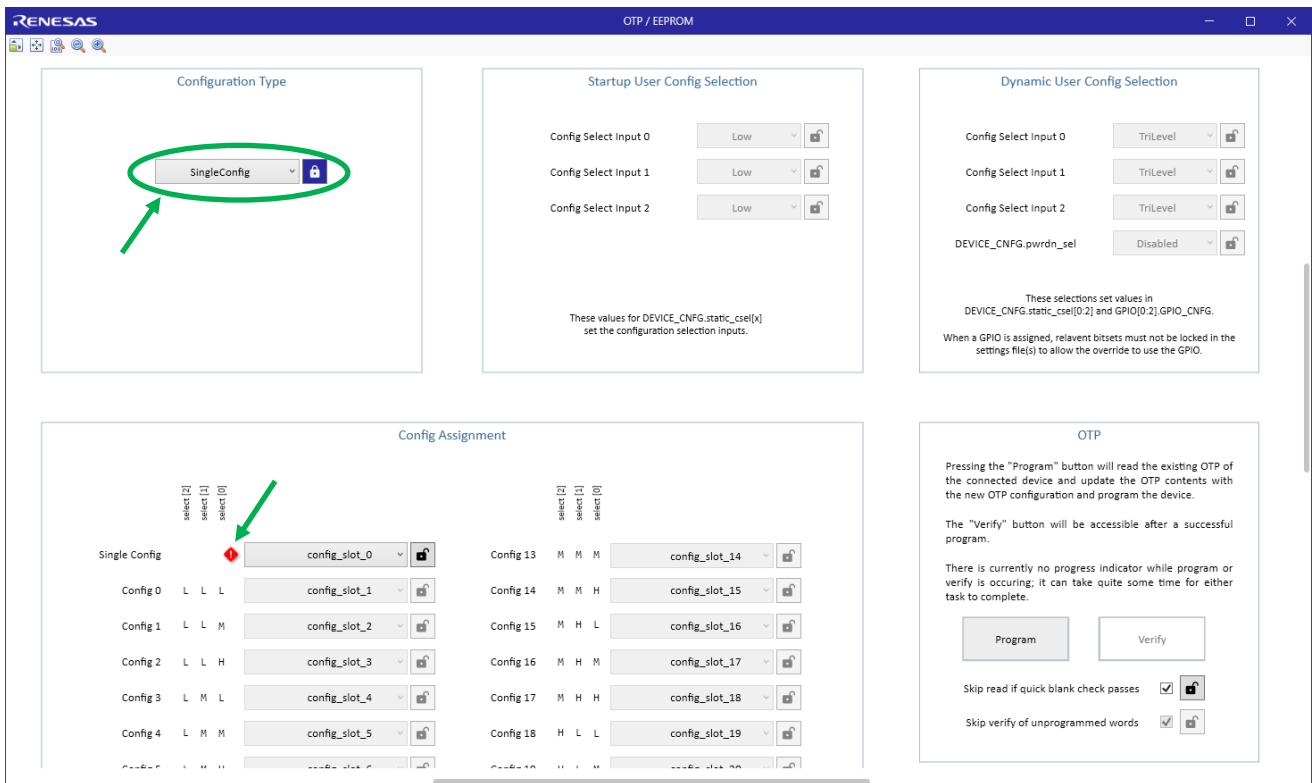


In the sub-diagram window for *OTP / EEPROM*, scroll up until you can see the *Configuration Type* section.

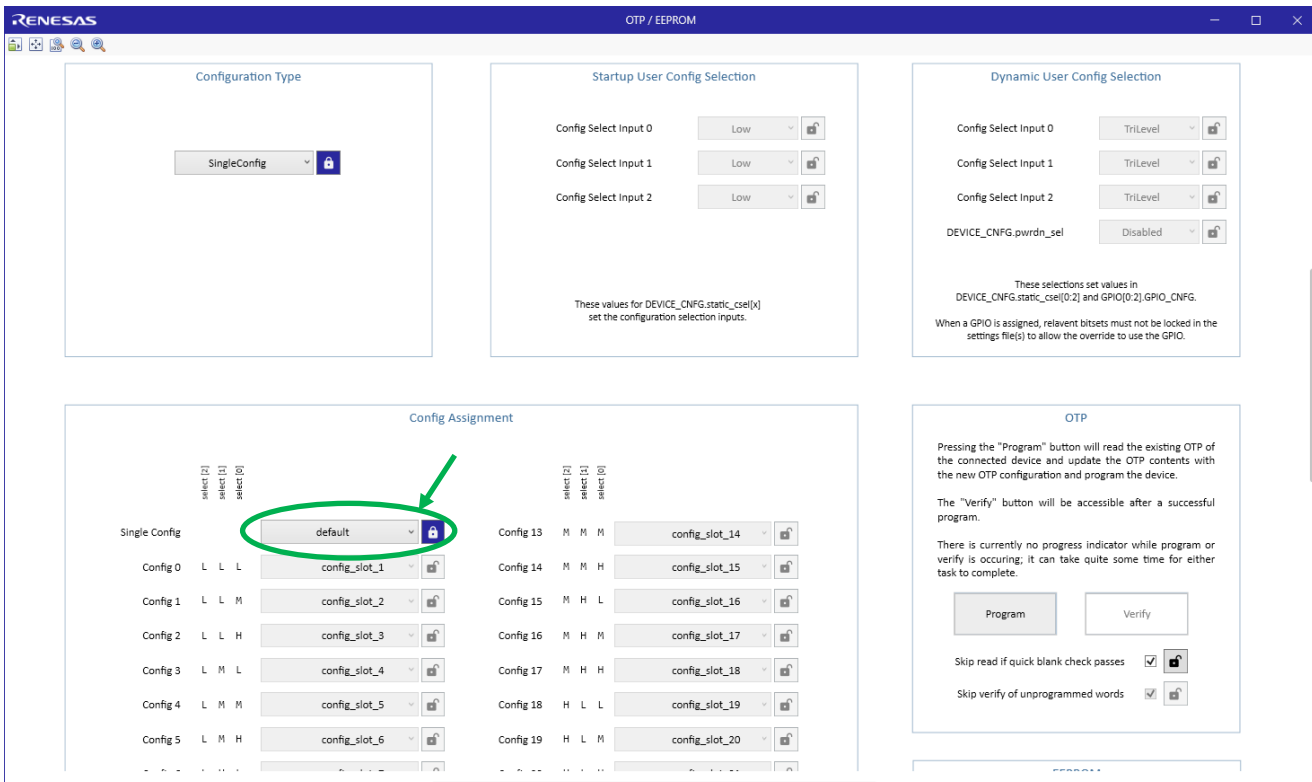


## VersaClock 7 Single and Static Multi Config Setup using RICBox

Next, select “SingleConfig” from the pull-down menu. You will see that a red “error” icon is showing.



From the Single Config pull-down menu, select “default”.



## VersaClock 7 Single and Static Multi Config Setup using RICBox

Scroll down to locate the “Filename to export EPROM as” box. Provide the full filename to export the EEPROM image. If only a filename is provided, then the EEPROM file will be saved in the RICBox install directory.

RENASAS OTP / EEPROM

These selections set values in DEVICE\_CFG.static\_cse[0:2] and GPIO[0:2].GPIO\_CFG. When a GPIO is assigned, relevant bitsets must not be locked in the settings file(s) to allow the override to use the GPIO.

Config Assignment

select [1] select [0]	select [1] select [0]	select [1] select [0]	select [1] select [0]
Single Config	default		
Config 0	L L L	config_slot_1	
Config 1	L L M	config_slot_2	
Config 2	L L H	config_slot_3	
Config 3	L M L	config_slot_4	
Config 4	L M M	config_slot_5	
Config 5	L M H	config_slot_6	
Config 6	L H L	config_slot_7	
Config 7	L H M	config_slot_8	
Config 8	L H H	config_slot_9	
Config 9	M L L	config_slot_10	
Config 10	M L M	config_slot_11	
Config 11	M L H	config_slot_12	
Config 12	M M L	config_slot_13	
Config 13	M M M	config_slot_14	
Config 14	M M H	config_slot_15	
Config 15	M H L	config_slot_16	
Config 16	M H M	config_slot_17	
Config 17	M H H	config_slot_18	
Config 18	H L L	config_slot_19	
Config 19	H L M	config_slot_20	
Config 20	H L H	config_slot_21	
Config 21	H M L	config_slot_22	
Config 22	H M M	config_slot_23	
Config 23	H M H	config_slot_24	
Config 24	H H L	config_slot_25	
Config 25	H H M	config_slot_26	
Config 26	H H H	config_slot_27	

OTP

Pressing the "Program" button will read the existing OTP of the connected device and update the OTP contents with the new OTP configuration and program the device.

The "Verify" button will be accessible after a successful program.

There is currently no progress indicator while program or verify is occurring; it can take quite some time for either task to complete.

Program Verify

Skip read if quick blank check passes

Skip verify of unprogrammed words

EEPROM

Build EPROM for this OTP dash code

001 (AT24C16 at address 0x50)

Filename to export EPROM as

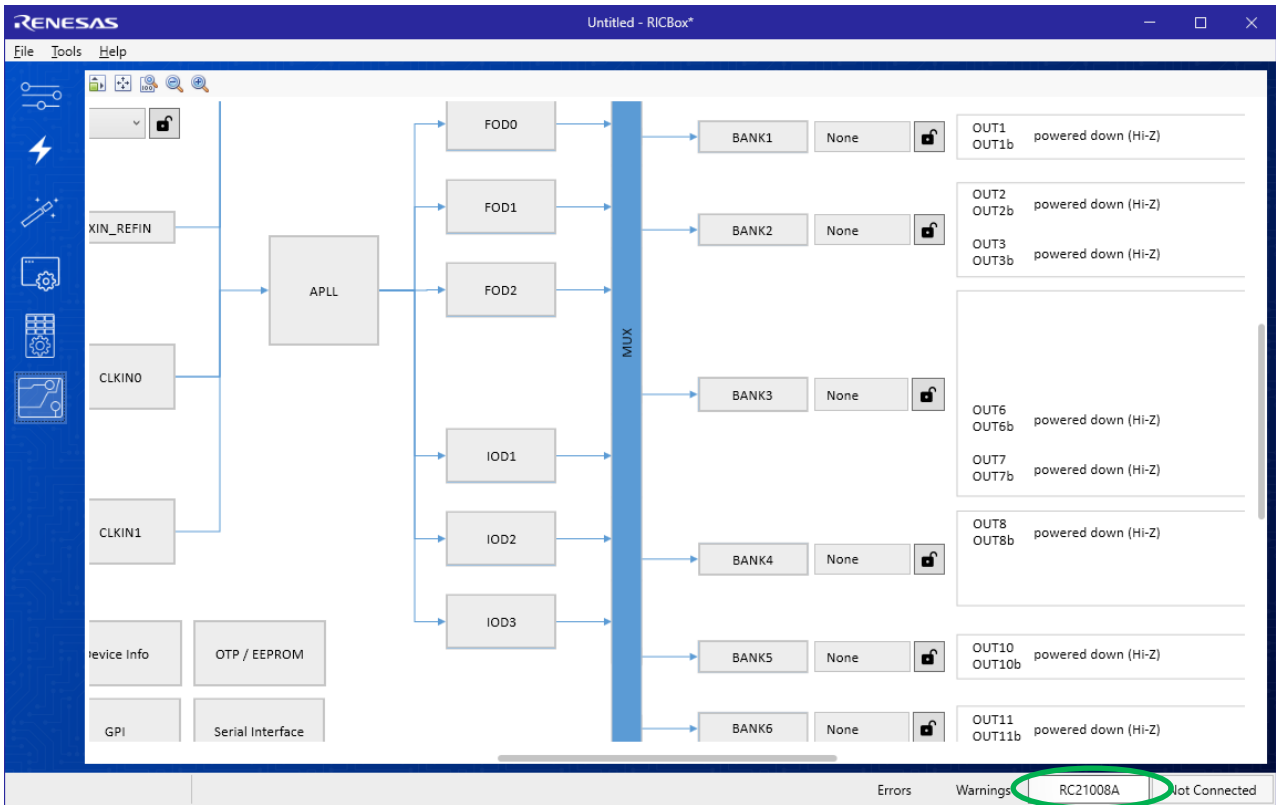
C:\temp\RC21008A.hex

Export Program

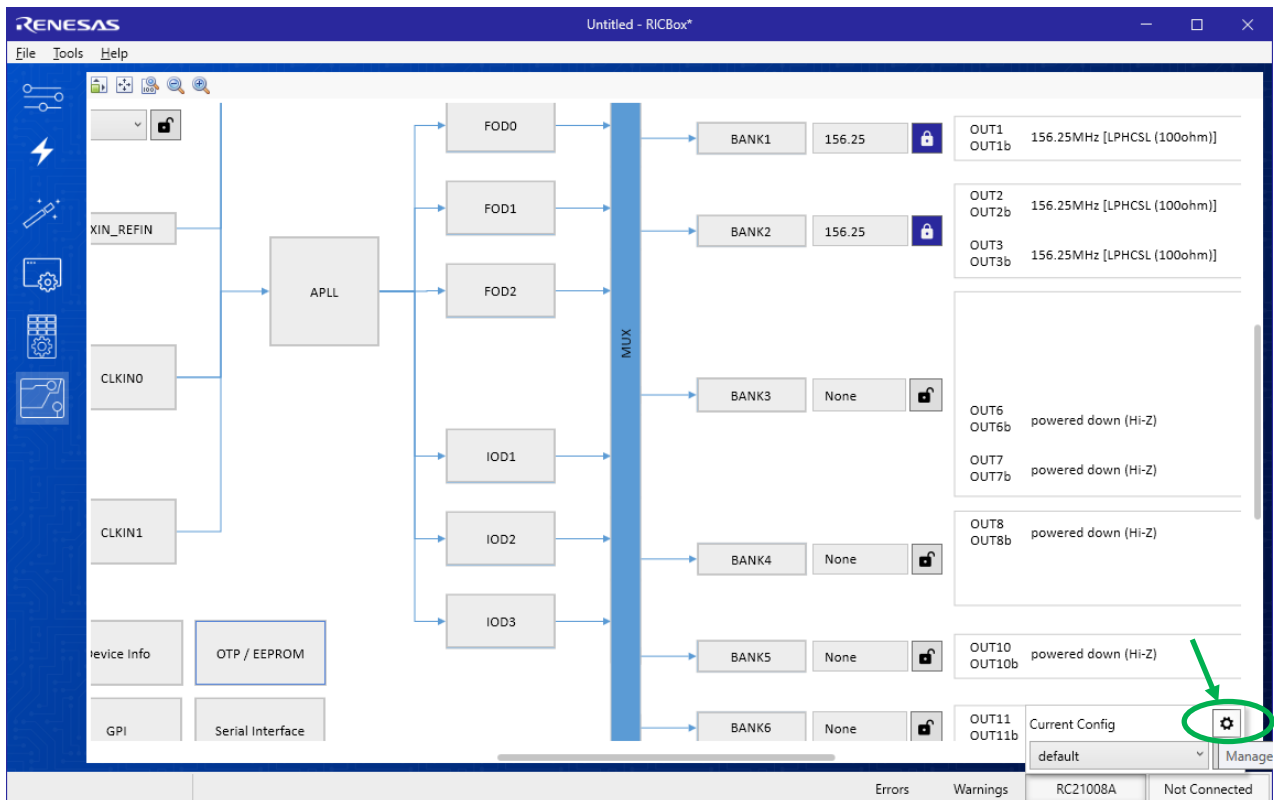
RBS files for each variant in Single Config mode can be downloaded from the [VersaClock Programmable Clocks](#) page.

## 4. Enable Static Multi Config

Before navigating to the OTP / EEPROM, more configs will need to be created. In the lower right of the GUI and to the left of the “Not Connected” button, click the button that is displaying the VC7 product.



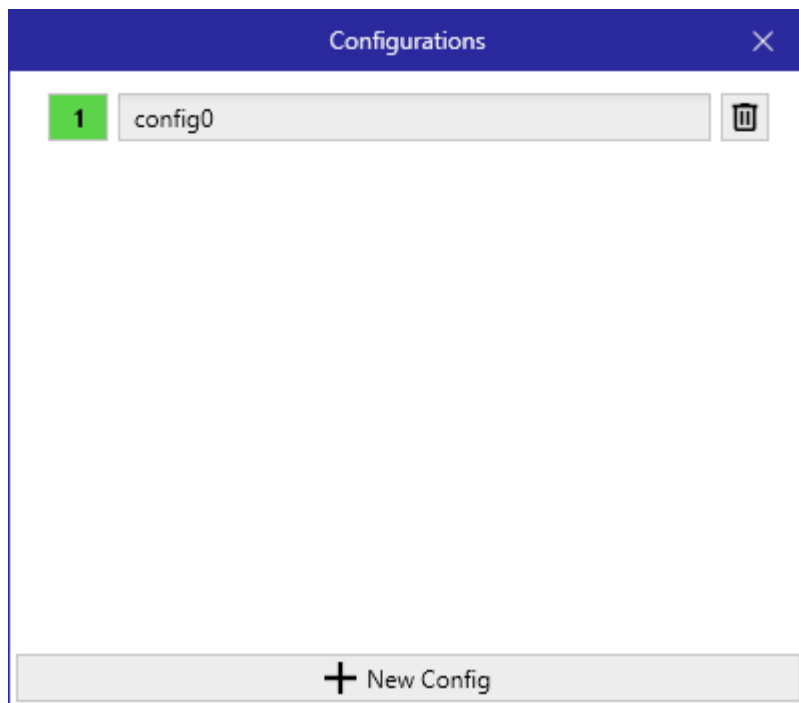
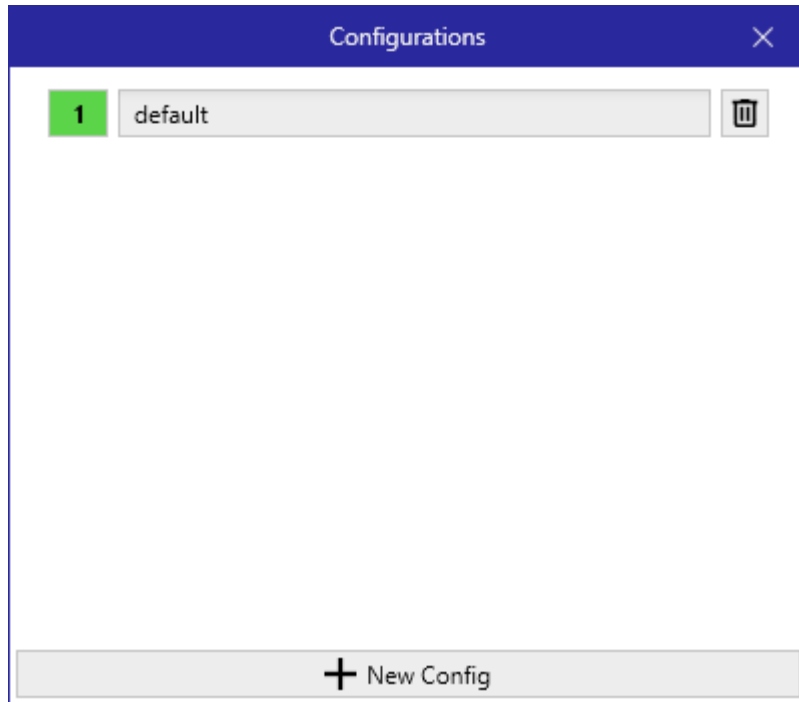
In the lower right corner of the pop-up menu, click on the gear icon to manage configs.



## VersaClock 7 Single and Static Multi Config Setup using RICBox

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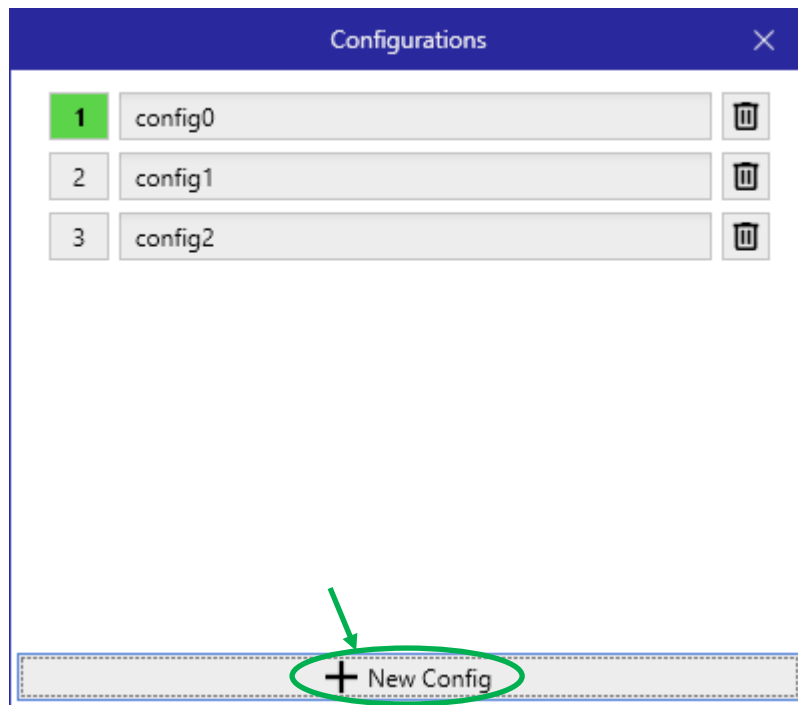
In the Configurations sub-window, configs can be renamed and more configs can be added. For this example, “default” config will be renamed to “config0”. Click inside the box, delete “default”, and then type in the new name, “config0”.



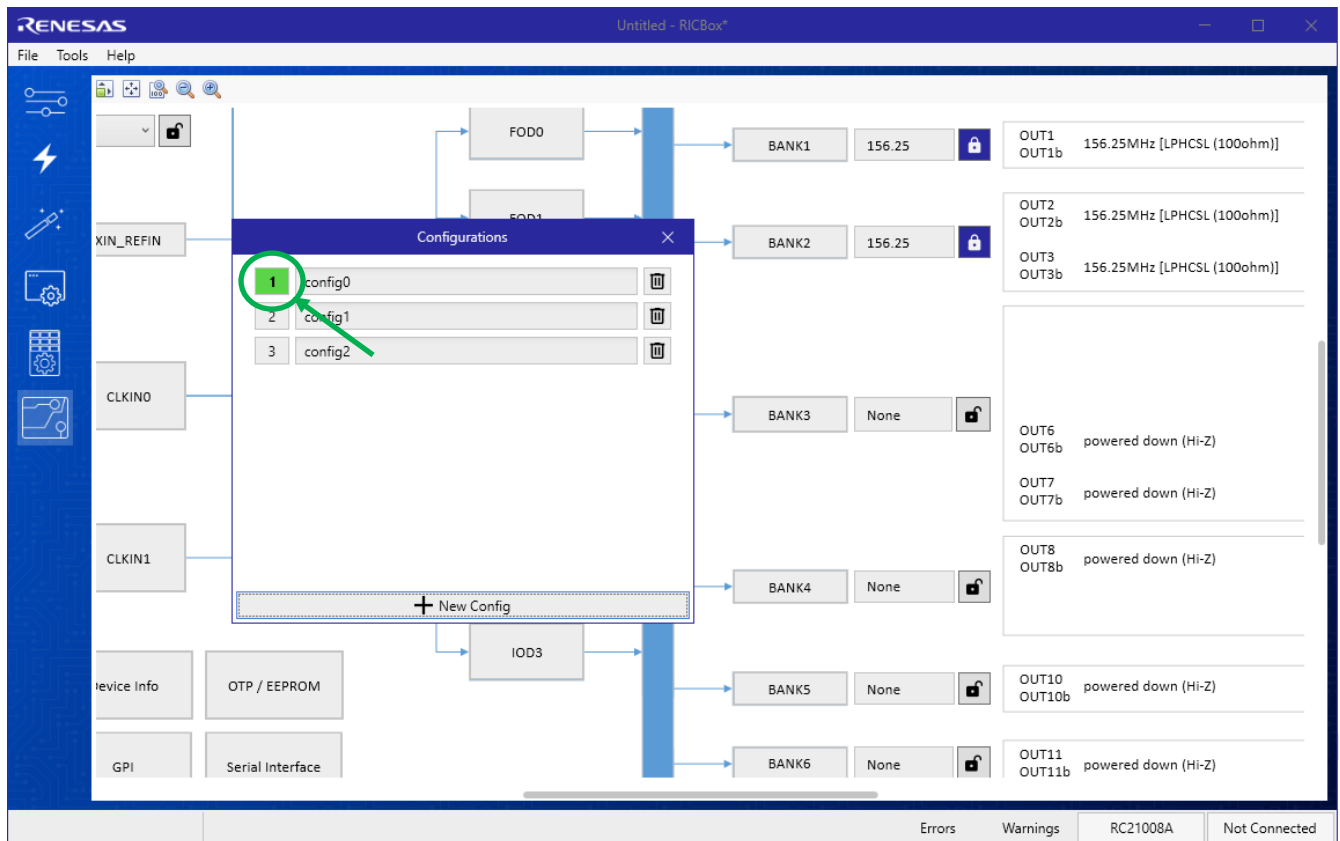


## VersaClock 7 Single and Static Multi Config Setup using RICBox

Click the “New Config” button to add a new config. For this example, 2 more configs will be added.

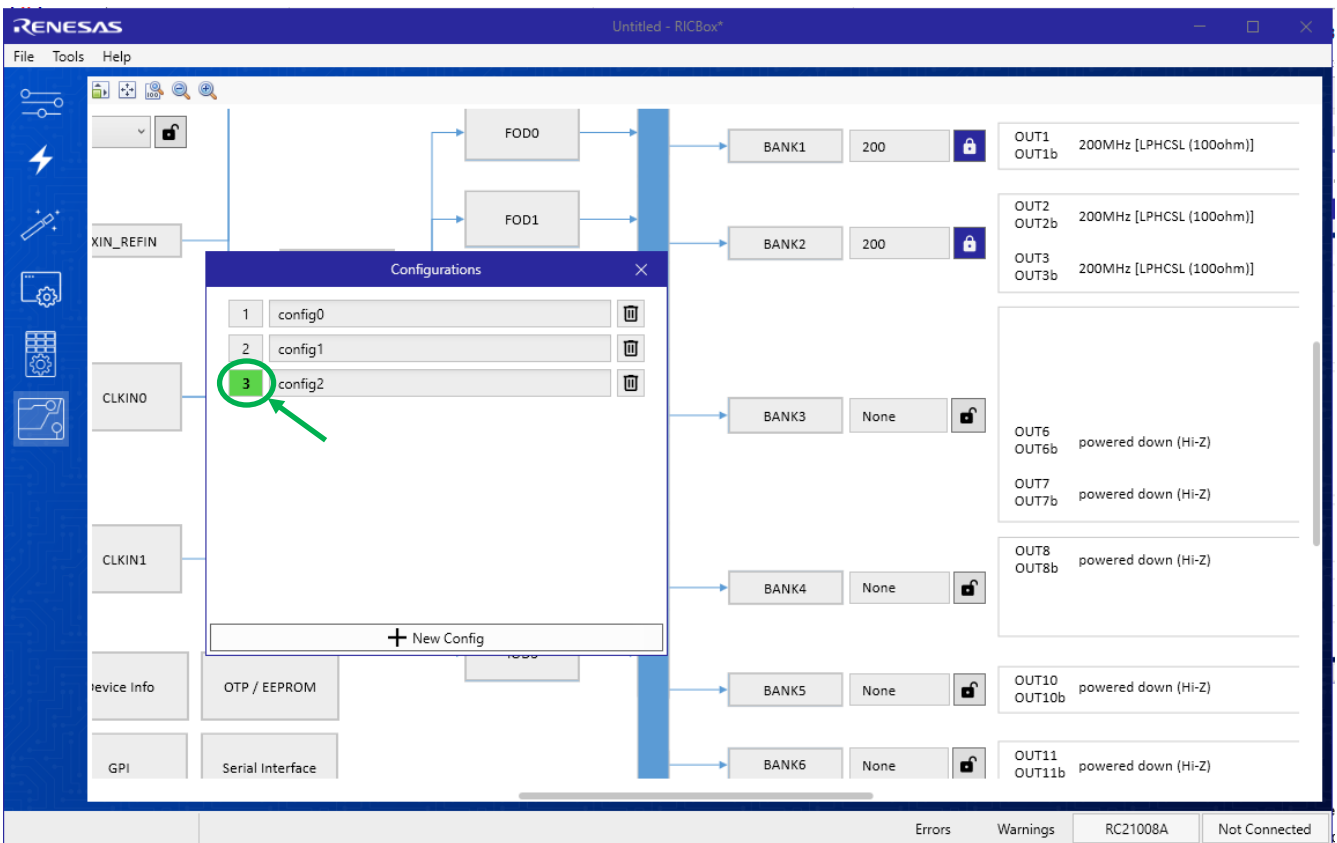
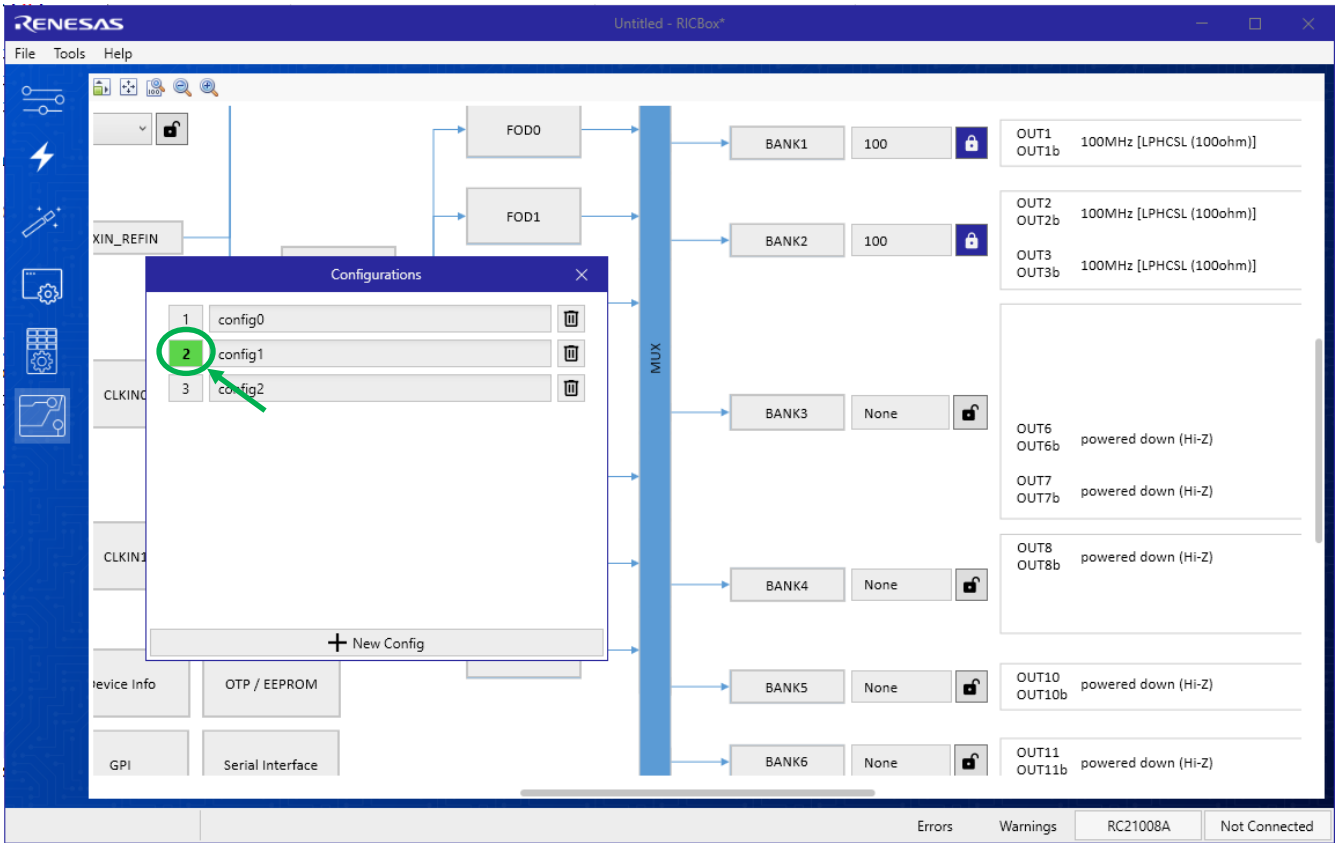


The green box on the config number identifies the config that is active in the block diagram.



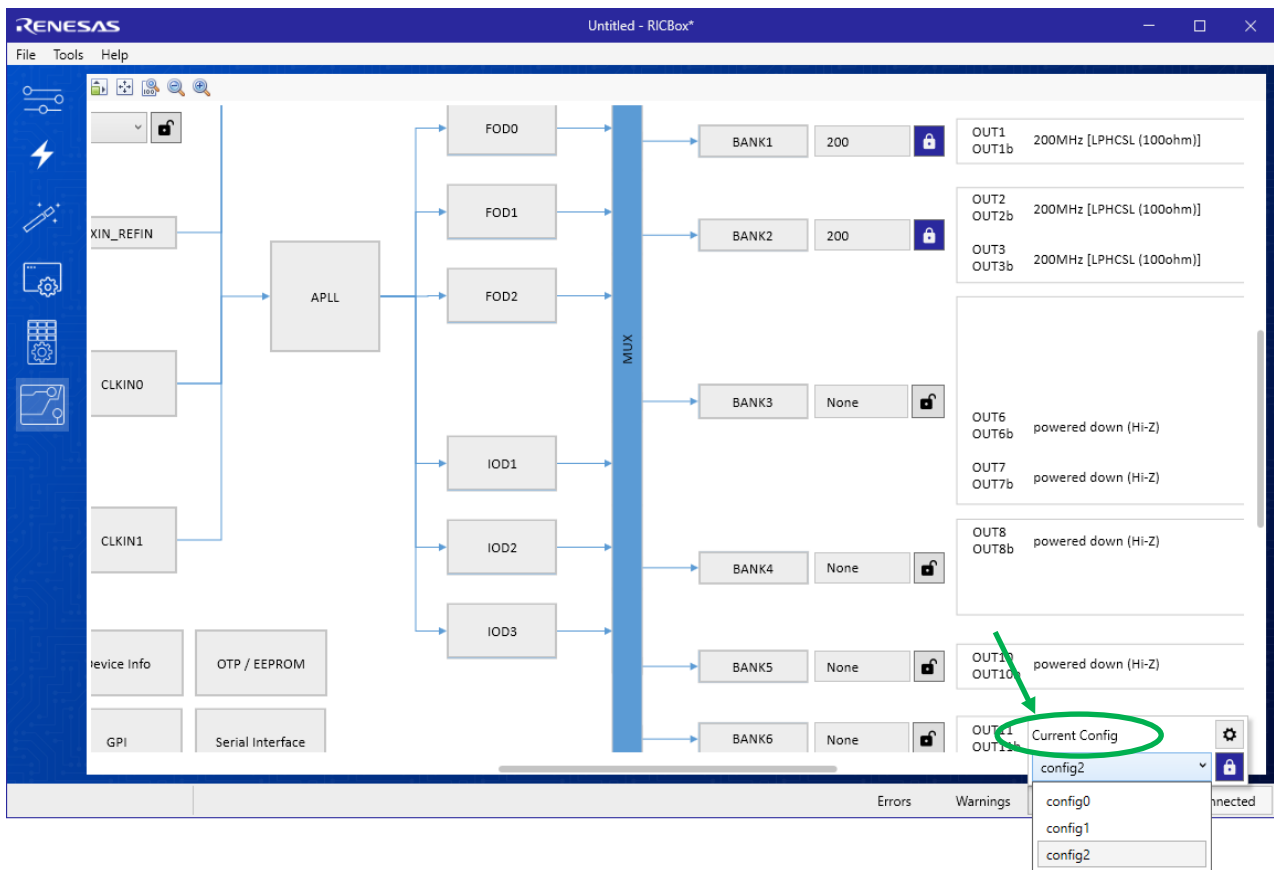
# VersaClock 7 Single and Static Multi Config Setup using RICBox

Click on the “2” to make changes and configure “config1” to meet the application needs. Do the same for “config2”.

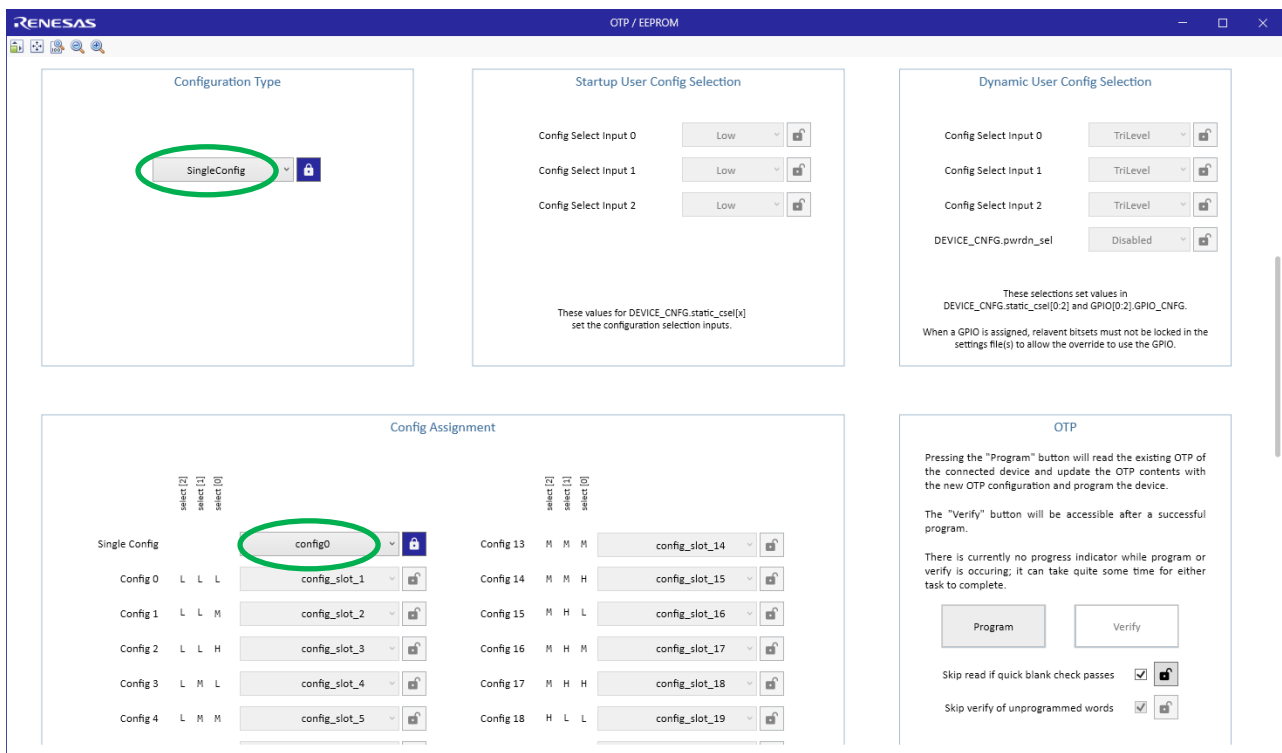


## VersaClock 7 Single and Static Multi Config Setup using RICBox

Configs can also be selected from the “Current Config” pull-down menu.

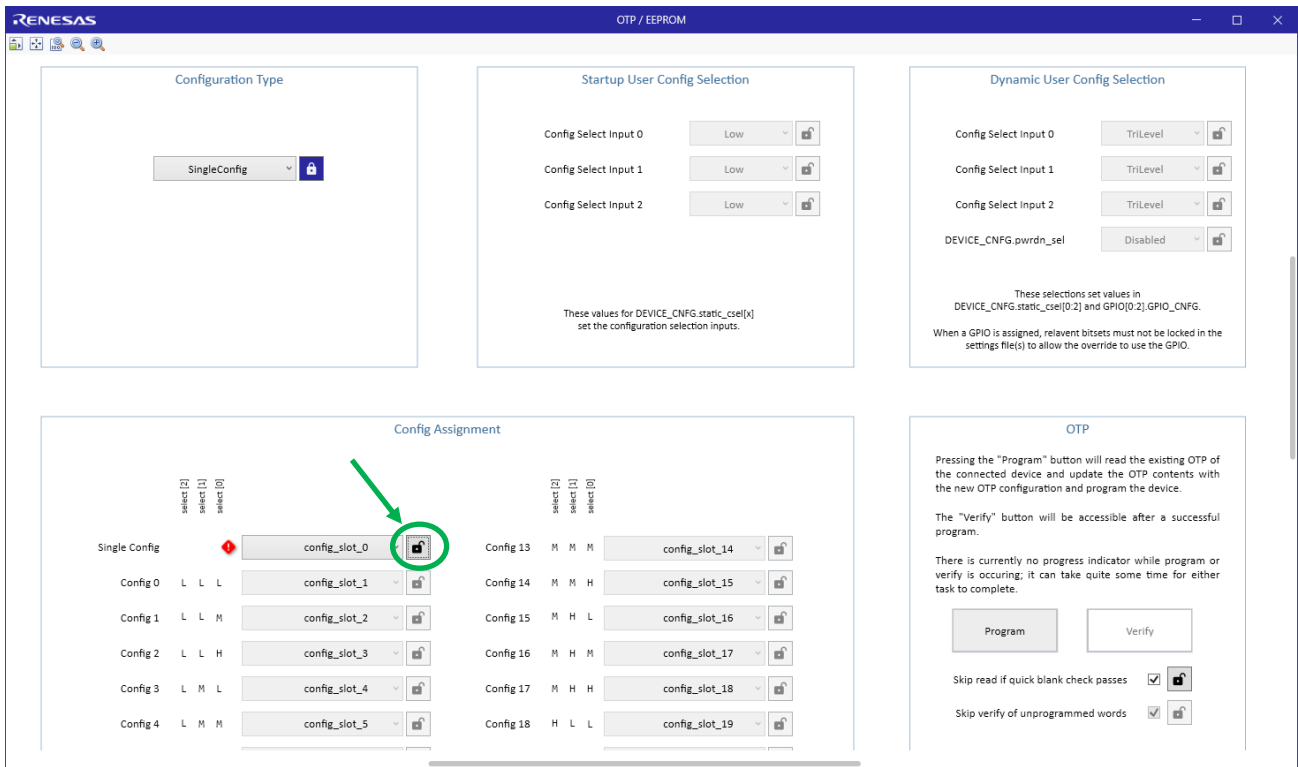


Once all configs have been created, click the *OTP / EEPROM* button. In the OTP/EEPROM window, scroll up until you can see the Configuration Type section. Currently, “Single Config” is selected for the Configuration Type. In the “Config Assignment > Single Config” section, the config name of “default” was previously displayed. Note “config0” is now displayed as the first config name was updated.

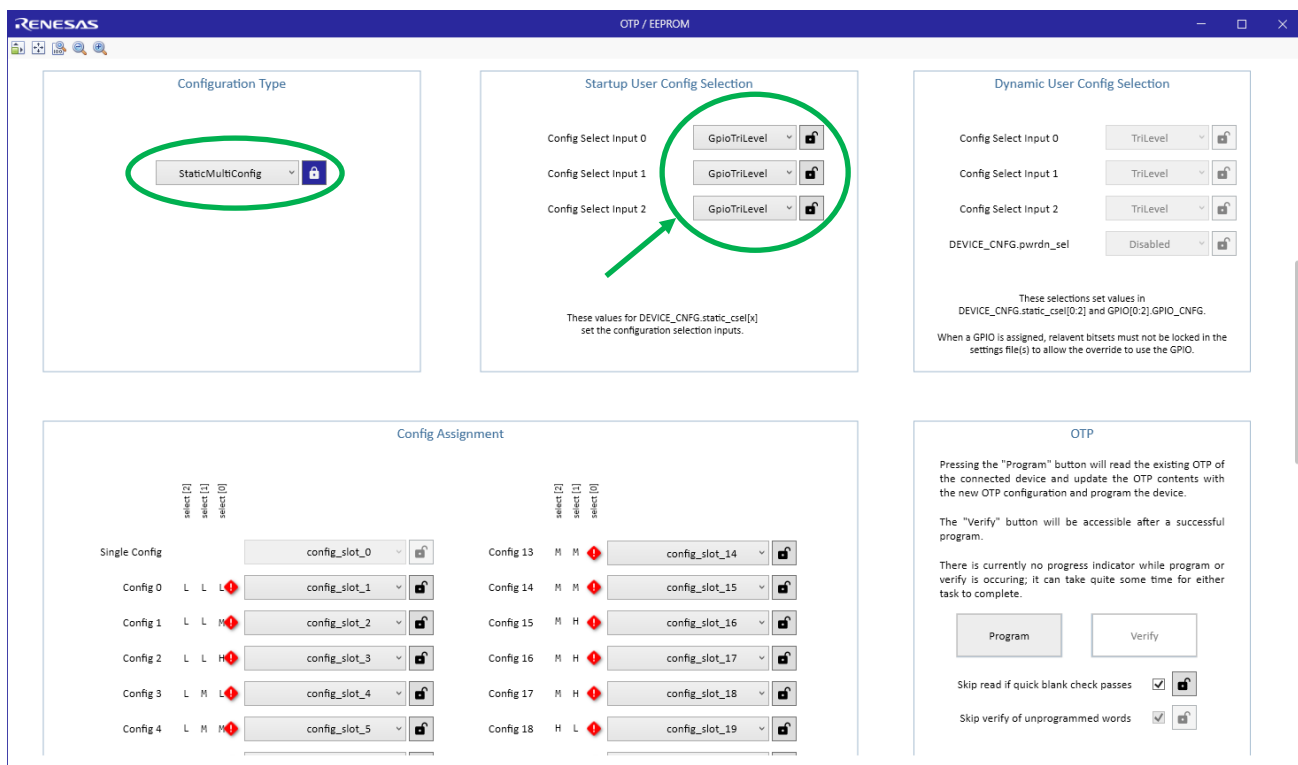


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To switch over to “StaticMultiConfig”, the Single Config selection must be cleared. This can be accomplished by clicking the blue lock icon. This will unlock the field back to default which is no config.

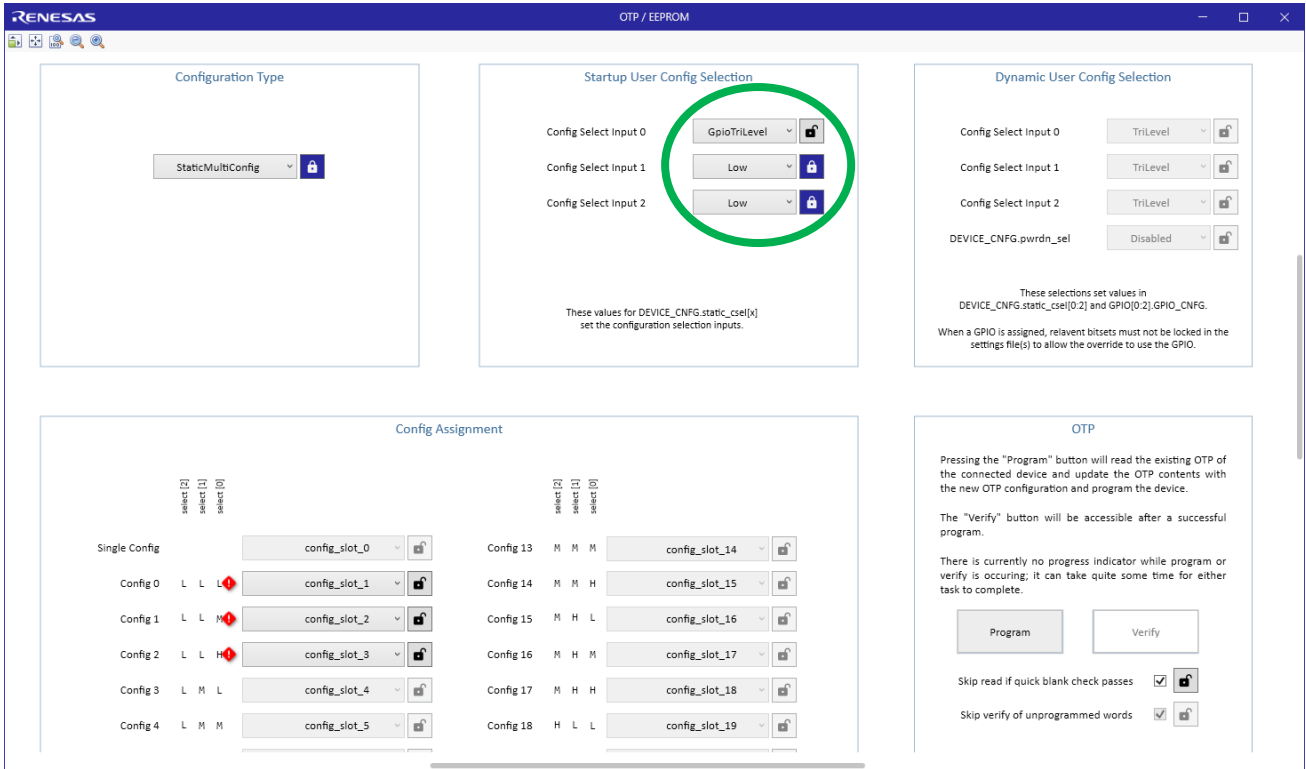


In the Configuration Type section, choose “StaticMultiConfig” from the pull-down menu. The “Startup User Config Selection” area is now enabled. By default, all Config Select input are set to “GpioTri Level”. Tri Level refers to VDD for High, VDD/2 for Mid, and 0 for L. With 3 CSEL bits, there can be a total of 3 x 3 x 3 different configs. As a result, all config slots must be given a config.

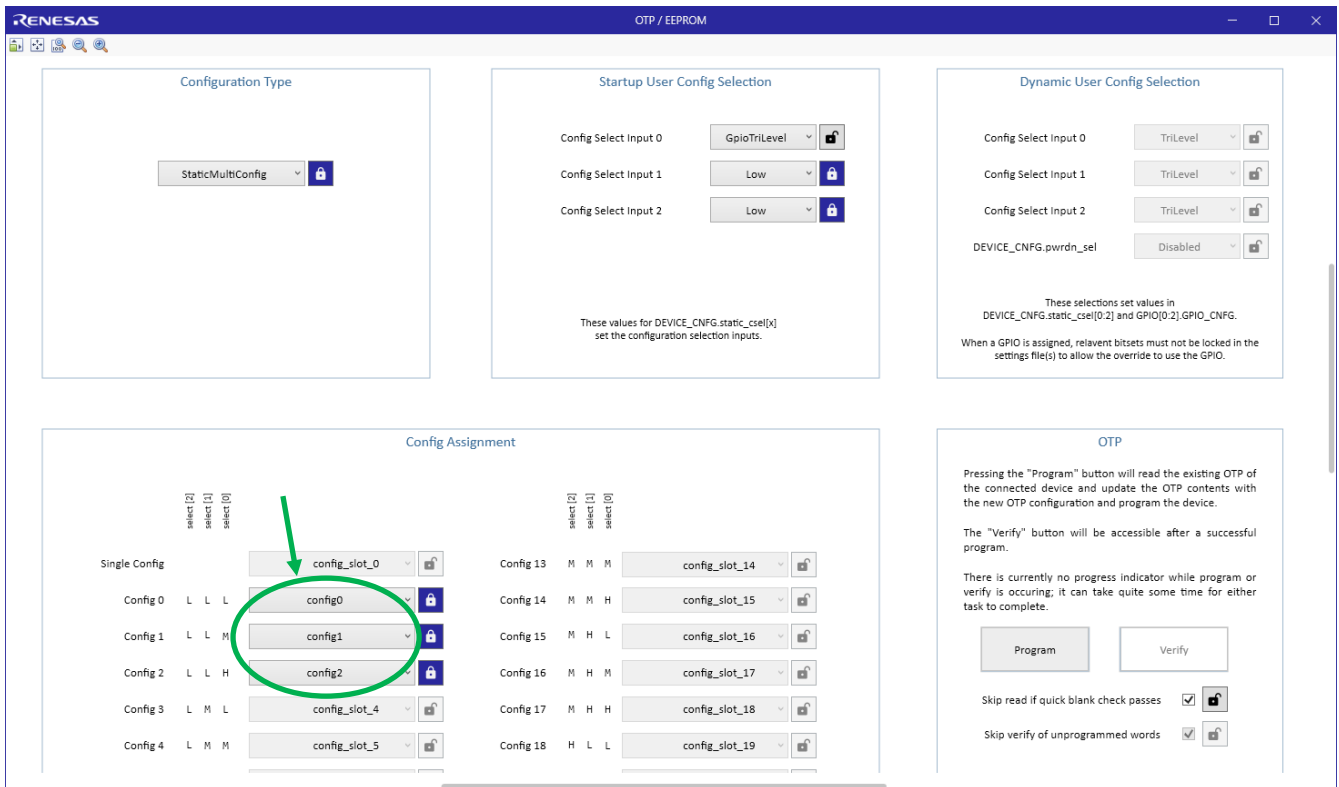


# VersaClock 7 Single and Static Multi Config Setup using RICBox

Forcing a CSEL to Low, Mid, or High will ignore the voltage state of the pin at power on. For this example, Config Select Input 1 and 2 will be set to "Low". Now only the first 3 slots need defining as these 3 slots have CSEL2 and CSEL1 set to L. Even though CSEL are set to L, they can still have other functions assigned.

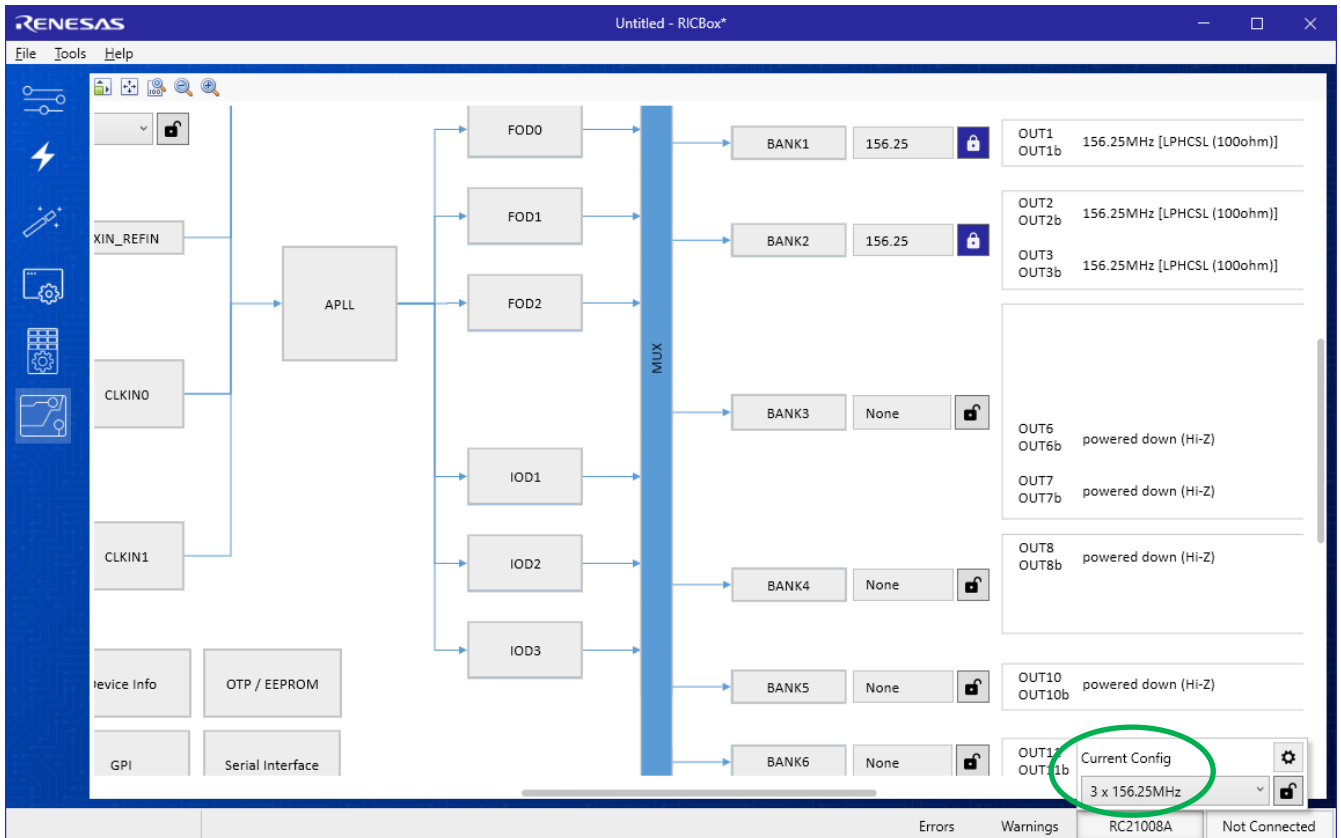
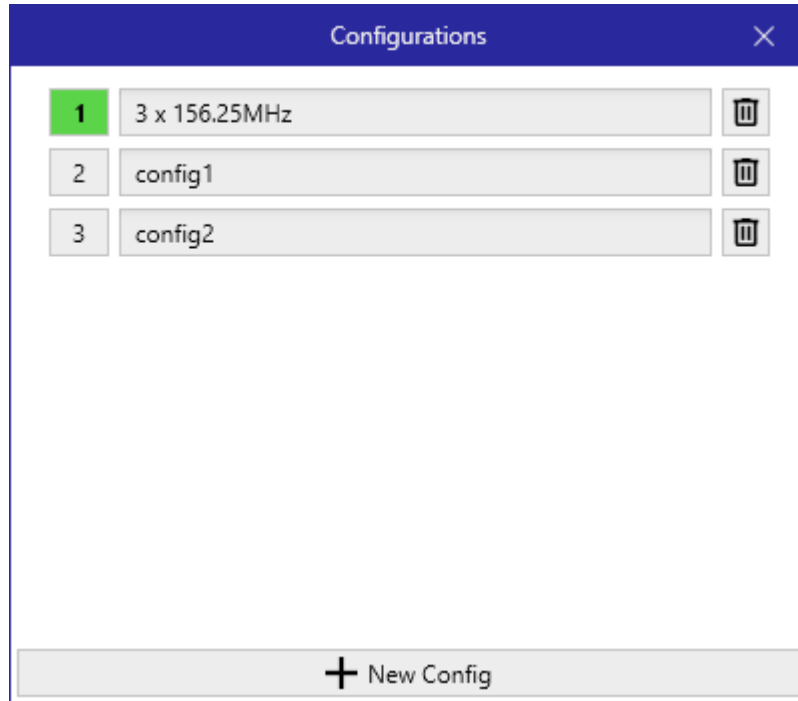


For each enabled pull-down box, select a config.

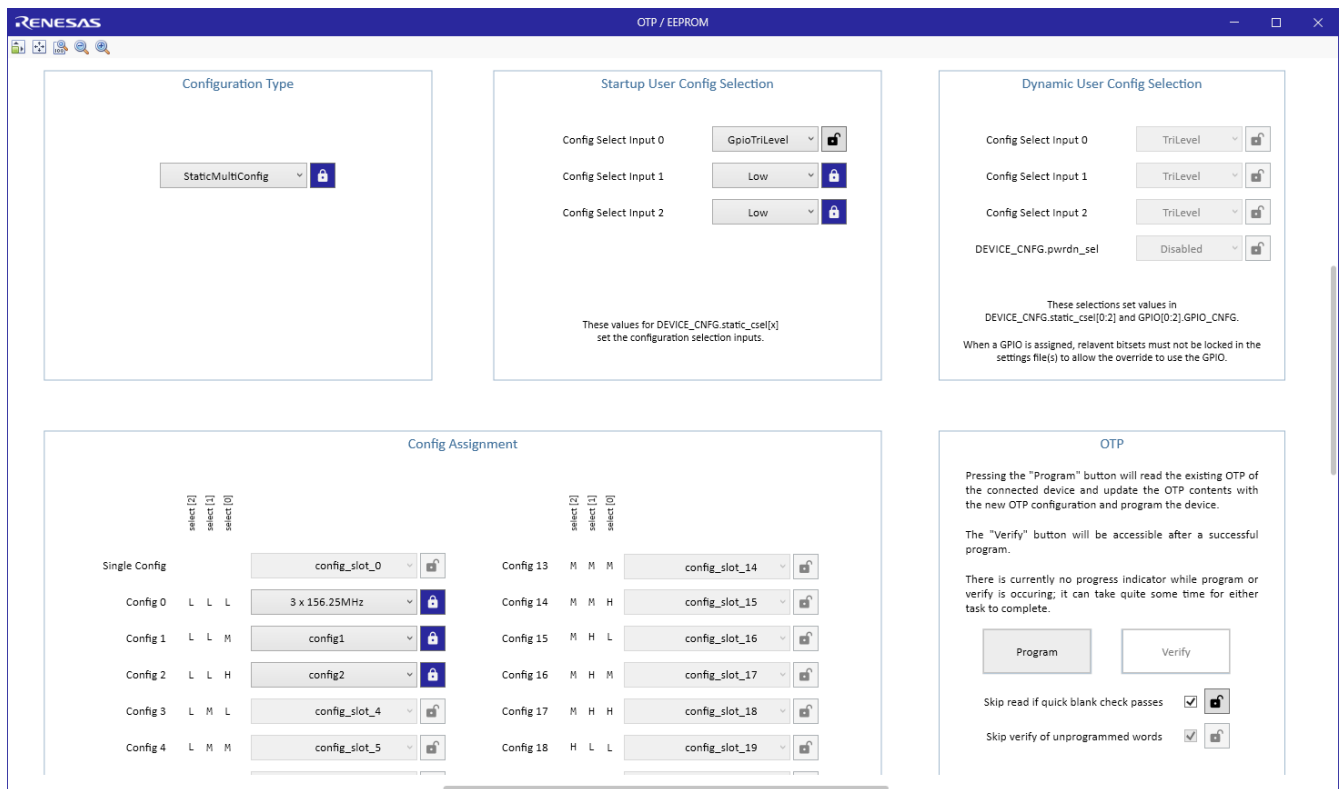


## VersaClock 7 Single and Static Multi Config Setup using RICBox

Configs may go into any slot. Also, any given config should have a more descriptive name. For example, "config0" could become "3 x 156.25MHz".



Config name updates are automatic.



## 5. Static vs Dynamic Config Selection

For static multi config selection, the voltage state of GPIO0–2 is latched at power up. Then, the corresponding OTP slot is loaded, at which point GPIO0–2 can take on another function. In dynamic multi config selection, GPIO0–2 is dedicated full time to loading the selected OTP config slot. One use of dynamic multi config selection would be to have a config where all outputs are set to LPHCSL at 100MHz. That config can then be copied to another config where SSC is turned on. In dynamic config slot 0, the non-SSC config can be set. In config slot 1, the SSC can be set. With this setup, the GPIO0 can be used to turn on/off SSC. With static multi config selection, the same function can be achieved if another GPIO is set up as a PWRGD/Restart#. By toggling the PWRGD/Restart# function, the state of GPIO0–2 will be relatched. The downside is the extra step required to toggle the PWRGD/Restart#.

Only in dynamic multi config selection is low power mode supported. One of the GPIO0–2 can be set up as the PWRGD/PWRDN# function. When the selected GPIO is held low, the low power config is loaded. When released, the normal power config is loaded. More detail can be found in the Dynamic Multi Config selection applications note.

## 6. Revision History

Revision	Date	Description
1.00	Mar 8, 2023	Initial release.