

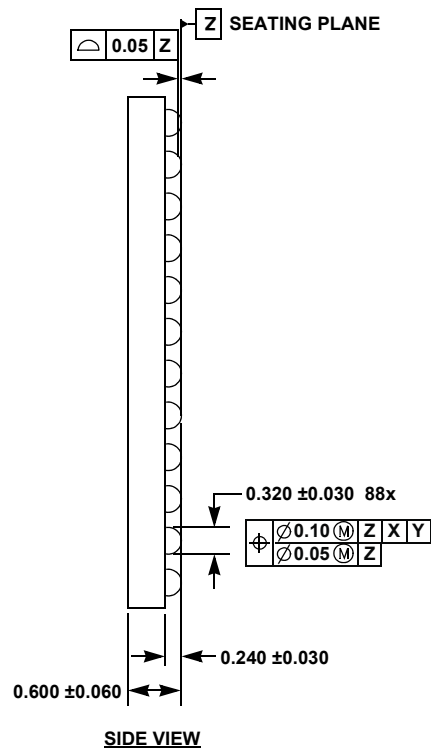
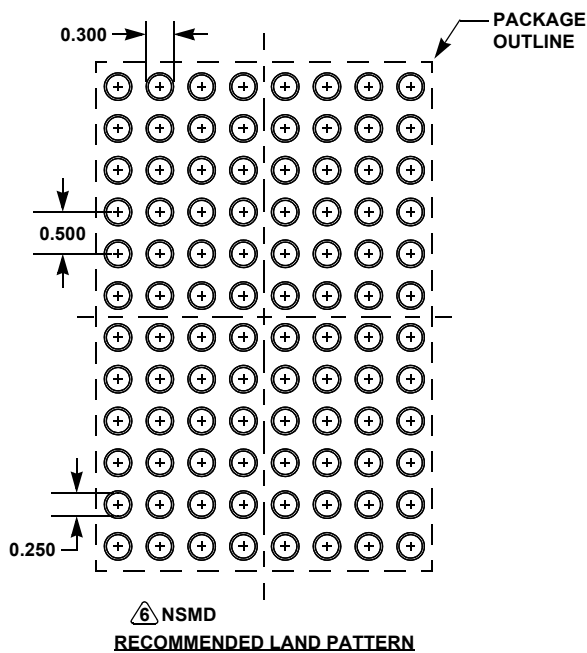
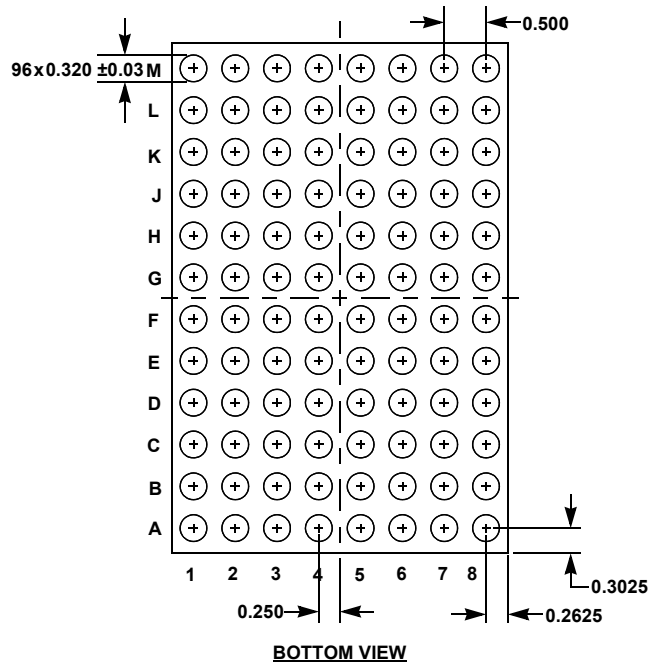
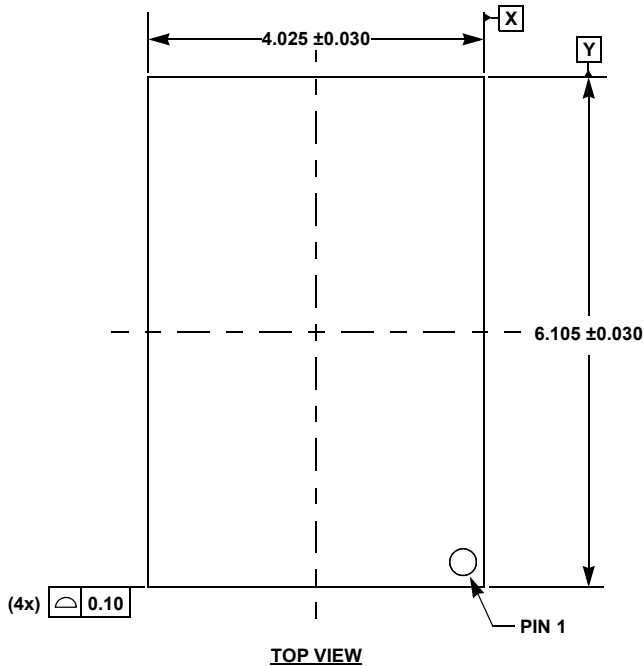
Plastic Packages for Integrated Circuits

Package Outline Drawing

W8x12.96

96 Ball Wafer Level Chip Scale Package (WLCSP 0.5mm Pitch)

Rev 0, 10/15



NOTES:

- All dimensions are in millimeters.
- Dimensions and tolerance per ASMEY 14.5-1994.
- Primary datum Z and seating plane are defined by the spherical crowns of the bump.
- Dimension is measured at the maximum bump diameter parallel to primary datum Z.
- Bump position designation per JESD 95-1, SPP-010.

\triangle NSMD refers to non-solder mask defined pad design per Intersil Techbrief [TB451](#).