



CONFIGURATION SHOWN =
 SMBus ADDRESS=0xC2
 SIDE BAND ENABLED

85 Ohm Differential

-v = Internal pull-down resistor
 -^v = Internal pull-up/down resistor

Tri-Level Inputs		
SMB_A1	SMB_A0	Add
0	0	D8
0	M	DA
0	1	DE
M	0	C2
M	M	C4
M	1	C6
1	0	CA
1	M	CC
1	1	CE

NOTE: FERRITE BEADS =

Manufacture	Part Number	Z@100MHz	PkgSz	DC_res.	Current (Ma)
muRata	BLM21A601R	600	0805	0.30	600
TDK	MMZ2012S601A	600	0805	0.30	600
STEWART	HZ0805E601R	600	0805	0.30	600
AssocCmpTch	CBG0805-600-50	600	0805	0.30	600

Revision history	
Rev	Change
0.1	First publication

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9QXL2001C Schematic		
Size B	Document Number	Rev 0.1
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