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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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2SK1300

Silicon N Channel MOS FET

REJ03G0919-0200
(Previous: ADE-208-1258)
Rev.2.00
Sep 07, 2005

Application

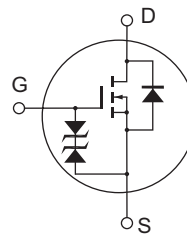
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device
 - Can be driven from 5 V source
- Suitable for motor drive, DC-DC converter, power switch and solenoid drive

Outline

RENESAS Package code: PRSS0004AC-A
(Package name: TO-220AB)



1. Gate
2. Drain
(Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	10	A
Drain peak current	I _{D(pulse)} ^{*1}	40	A
Body to drain diode reverse drain current	I _{DR}	10	A
Channel dissipation	P _{ch} ^{*2}	40	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. Value at T_C = 25°C

Electrical Characteristics

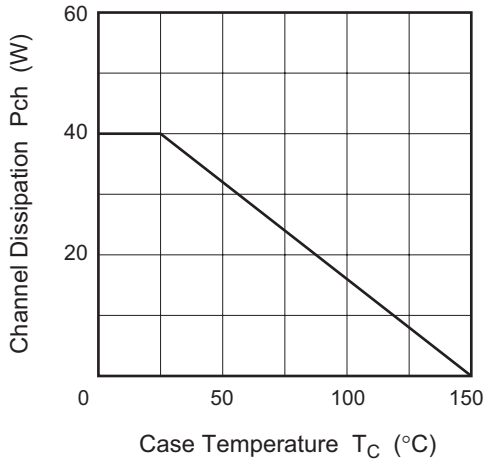
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	100	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	250	μA	V _{DS} = 80 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.0	—	2.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	0.20	0.25	Ω	I _D = 5 A, V _{GS} = 10 V ^{*3}
		—	0.25	0.35	Ω	I _D = 5 A, V _{GS} = 4 V ^{*3}
Forward transfer admittance	y _{fs}	4.5	7.0	—	S	I _D = 5 A, V _{DS} = 10 V ^{*3}
Input capacitance	C _{iss}	—	525	—	pF	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	205	—	pF	
Reverse transfer capacitance	C _{rss}	—	60	—	pF	
Turn-on delay time	t _{d(on)}	—	5	—	ns	I _D = 5 A, V _{GS} = 10 V, R _L = 6 Ω
Rise time	t _r	—	50	—	ns	
Turn-off delay time	t _{d(off)}	—	170	—	ns	
Fall time	t _f	—	75	—	ns	
Body to drain diode forward voltage	V _{DF}	—	1.2	—	V	I _F = 10 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	220	—	ns	I _F = 10 A, V _{GS} = 0, di _F /dt = 50 A/μs

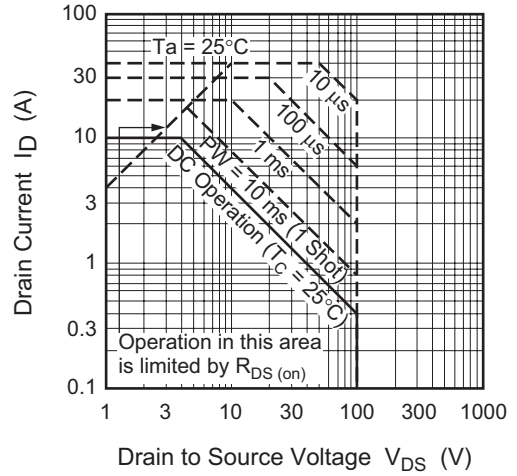
Note: 3. Pulse test

Main Characteristics

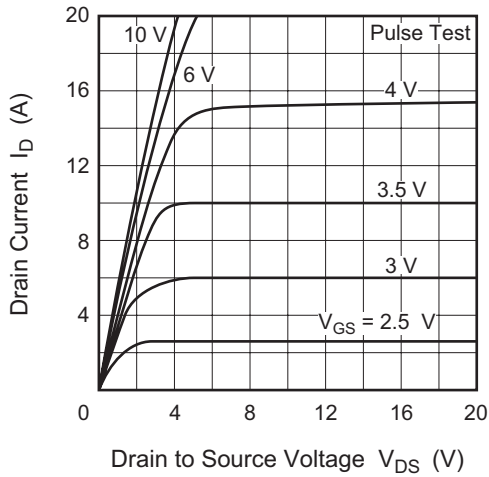
Power vs. Temperature Derating



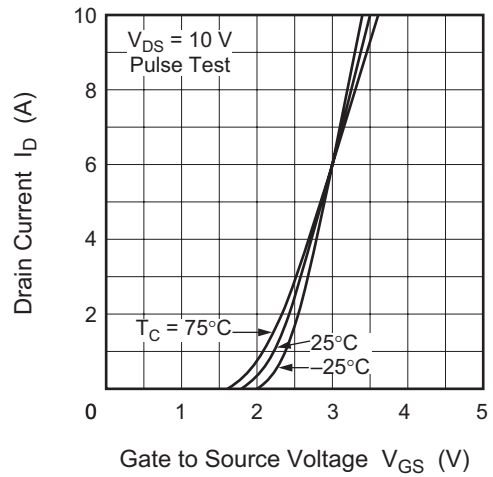
Maximum Safe Operation Area



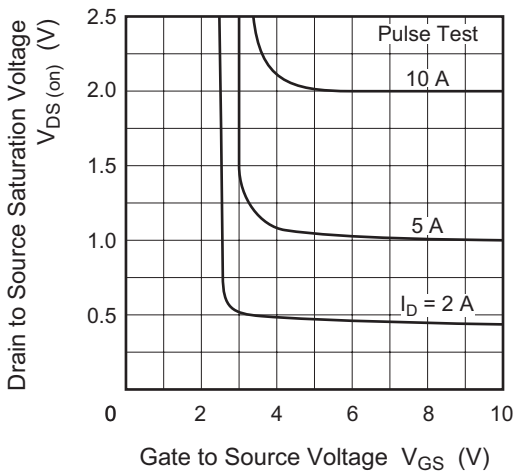
Typical Output Characteristics



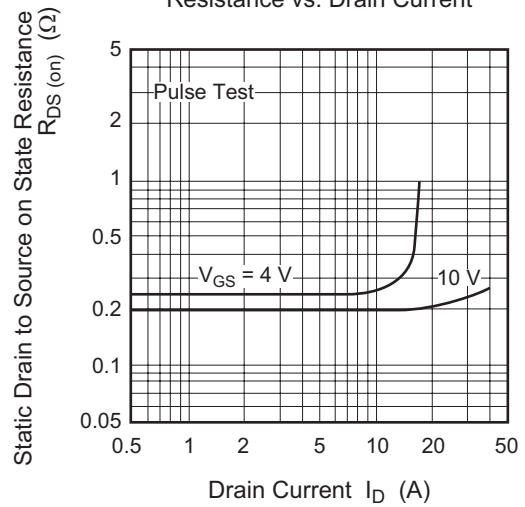
Typical Transfer Characteristics

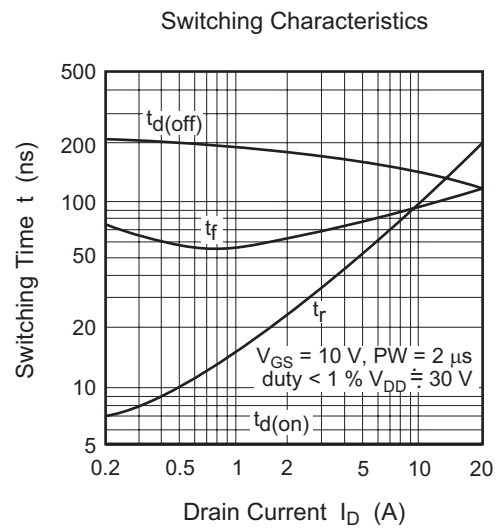
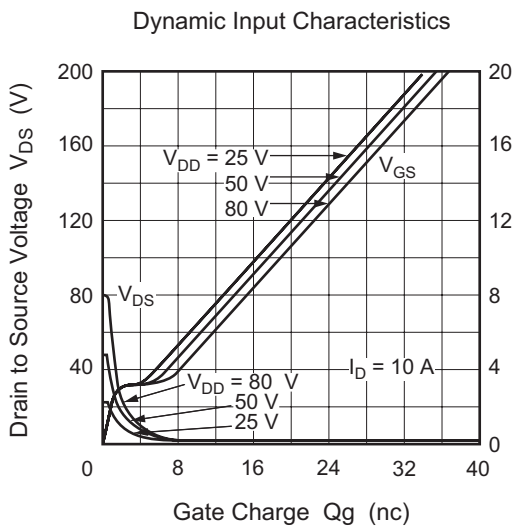
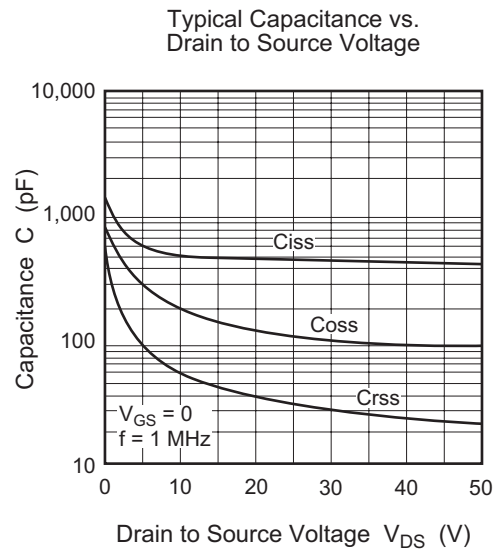
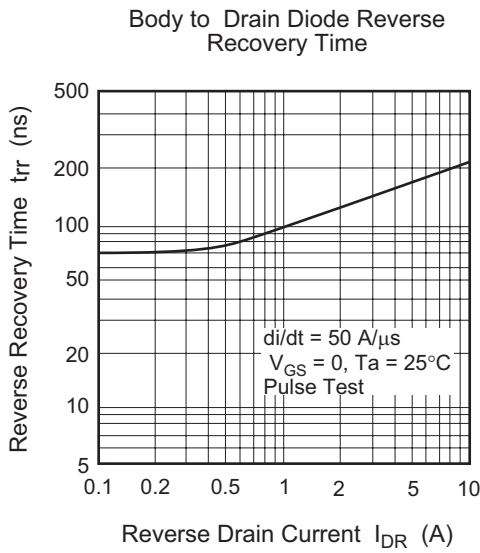
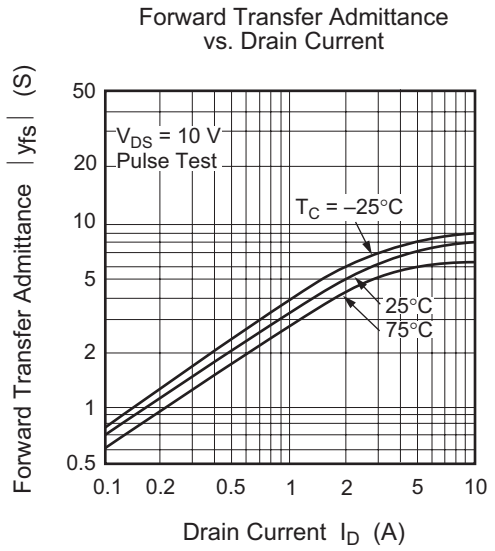
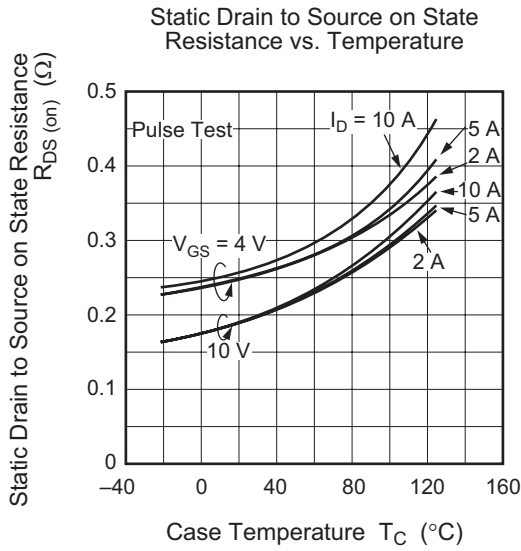


Drain to Source Saturation Voltage vs. Gate to Source Voltage

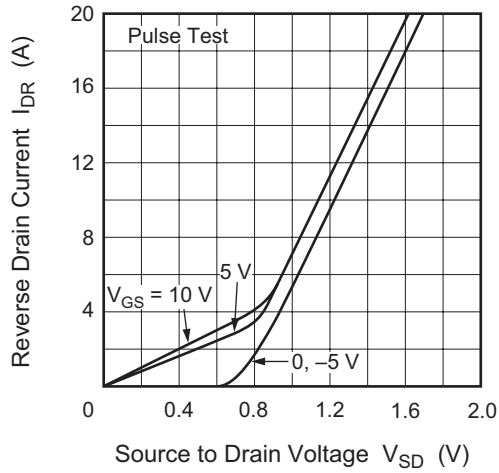


Static Drain to Source on State Resistance vs. Drain Current

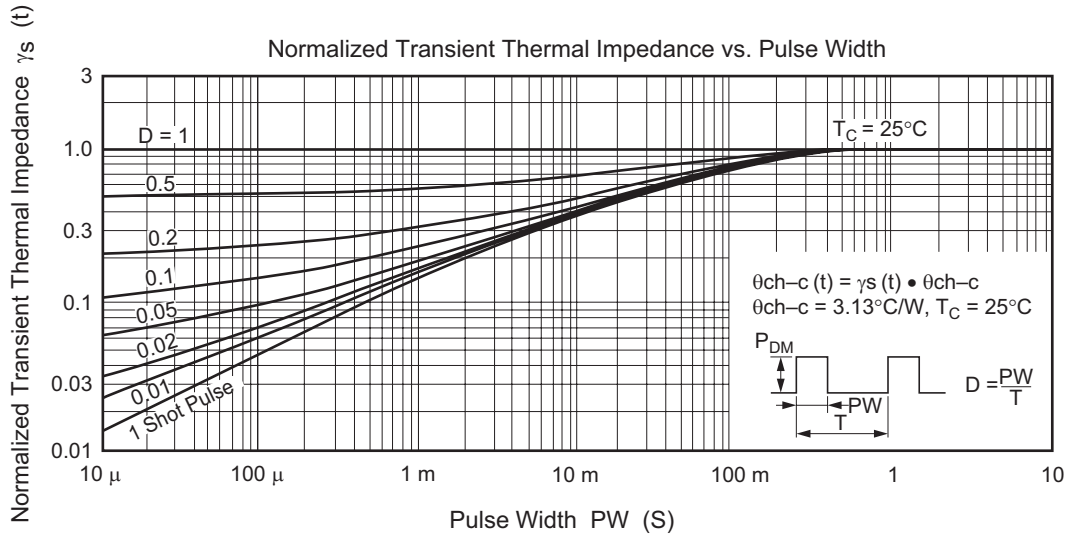




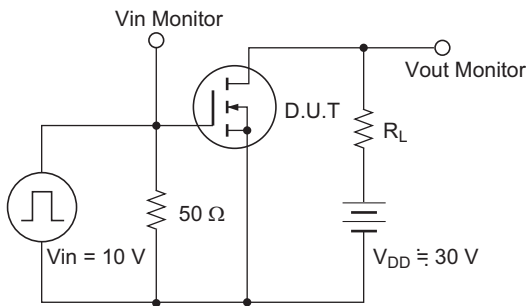
Reverse Drain Current vs. Source to Drain Voltage



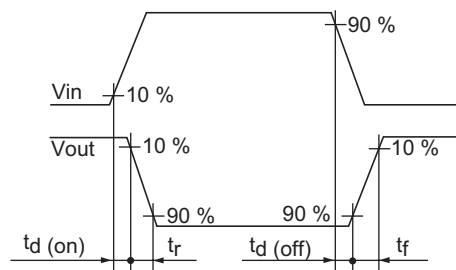
Normalized Transient Thermal Impedance vs. Pulse Width



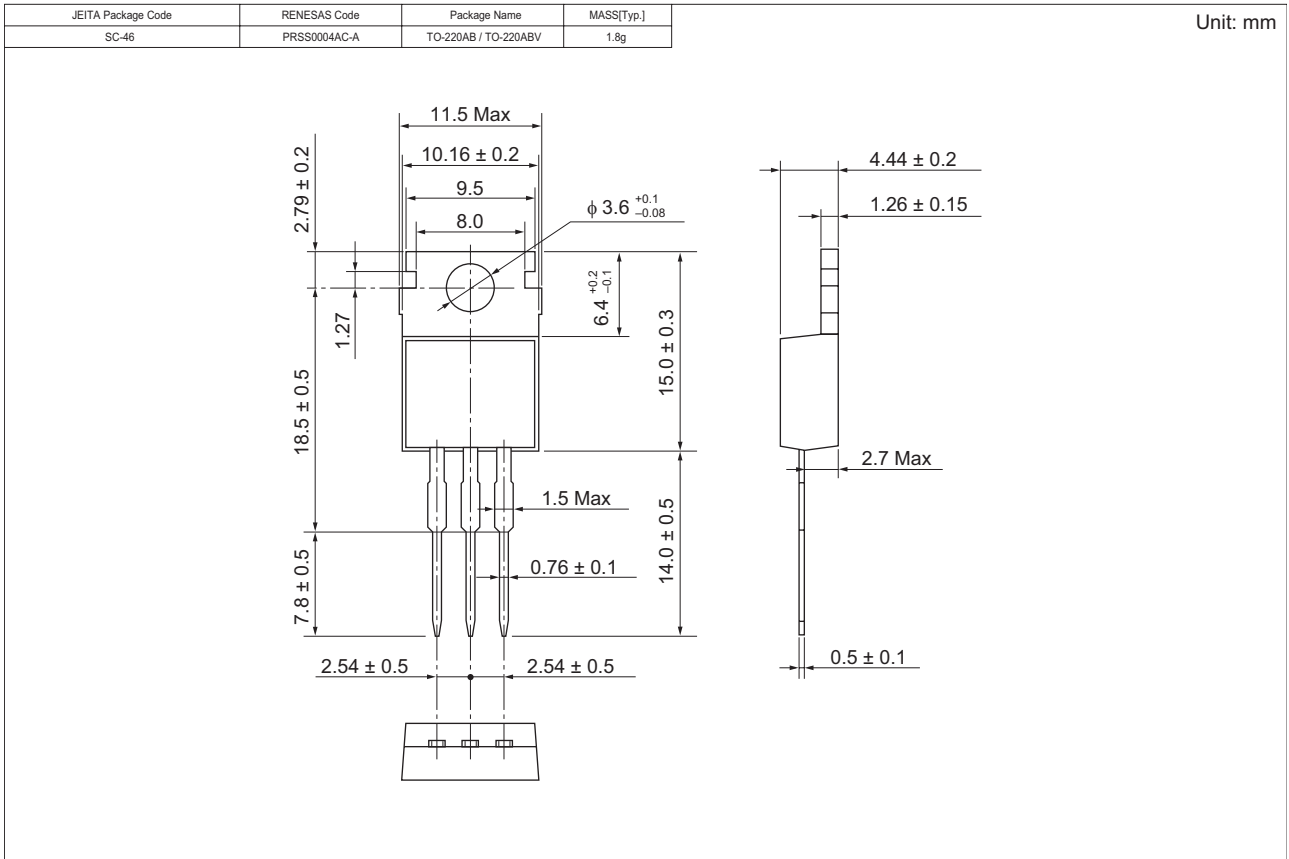
Switching Time Test Circuit



Waveforms



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK1300-E	500 pcs	Box (Sack)

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