

2SK1590C

N-CHANNEL MOSFET FOR SWITCHING

R07DS1276EJ0200 Rev.2.00 Jun 24, 2015

Description

The 2SK1590C, N-channel vertical type MOSFET designed for general-purpose switch, is a device which can be driven directly by a 4.5 V power source.

Features

• Directly driven by a 4.5 V power source.

• Low on-state resistance

 $R_{DS(on)1} = 2.7 \Omega MAX. (Vgs = 10 V, ID = 100 mA)$

 $R_{DS(on)2} = 3.2 \Omega MAX. (V_{GS} = 4.5 V, I_{D} = 50 mA)$

Ordering Information

Part Number	Lead Plating	Packing	Package	
2SK1590C-T1B-A/AT	-A : Sn-Bi , -AT : Pure Sn	3000p/Reel	SC-59 (3pMM)	

Remark "-A/AT" indicates Pb-free. This product does not contain Pb in external electrode and other parts.

Marking XK

Absolute Maximum Ratings (TA = 25°C)

Drain to Source Voltage (Ves = 0 V)	VDSS	60	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC)	I _{D(DC)}	±200	mA
Drain Current (pulse) Note	ID(pulse)	±800	mA
Total Power Dissipation	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

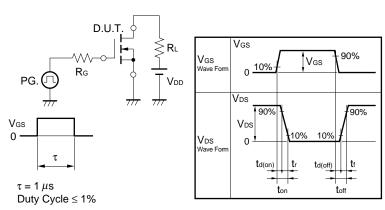
Note PW \leq 10 μ s, Duty Cycle \leq 1%

Electrical Characteristics (T_A = 25°C)

Characteristics	Symbol	Test Conditions	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V _{DS} = 60 V, V _{GS} = 0 V			1	μА
Gate Leakage Current	Igss	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μА
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0		2.5	V
Forward Transfer Admittance Note	y fs	V _{DS} = 10 V, I _D = 100 mA	150			mS
Drain to Source On-state Resistance Note	R _{DS(on)1}	V _G S = 10 V, I _D = 100 mA		2.1	2.7	Ω
	R _{DS(on)2}	V _G S = 4.5 V, I _D = 50 mA		2.4	3.2	Ω
Input Capacitance	Ciss	V _{DS} = 10 V,		20		pF
Output Capacitance	Coss	V _G S = 0 V,		9		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		2		pF
Turn-on Delay Time	td(on)	V _{DD} = 10 V,		16		ns
Rise Time	t r	I _D = 200 mA,		6.5		ns
Turn-off Delay Time	td(off)	V _G S = 10 V,		82		ns
Fall Time	t f	$R_G = 10 \Omega$		32		ns
Total Gate Charge	Q _G	ID = 200 mA, VDD = 25 V, VGS = 10 V		2		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	IF = 200 mA, VGS = 0 V		0.86		V

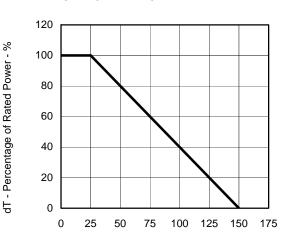
Note Pulsed

Test Circuit Switching Time



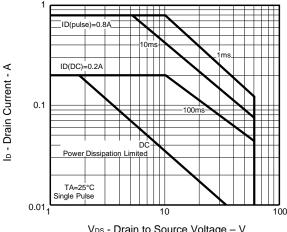
Typical Characteristics (T_A = 25°C)

DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



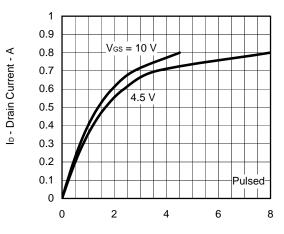
 T_A – Ambient Temperature - $^{\circ}C$

FORWARD BIAS SAFE OPERATING AREA



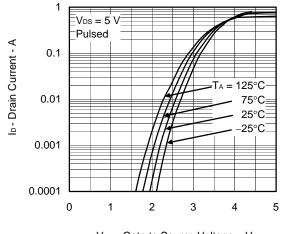
V_{DS} - Drain to Source Voltage - V

DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



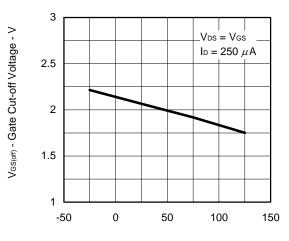
V_{DS} - Drain to Source Voltage - V

FORWARD TRANSFER CHARACTERISTICS



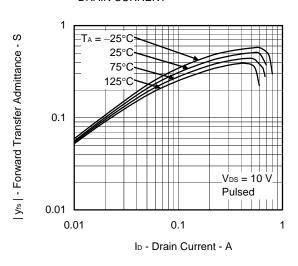
V_{GS} - Gate to Source Voltage - V

GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



Tch - Channel Temperature - °C

FORWARD TRANSFER ADMITTANCE vs. **DRAIN CURRENT**

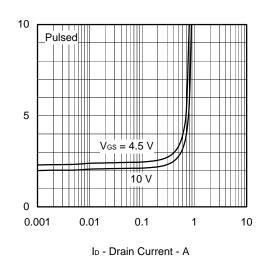


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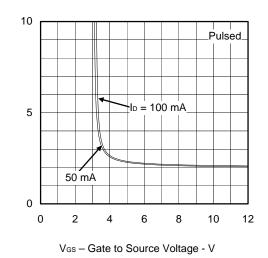
R_{DS(m)} - Drain to Source On-state Resistance - Ω

R_{DS(on)} - Drain to Source On-state Resistance - Ω

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

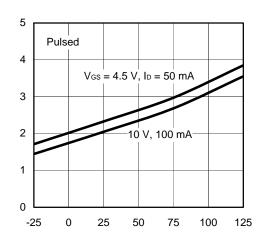


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

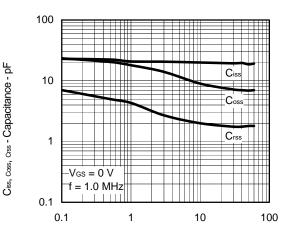


 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - Ω

DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



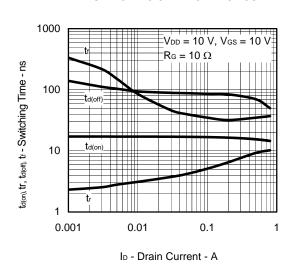
CAPACITANCE vs.
DRAIN TO SOURCE VOLTAGE



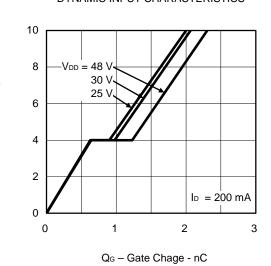
Tch - Channel Temperature - °C

V_{DS} - Drain to Source Voltage - V

SWITCHING CHARACTERISTICS

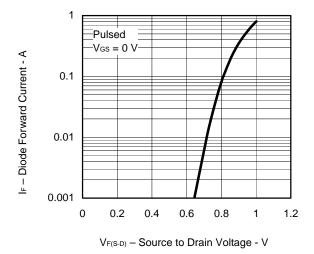


DYNAMIC INPUT CHARACTERISTICS



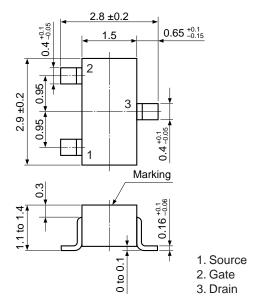
Ves - Gate to Source Voltage - V

SOURCE TO DRAIN DIODE FORWARD VOLTAGE

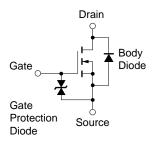


Package Drawings (Unit: mm)

SC-59 (Mini Mold)



Equivalent Circuit



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

2SK1590C

		Description		
Rev.	Date	Page	Summary	
1.00	Sep , 2013	_	First Edition Issued	
2.00	Jun, 2015	3	Added FORWARD BIAS SAFE OPERATING AREA	

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