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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK2414, 2414-Z

<R>

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK2414 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

FEATURES

<R>

· Low On-Resistance

 $R_{DS(on)1} = 70$ m Ω MAX. (VGS = 10 V, ID = 5.0 A) $R_{DS(on)2} = 95$ m Ω MAX. (VGS = 4 V, ID = 5.0 A)

- Low Ciss: Ciss = 860 pF TYP.
- · Built-in G-S Gate Protection Diodes
- · High Avalanche Capability Ratings

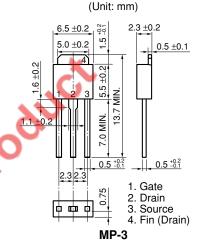
ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

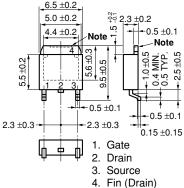
Drain to Source Voltage	Vdss	60	V
Gate to Source Voltage	Vgss	±20	V
Drain Current (DC)	ID(DC)	±10	Α
Drain Current (pulse) Note 1	ID(pulse)	±40	Α
Total Power Dissipation (Tc = 25 °C)	PT1	20	W
Total Power Dissipation (T _A = 25 °C)	Рт2	1.0	W
Channel Temperature	T ch	150	$^{\circ}\text{C}$
Storage Temperature ?	T _{stg}	-55 to +150	$^{\circ}\text{C}$
Single Avalanche Current Note 2	las	10	Α
Single Avalanche Energy Note 2	Eas	10	mJ

Notes 1 PW \leq 10 μ s, Duty Cycle \leq 1 %

2 Starting T_{ch} = 25 °C, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

PACKAGE DIMENSIONS

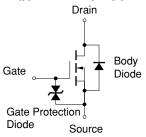




Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

MP-3Z (SURFACE MOUNT TYPE)

EQUIVALENT CIRCUIT



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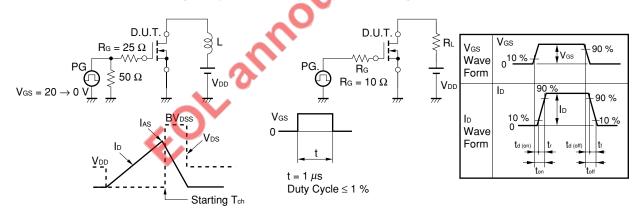


ELECTRICAL CHARACTERISTICS (TA = 25 ^{\circ}C)

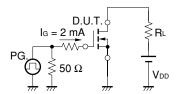
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	R _{DS(on)1}		52	70	mΩ	Vgs = 10 V, ID = 5.0 A
Drain to Source On-Resistance	RDS(on)2		68	95	mΩ	Vgs = 4 V, ID = 5.0 A
Gate to Source Cutoff Voltage	V _{GS(off)}	1.0	1.6	2.0	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	yfs	7.0	12		S	VDS = 10 V, ID = 5.0 A
Drain Leakage Current	IDSS			10	μΑ	VDS = 60 V, VGS = 0 V
Gate to Source Leakage Current	Igss			±10	μΑ	Vgs = ±20 V, Vps = 0 V
Input Capacitance	Ciss		860		pF	V _{DS} = 10 V
Output Capacitance	Coss		440		pF	Vgs = 0 V
Reverse Transfer Capacitance	Crss		110		pF	f = 1 MHz
Turn-On Delay Time	td(on)		15		ns	ID = 5.0 A
Rise Time	tr		90		ns	Vgs = 10 V
Turn-Off Delay Time	td(off)		75		ns	V _{DD} = 30 V
Fall Time	tf		35		ns	$R_G = 10 \Omega$
Total Gate Charge	QG		24		nC	lo = 10 A
Gate to Source Charge	Qgs		2.6		nC	VDD = 48 V
Gate to Drain Charge	Q _{GD}		6.0		nC	Vgs = 10 V
Body Diode Forward Voltage	V _{F(S-D)}		1.0	Y	V	IF = 10 A, VGS = 0 V
Reverse Recovery Time	trr		85 (2	ns	IF = 10 A, VGS = 0 V
Reverse Recovery Charge	Qrr		220		nC	di/dt = 50 A/μs

Test Circuit 1 Avalanche Capability

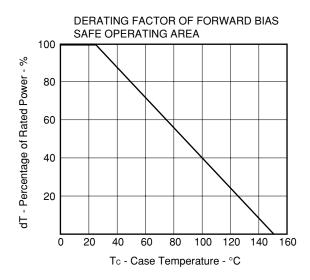
Test Circuit 2 Switching Time

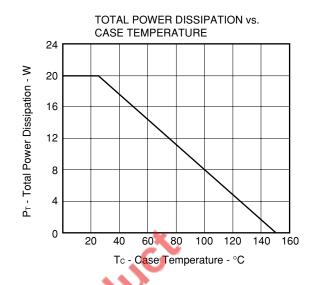


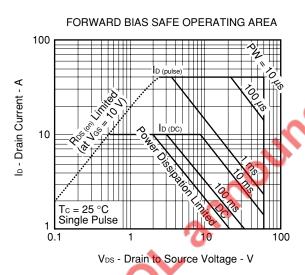
Test Circuit 3 Gate Charge

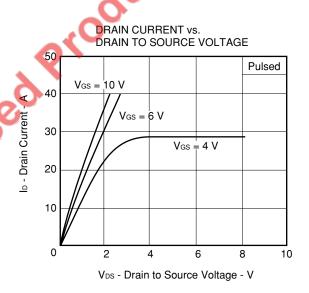


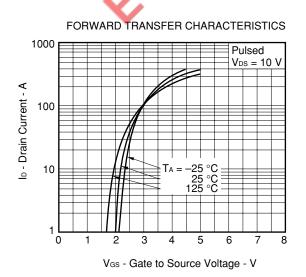
TYPICAL CHARACTERISTICS (TA = 25 °C)



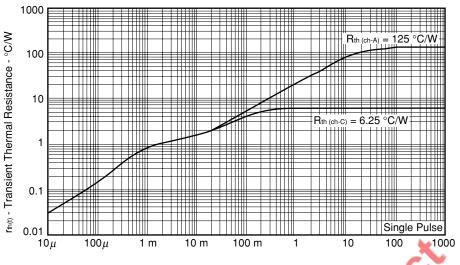




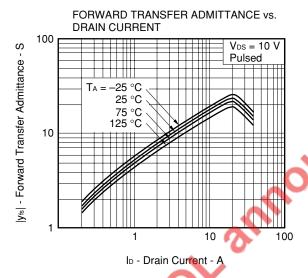


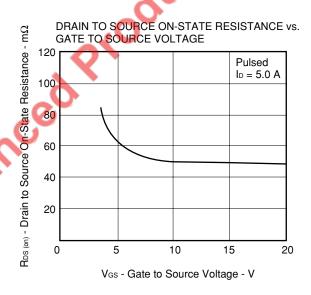


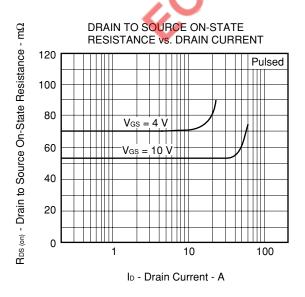
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

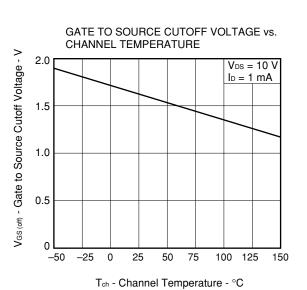


PW - Pulse Width - s

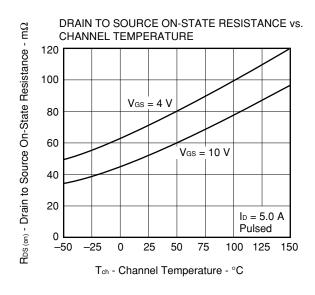


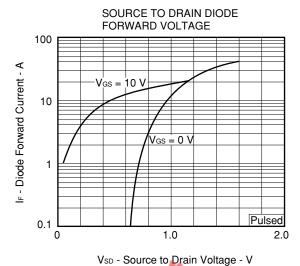


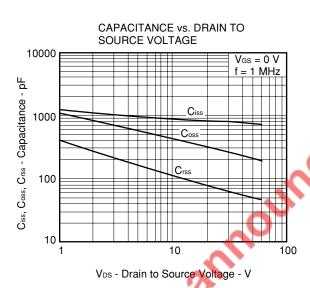


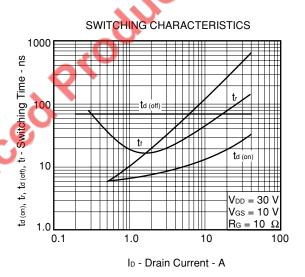


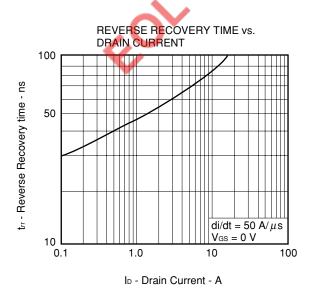
Data Sheet D13193EJ4V0DS

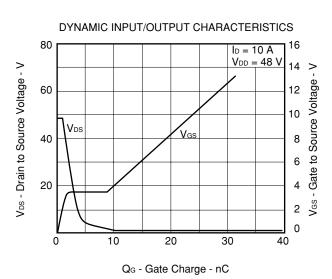


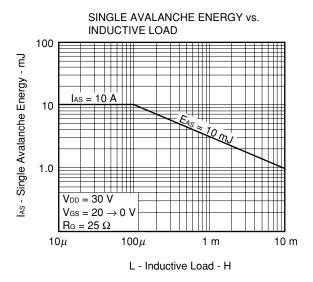


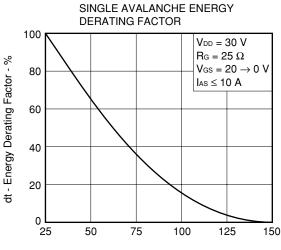












Starting Tch - Starting Channel Temperature - °C



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