2SK3147(L), 2SK3147(S)

100V, 5A, 0.13Ωmax.
Silicon N Channel Power MOS FET
High Speed Power Switching

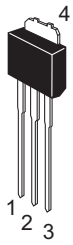
R07DS1254EJ0400
(Previous: REJ03G1072-0300)
Rev.4.00
Mar 25, 2015

Features

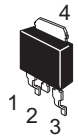
- Low on-resistance
  R_{DS} = 0.1 Ω typ.
- High speed switching
- 4 V gate drive device can be driven from 5 V source

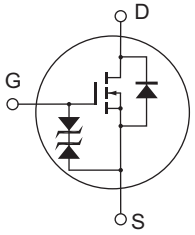
Outline

RENESAS Package code: PRSS0004ZD-B
(Package name: DPAK(L)-(2))



RENESAS Package code: PRSS0004ZD-C
(Package name: DPAK(S))





1. Gate
2. Drain
3. Source
4. Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	5	A
Drain peak current	$I_{D(pulse)}$ Note1	20	A
Body-drain diode reverse drain current	I_{DR}	5	A
Avalanche current	I_{AP} Note3	5	A
Avalanche energy	E_{AR} Note3	2.5	mJ
Channel dissipation	Pch Note2	20	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1 %
2. Value at Tc = 25°C
3. Value at Tch = 25°C, Rg ≥ 50 Ω

Electrical Characteristics

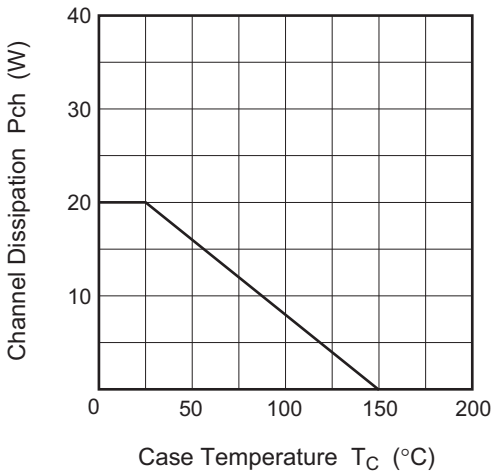
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	I_G = ±100 μA, V_{DS} = 0
Gate to source leak current	I_{GSS}	—	—	±10	μA	V_{GS} = ±16 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	V_{DS} = 100 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.5	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS(on)}$	—	0.1	0.13	Ω	I_D = 3 A, V_{GS} = 10 V Note4
	$R_{DS(on)}$	—	0.13	0.17	Ω	I_D = 3 A, V_{GS} = 4 V Note4
Forward transfer admittance	$\|y_{fs}\|$	3.5	6	—	S	I_D = 3 A, V_{DS} = 10 V Note4
Input capacitance	Ciss	—	420	—	pF	V_{DS} = 10 V, V_{GS} = 0, f = 1 MHz
Output capacitance	Coss	—	185	—	pF	
Reverse transfer capacitance	Crss	—	100	—	pF	
Turn-on delay time	$t_{d(on)}$	—	10	—	ns	I_D = 3 A, V_{GS} = 10V, R_L = 10 Ω
Rise time	t_r	—	35	—	ns	
Turn-off delay time	$t_{d(off)}$	—	110	—	ns	
Fall time	t_f	—	60	—	ns	
Body–drain diode forward voltage	V_{DF}	—	0.85	—	V	I_F = 5 A, V_{GS} = 0
Body–drain diode reverse recovery time	t_{rr}	—	85	—	ns	I_F = 5 A, V_{GS} = 0 di_F/ dt = 50 A/ μs

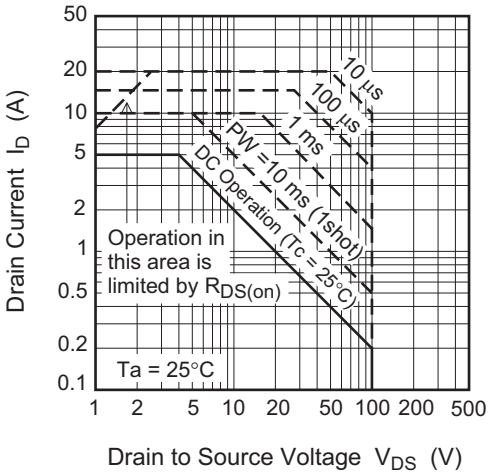
Note: 4. Pulse test

Main Characteristics

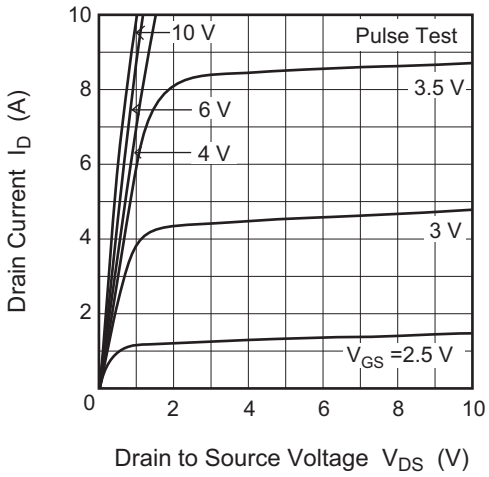
Power vs. Temperature Derating



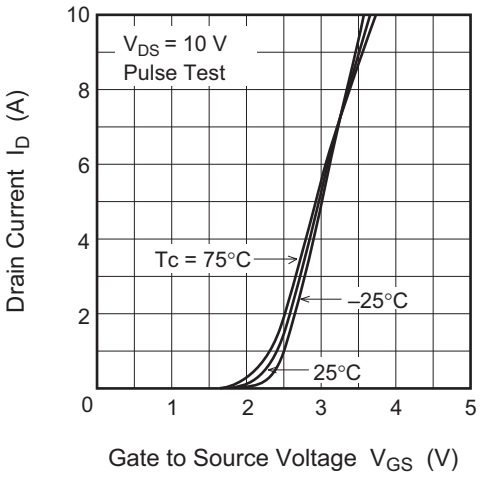
Maximum Safe Operation Area



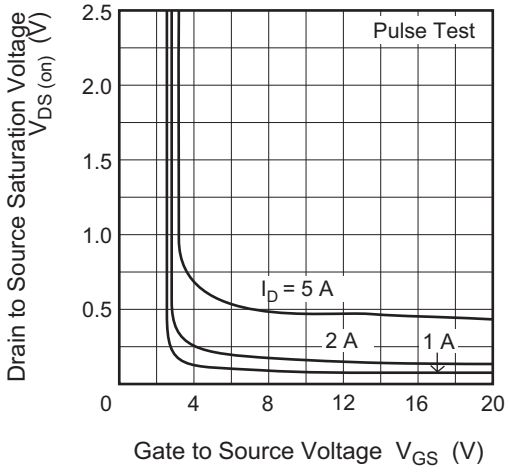
Typical Output Characteristics



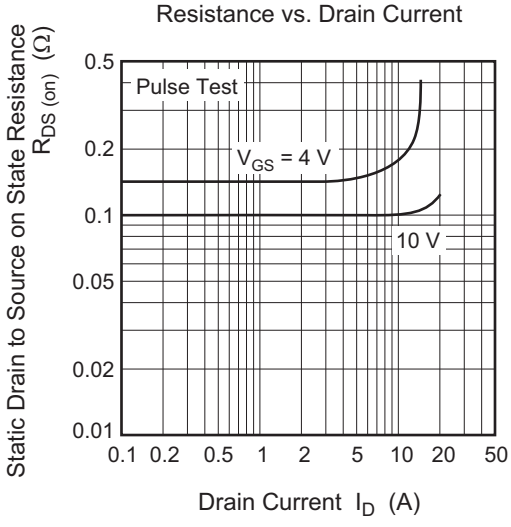
Typical Transfer Characteristics



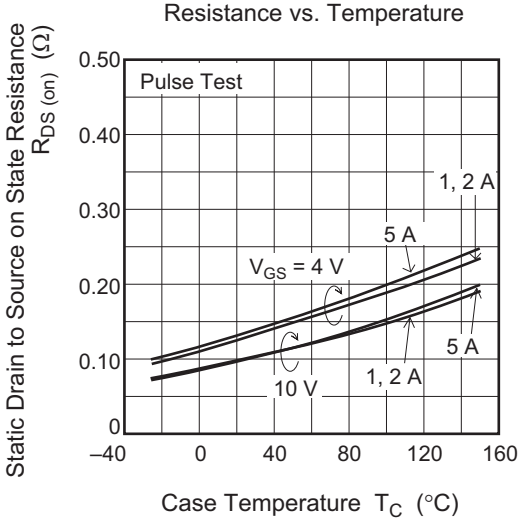
Drain to Source Saturation Voltage vs. Gate to Source Voltage



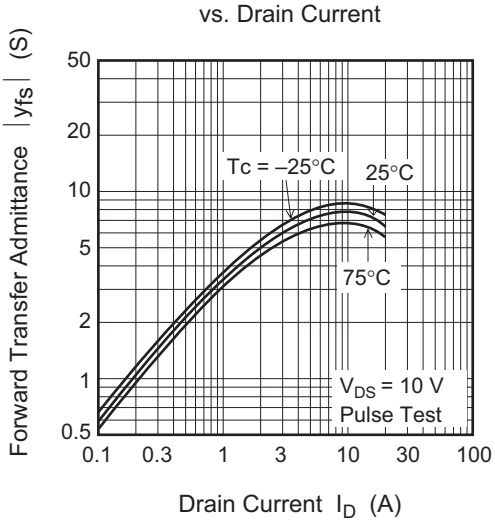
Static Drain to Source on State Resistance vs. Drain Current



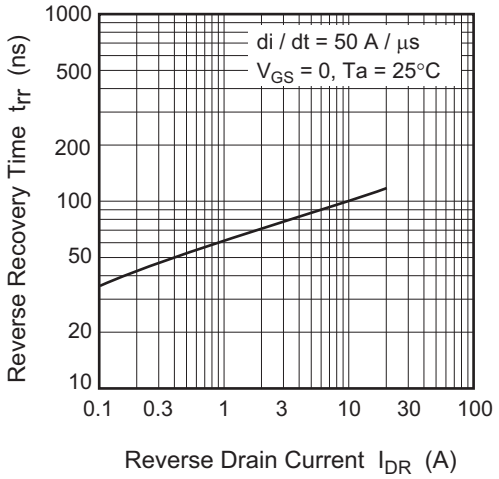
Static Drain to Source on State Resistance vs. Temperature



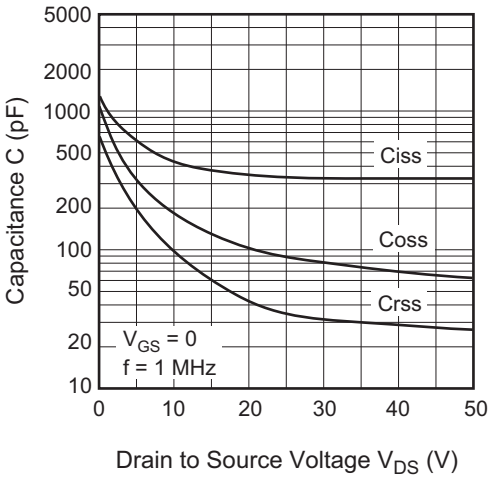
Forward Transfer Admittance vs. Drain Current



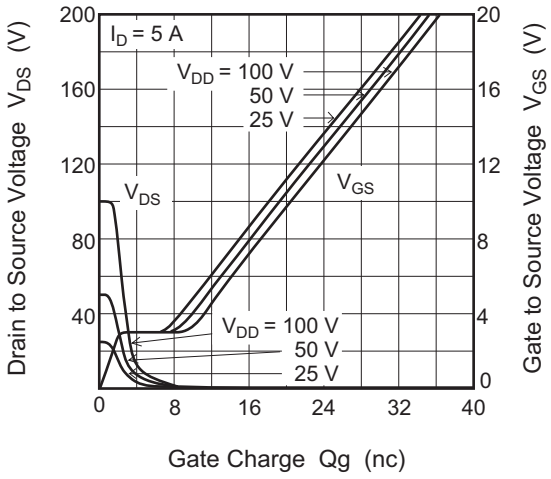
Body to Drain Diode Reverse Recovery Time



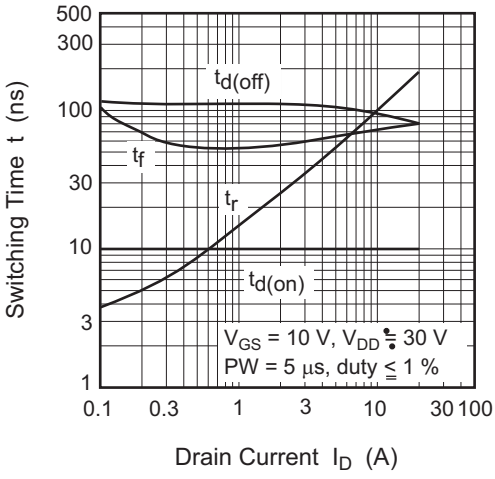
Typical Capacitance vs. Drain to Source Voltage



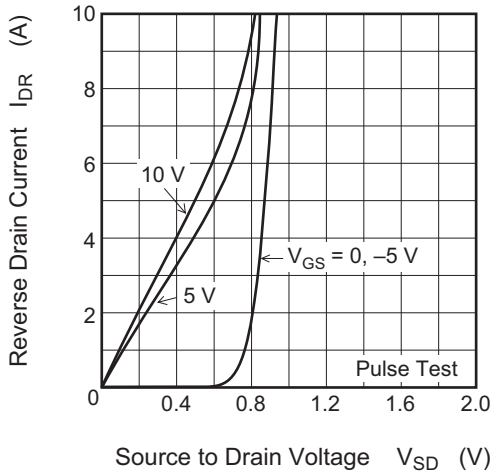
Dynamic Input Characteristics



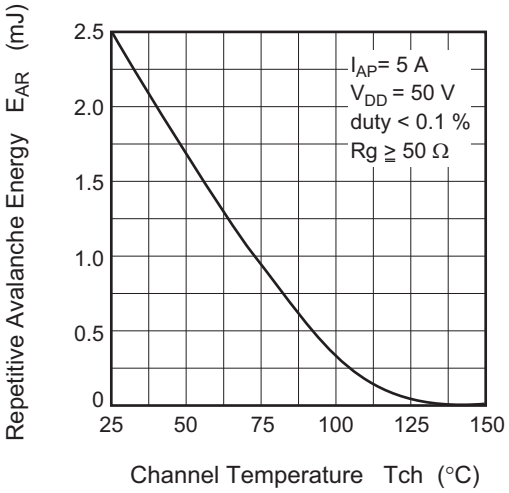
Switching Characteristics



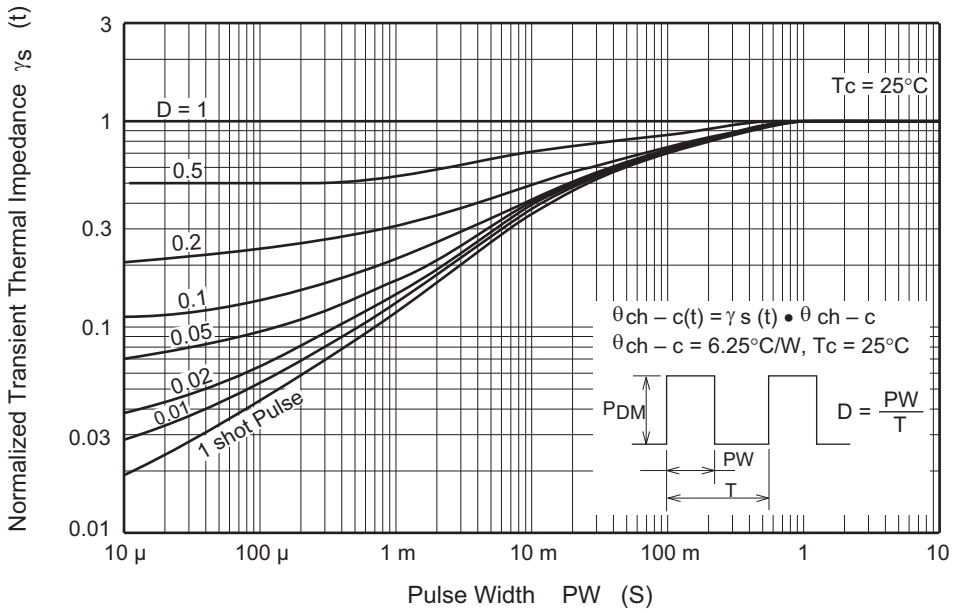
Reverse Drain Current vs. Source to Drain Voltage



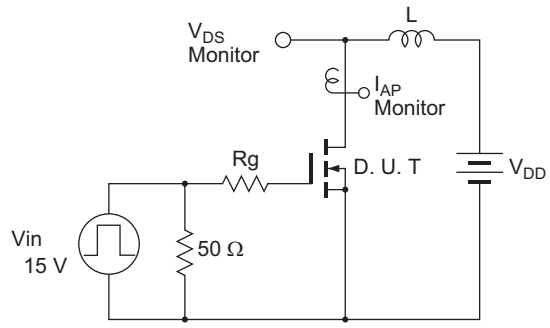
Maximum Avalanche Energy vs. Channel Temperature Derating



Normalized Transient Thermal Impedance vs. Pulse Width

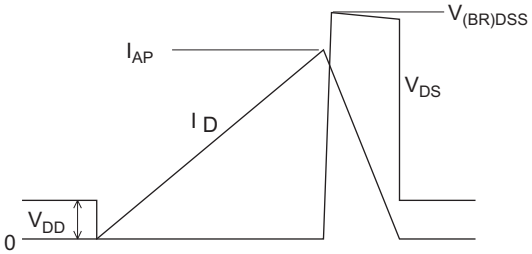


Avalanche Test Circuit

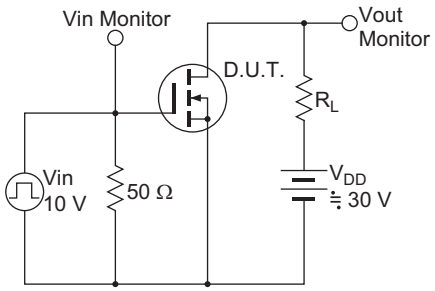


Avalanche Waveform

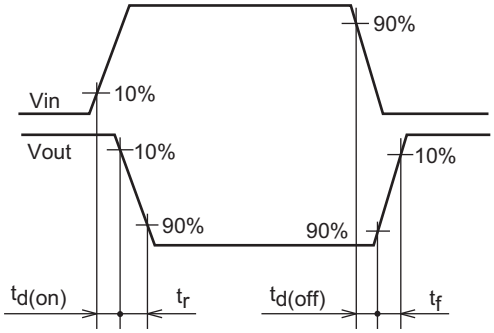
$$E_{AR} = \frac{1}{2} \bullet L \bullet I_{AP}^2 \bullet \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



Switching Time Test Circuit



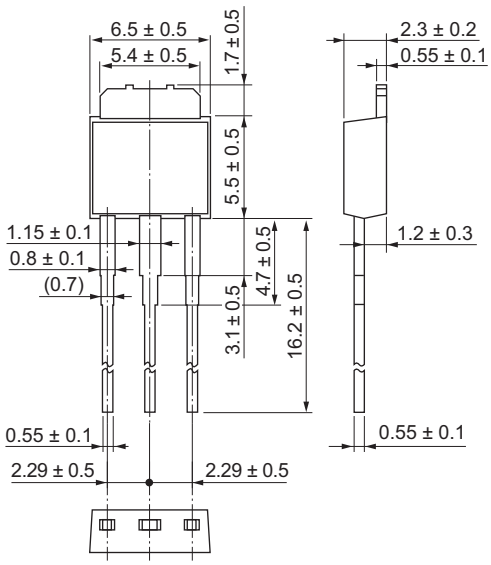
Switching Time Waveforms



Package Dimensions

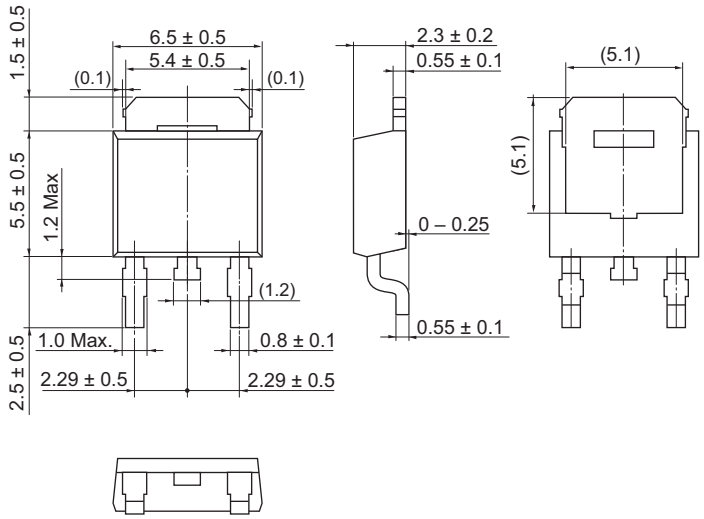
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
DPAK(L)-(2)	—	PRSS0004ZD-B	DPAK(L)-(2) / DPAK(L)-(2)V	0.42g

Unit: mm



Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
DPAK(S)	SC-63	PRSS0004ZD-C	DPAK(S) / DPAK(S)V	0.28g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
2SK3147L-E	3200 pcs	Box (Sack)
2SK3147STR-E	3000 pcs	Taping

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