Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Thyristor Low Power Use

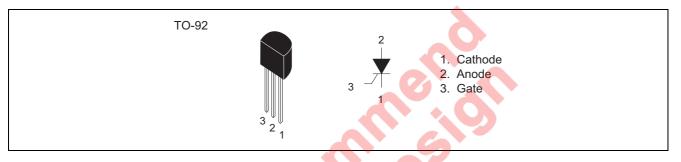
> REJ03G0355-0100 Rev.1.00 Aug.20.2004

Features

- $I_{T(AV)}: 0.3 A$
- V_{DRM} : 800 V
- I_{GT} : 100 μA

- Non-Insulated Type
- Glass Passivation Type

Outline



Applications

Leakage protector, timer, and gas igniter

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
Faiameter	Symbol	16	Onit
Repetitive peak reverse voltage	V _{RRM}	800	V
Non-repetitive peak reverse voltage	V _{RSM}	960	V
DC reverse voltage	V _{R(DC)}	640	V
Repetitive peak off-state voltage ^{Note1}	V _{DRM}	800	V
Non-repetitive peak off-state voltage Note1	V _{DSM}	960	V
DC off-state voltage ^{Note1}	V _{D(DC)}	640	V

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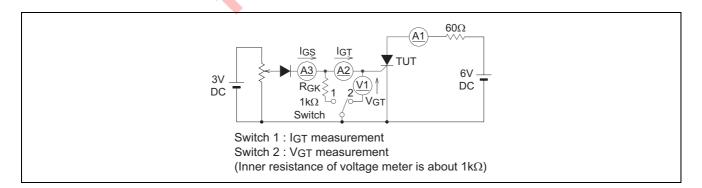
Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.47	A	
Average on-state current	I _{T (AV)}	0.3	A	Commercial frequency, sine half wave 180° conduction, Ta = 47°C
Surge on-state current	I _{TSM}	20	A	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	1.6	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	0.5	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate forward voltage	V _{FGM}	6	V	
Peak gate reverse voltage	V _{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.3	A	
Junction temperature	Tj	- 40 to +110	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass		0.23	g	Typical value

Notes: 1. With gate to cathode resistance $R_{GK} = 1 \ k\Omega$.

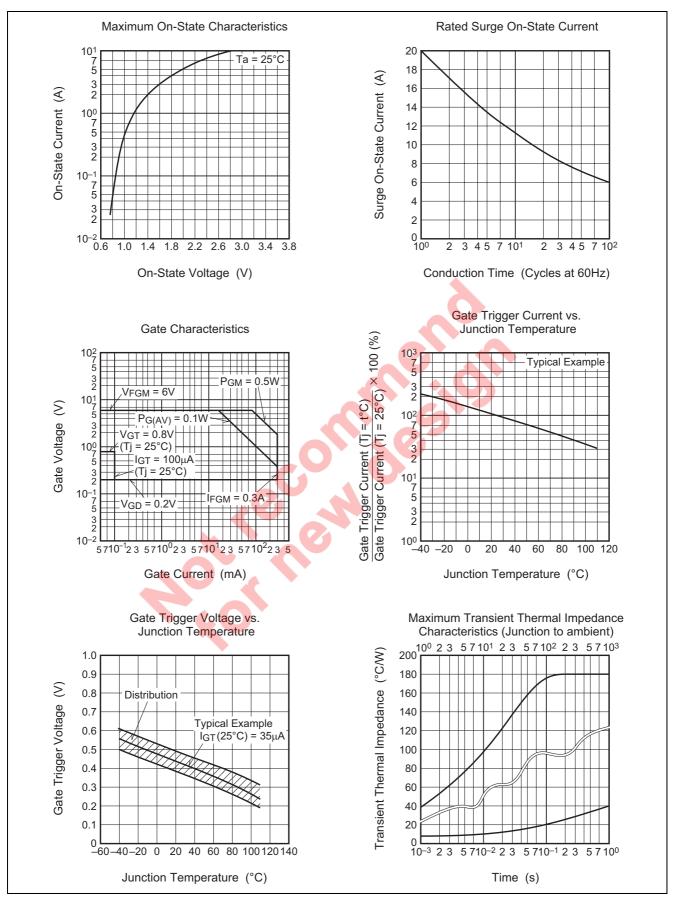
Electrical Characteristics

Devenenter	Cumhal	Rated value			Unit	Test conditions	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions	
Repetitive peak reverse current	I _{RRM}	—	—	0.1	mA	Tj = 110°C, V _{RRM} applied	
Repetitive peak off-state current	I _{DRM}	—		0.1	mA	Tj = 110°C, V _{DRM} applied, R _{GK} = 1 k Ω	
On-state voltage	V _{TM}	_		1.8	V	Ta = 25° C, I _{TM} = 4 A, instantaneous value	
Gate trigger voltage	V _{GT}		- (0.8	V	$\label{eq:transform} \begin{array}{l} Tj = 25^{\circ}C, \ V_{D} = 6 \ V, \\ I_{T} = 0.1 \ A^{Note2} \end{array}$	
Gate non-trigger voltage	V _{GD}	0.2		—	V	$\label{eq:Tj} \begin{split} Tj &= 110^\circ C, \ V_D = 1/2 \ V_{DRM}, \\ R_{GK} &= 1 \ k\Omega \end{split}$	
Gate trigger current	I _{GT}	1	-	100	μΑ	$\label{eq:tilde} \begin{split} Tj &= 25^\circ C, \ V_D = 6 \ V, \\ I_T &= 0.1 \ A^{Note2} \end{split}$	
Holding current	Ін		1.5	3	mA	$\label{eq:transform} \begin{array}{l} Tj=25^{\circ}C,\ V_{D}=12\ V,\\ R_{GK}=1\ k\Omega \end{array}$	
Thermal resistance	R _{th (j-a)}	_	_	180	°C/W	Junction to ambient	

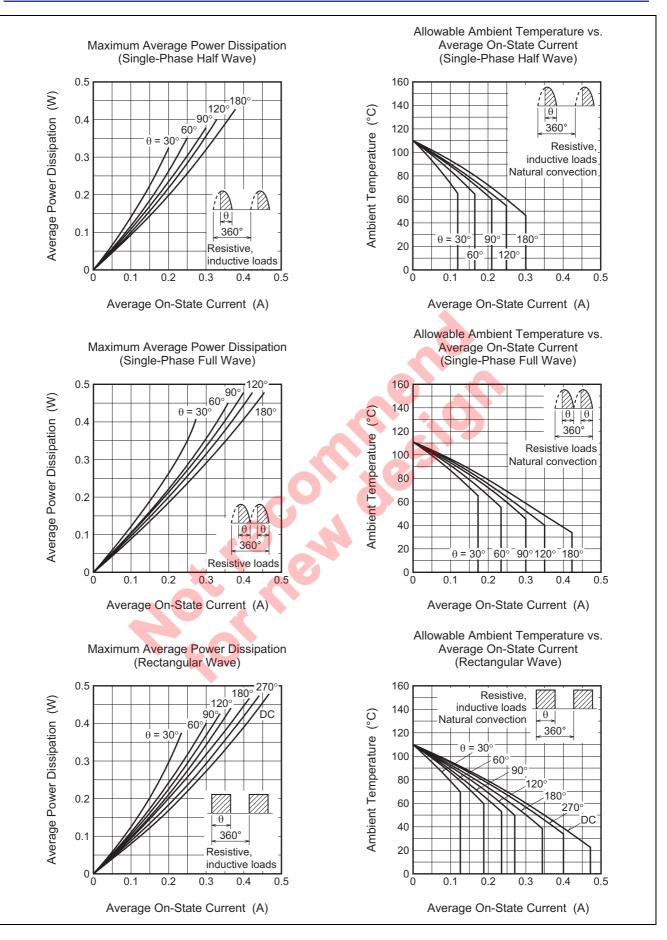
Notes: 2. IGT, VGT measurement circuit.



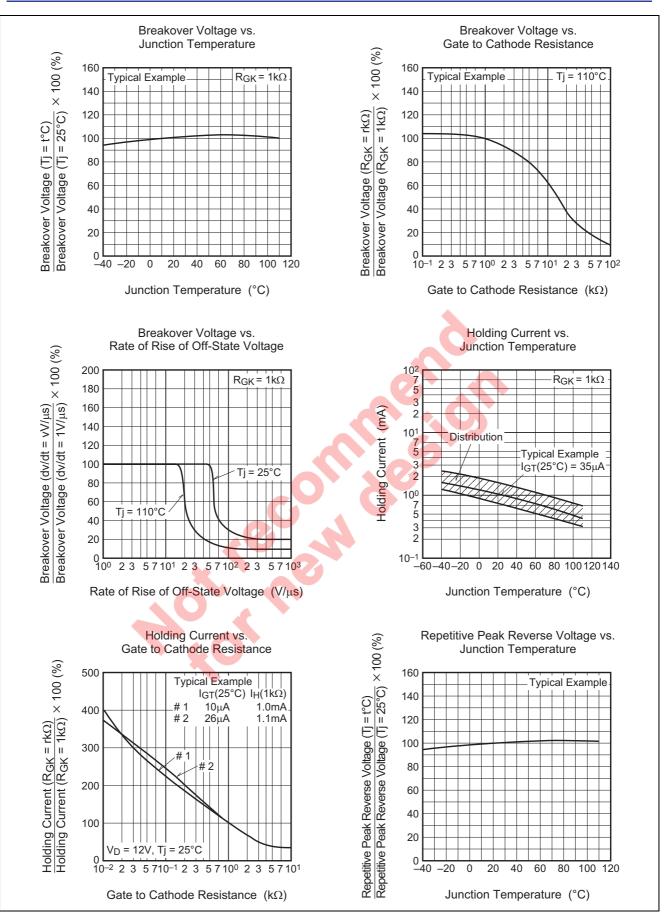
Performance Curves



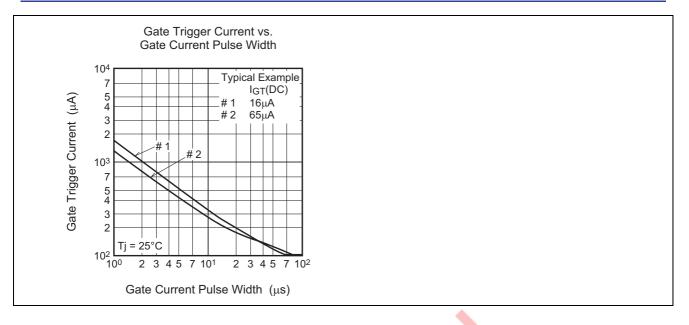
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Package Dimensions

TO-92 EIAJ Package Code	JEDEC Code	Mass (g) (reference value)	Lead Material					
Conforms	Conforms	0.23	Cu alloy					
Comornis	¢ 5.0 max	5.0 max						
Circumscribed	1.25 1.25 + + + + + + + + + + + + + + + + + + +			6				
-		99 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		ne (o	Symb A A1 A2 b D E	Min 	sion in Mil	limeters Max
Note 1) The di otherw	mensional figures ind ise the tolerance is s	icate representative values unless pecified.		65	e x y y_1 ZE ZE			

Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	CR03AM-16
Lead form	Vinyl sack	500	Type name – Lead forming code	CR03AM-16-A6
Form A8	Taping	2000	Type name – TB	CR03AM-16-TB

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Note : Please confirm the specification about the shipping in detail.

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