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Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Not recommend for new design

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HA17903 Series

Dual Comparator

REJ03D0687-0100
(Previous: ADE-204-048)
Rev.1.00
Jun 15, 2005

Description

HA17903 are comparators designed for car use and control system use.

They provide wide voltage range with single power source, and the change of supply current is small, because it is independent of the supply voltage. They can be widely applied, such as limit comparator, simple analog/digital converter, pulse/square wave/time delay generator, wide range VCO, MOS clock timer, multivibrator, high voltage logic gate, etc.

Features

- Wide supply voltage: 2 to 36V
- Very low supply current: 0.8mA
- Small input bias: 25nA
- Small input offset current: 3nA
- Small input offset voltage: 2mV
- Common mode input voltage range including ground.
- Small output saturation voltage: 1mV (5µA)
- 70mV (1mA)
- Output voltage is compatible with CMOS logic system.

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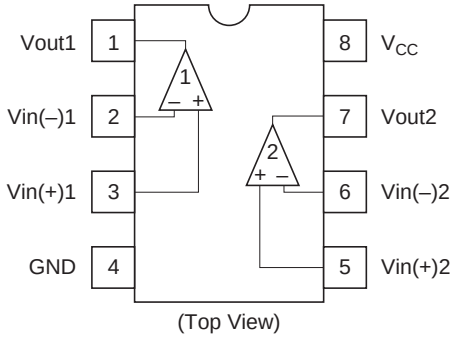
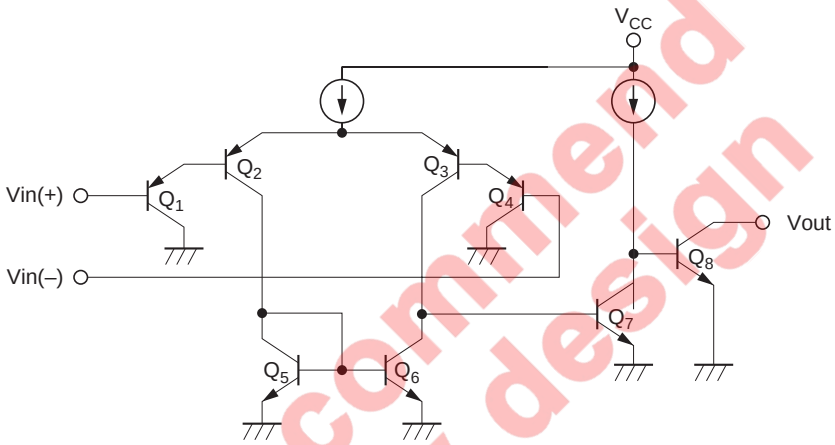
Ordering Information

Type No.	Application	Package Code (Previous Code)
HA17903PSJ	Car use	PRDP0008AF-A (DP-8B)
HA17903FPJ		PRSP0008DE-B (FP-8DGV)
HA17903FPK		PRSP0008DE-B (FP-8DGV)

Pin Arrangement



Circuit Schematic (1/2)



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Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings			Unit
		HA17903PSJ	HA17903FPJ	HA17903FPK	
Supply voltage	V_{CC}	36	36	36	V
Dfferential input voltage	$V_{IN(diff)}$	V_{CC}	V_{CC}	V_{CC}	V
Input voltage	V_{IN}	–0.3 to + V_{CC}	–0.3 to + V_{CC}	–0.3 to + V_{CC}	V
Output short current	I_{OS}*[3]	constant	constant	constant	
Power dissipation	P_T	570*[1]	385*[1]	385*[2]	mW
Operating temperature	Topr	–40 to +85	–40 to +85	–40 to +125	°C
Storage temperature	Tstg	–55 to +125	–55 to +125	–55 to +150	°C

Note: 1. These are the allowable values up to Ta = 55 °C. Derate by 8.3mW/°C above that temperature.
2. These are the allowable values up to Ta = 45 °C mounting on 30% wiring density glass epoxy board. Derate by 7.14mW/°C above that temperature.
3. Short circuit between the output and VCC will be a cause to destory the circuit. The maximum output current is about 20mA for any supply voltage.

Electrical Characteristics-1

(V_{CC} = 5V, Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test condition
Input offset voltage*[1]	V_{IO}	—	2.0	5.0	mV	
Input bias current*[2]	I_{IB}	—	25	250	nA	$I_{IN(+)}$ or $I_{IN(-)}$
Input offset current	I_{IO}	—	3	50	nA	$\lvert I_{IN(+)} - I_{IN(-)} \rvert$
Common mode input voltage*[3]	V_{CM}+	3.5	—	—	V	
	V_{CM}–	—	—	0	V	
Supply current	I_{CC}	—	0.8	2.0	mA	All comparators: $R_L = \infty$, All channels on
Voltage gain	A_{VD}	—	200	—	V/mV	V_{CC} = 15V, $R_L \geq 15k\Omega$
Response time*[4]	t_R	—	1.3	—	µs	V_{RL} = 5V, R_L = 5.1kΩ
Large signal response time	t_{RI}	—	300	—	ns	V_{IN} = TTL Threshold width, V_{REF} = 1.4V
Out put sink current	Iosink	6	16	—	mA	$V_{IN(-)} \geq 1V$, $V_{IN(+)} = 0$, $V_O \leq 1.5V$
Output saturation voltage	V_O (sat)	—	—	400	mA	$V_{IN(-)} \geq 1V$, $V_{IN(+)} = 0$, Iosink = 4mA
Output leak current	I_{LO}	—	0.1	—	nA	$V_{IN(-)} = 0$, $V_{IN(+)} \geq 1V$, V_O = 5V

Notes: 1. V_{REF} = 1.4V and $R_S = 50\Omega$, when V_O = 1.4V at output switching point.
2. Under linear operation.
3. Common mode input voltage or each one of the input signal should not be less than –0.3V.
4. This is a value to 100mV input step voltage with 5mV over drive.

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Electrical Characteristics-2

(V_{CC} = 5V, Ta = –40 to +125°C)

Item	Symbol	Min	Typ	Max	Unit	Test condition
Input offset voltage*[1]	V_{IO}	—	—	5.0	mV	
Input offset current	I_{IO}	—	—	200	nA	$\lvert I_{IN(+)} - I_{IN(-)} \rvert$
Input bias current	I_{IB}	—	—	500	nA	Output linear range
Common mode input voltage	V_{CM}	0	—	V_{CC} – 2.0	V	
Output saturation voltage	V_O (sat)	—	—	440	mV	$V_{IN(-)} \geq 1V$, $V_{IN(+)} = 0$, Iosink ≤ 4mA
Output leak current	I_{LO}	—	1.0	—	µA	$V_{IN(-)} = 0$, $V_{IN(+)} \geq 1$ V, V_O = 30V
Supply current	I_{CC}	—	—	4.0	mA	All comparators: $R_L = \infty$, All channels on

Note: 1. V_{REF} = 1.4V and R_S = 50Ω, when V_O = 1.4V at the output switching point.

Characteristics Curve

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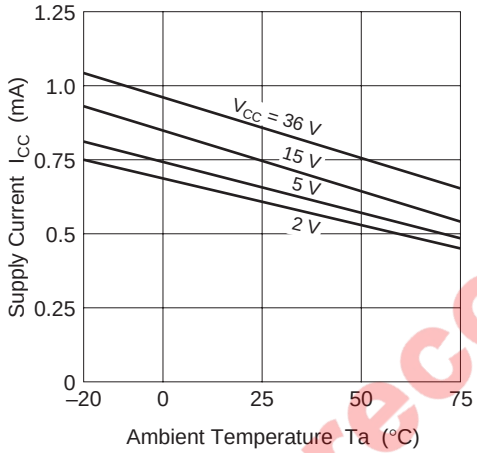
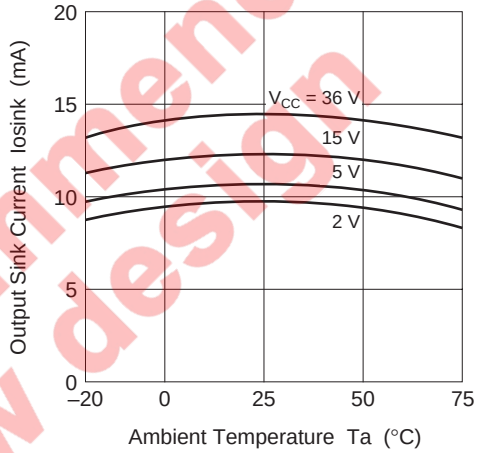
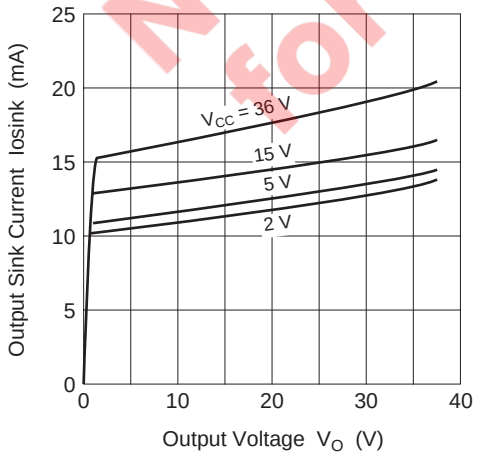
Supply Current vs. Ambient Temperature



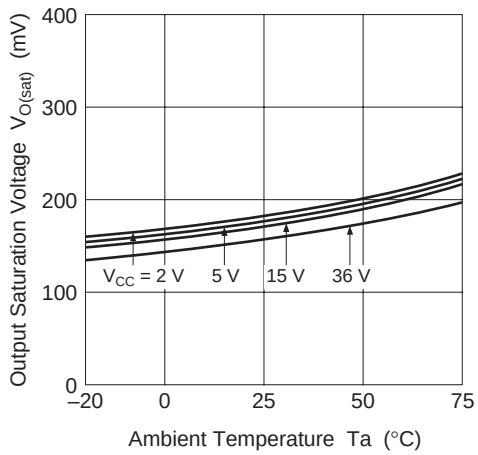
Output Sink Current vs. Ambient Temperature



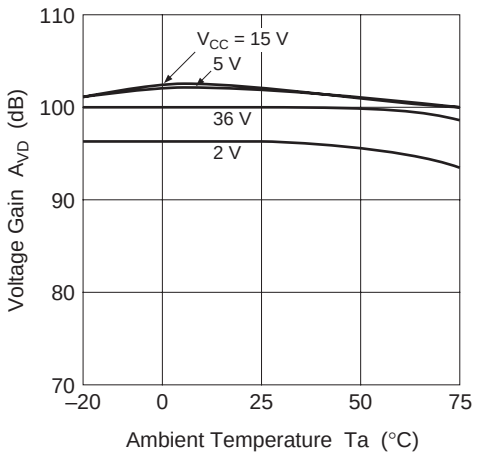
Output Sink Current vs. Output Voltage



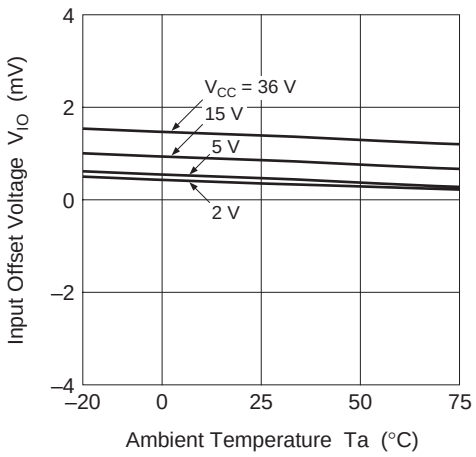
Output Saturation Voltage vs. Ambient Temperature



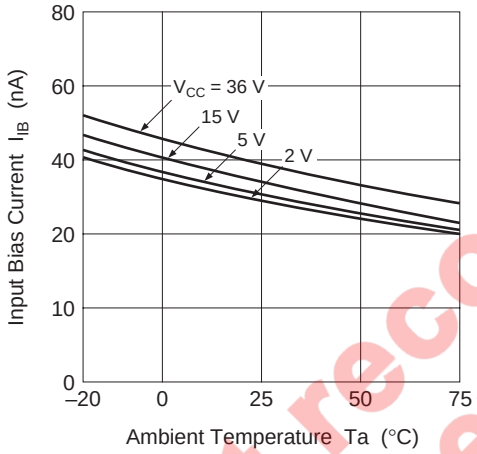
Voltage Gain vs. Ambient Temperature



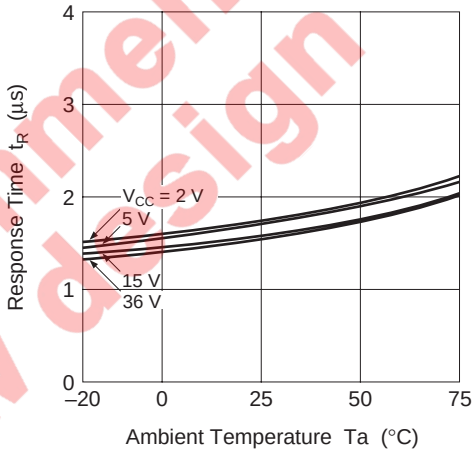
Input Offset Voltage vs. Ambient Temperature



Input Bias Current vs. Ambient Temperature



Response Time vs. Ambient Temperature



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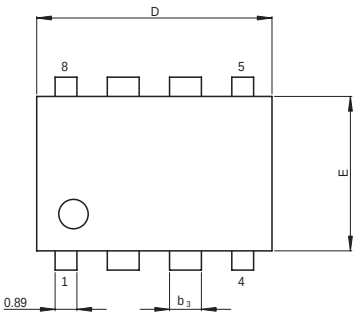
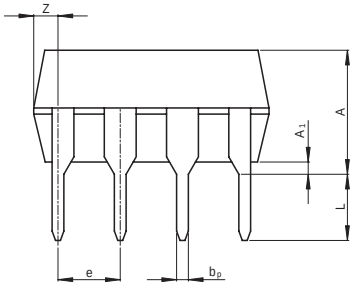
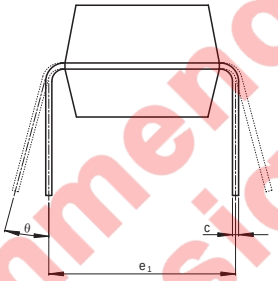
Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-DIP8-6.3x9.6-2.54	PRDP0008AF-A	DP-8B	0.51g







Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
e 1	——	7.62	——
D	——	9.6	10.6
E	——	6.3	7.4
A	——	——	5.06
A 1	0.5	——	——
b p	0.38	0.48	0.58
b 3	——	1.3	——
c	0.20	0.25	0.35
θ	0°	——	15°
e	2.29	2.54	2.79
Z	——	——	1.27
L	2.54	——	——

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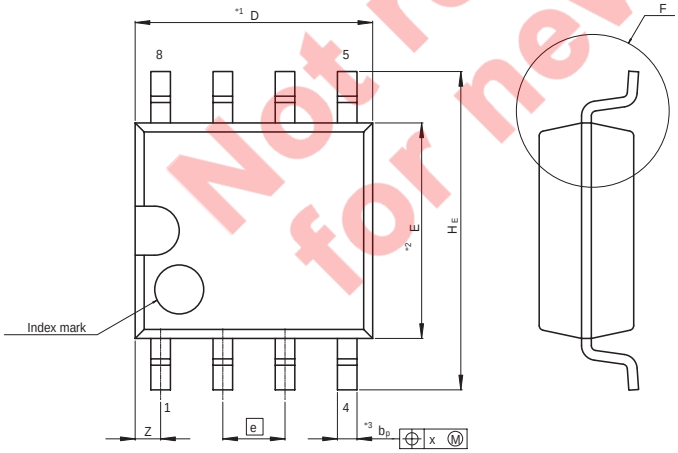
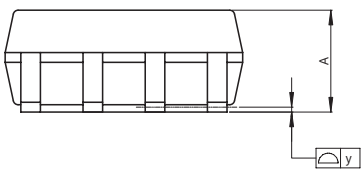
JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP8-4.4x4.85-1.27	PRSP0008DE-B	FP-8DGV	0.1g





NOTE)
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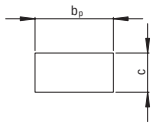
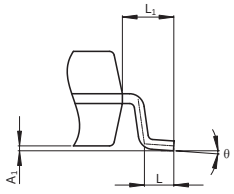
2. DIMENSION"*3"DOES NOT INCLUDE TRIM OFFSET.



Terminal cross section
(Ni/Pd/Au plating)



Detail F

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	——	4.85	5.25
E	——	4.4	——
A 2	——	——	——
A 1	0.00	0.1	0.20
A	——	——	2.03
b p	0.35	0.4	0.45
b 1	——	——	——
c	0.15	0.20	0.25
c 1	——	——	——
θ	0°	——	8°
H E	6.35	6.5	6.75
e	——	1.27	——
x	——	——	0.12
y	——	——	0.15
Z	——	——	0.75
L	0.42	0.60	0.85
L 1	——	1.05	——

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