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April 1st, 2010
Renesas Electronics Corporation

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HAT2142H

Silicon N Channel Power MOS FET Power Switching

REJ03G1194-0800

Rev.8.00

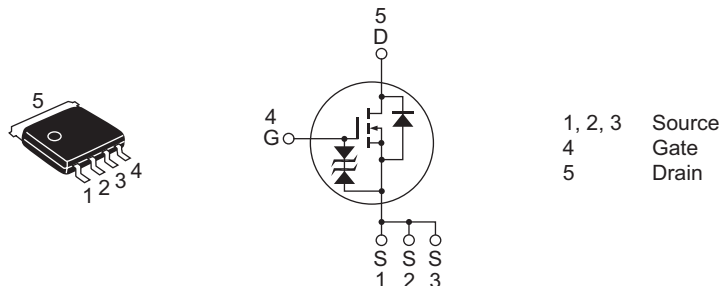
Jul 29, 2009

Features

- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
 $R_{DS(on)} = 35 \text{ m}\Omega$ typ. (at $V_{GS} = 10 \text{ V}$)

Outline

RENESAS Package code: PTZZ0005DA-A
(Package name: LFPAK)



Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

| Item | Symbol | Value | Unit |
|--|----------------------------------|-------------|------------------|
| Drain to source voltage | V_{DSS} | 100 | V |
| Gate to source voltage | V_{GSS} | ± 20 | V |
| Drain current | I_D | 10 | A |
| Drain peak current | $I_{D(pulse)}$ ^{Note 1} | 40 | A |
| Body-drain diode reverse drain current | I_{DR} | 10 | A |
| Avalanche current | I_{AP} ^{Note 3} | 10 | A |
| Avalanche energy | E_{AR} ^{Note 3} | 10 | mJ |
| Channel dissipation | P_{ch} ^{Note 2} | 15 | W |
| Channel temperature | T_{ch} | 150 | $^\circ\text{C}$ |
| Storage temperature | T_{stg} | -55 to +150 | $^\circ\text{C}$ |

- Notes: 1. $PW \leq 10 \mu\text{s}$, duty cycle $\leq 1\%$
 2. $T_c = 25^\circ\text{C}$
 3. Value at $T_{ch} = 25^\circ\text{C}$, $R_g \geq 50 \Omega$

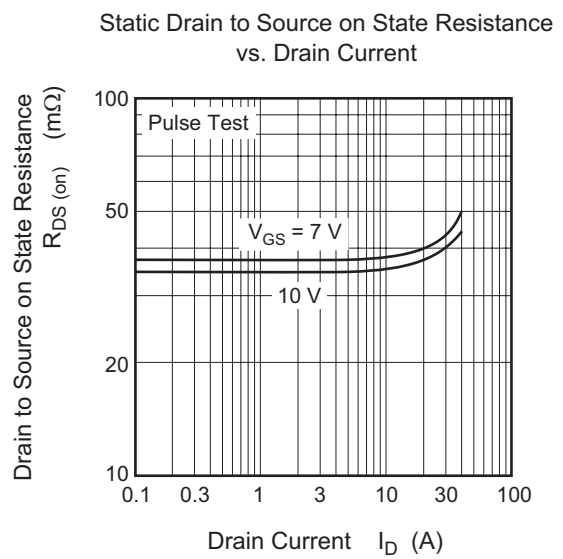
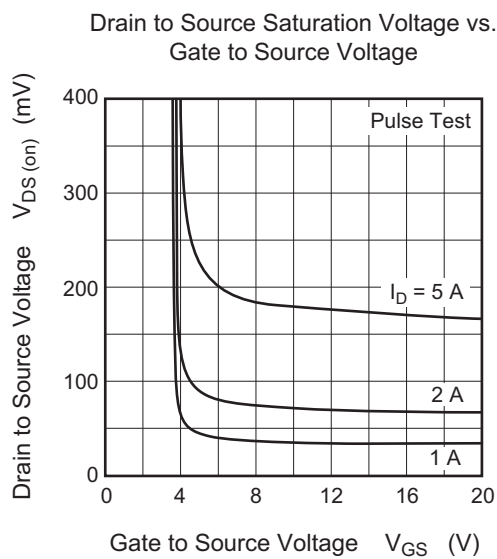
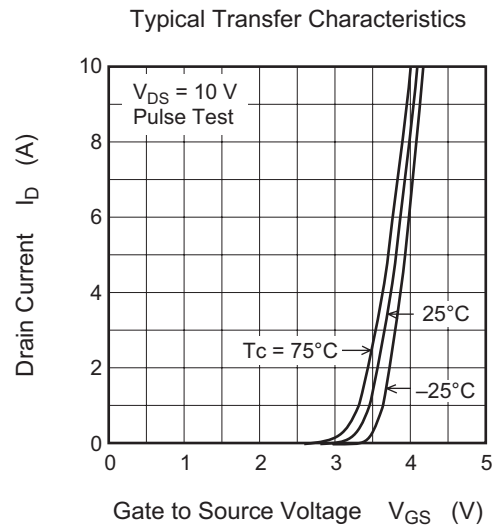
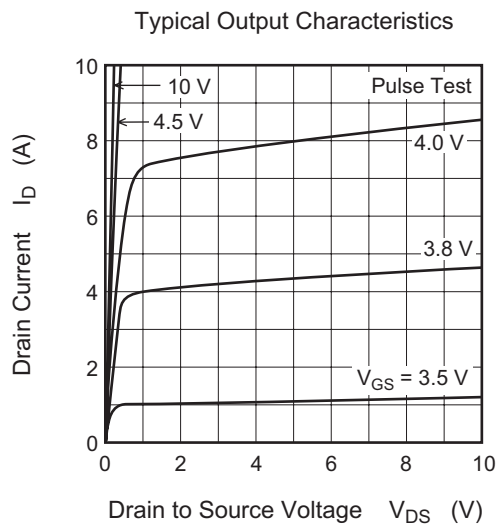
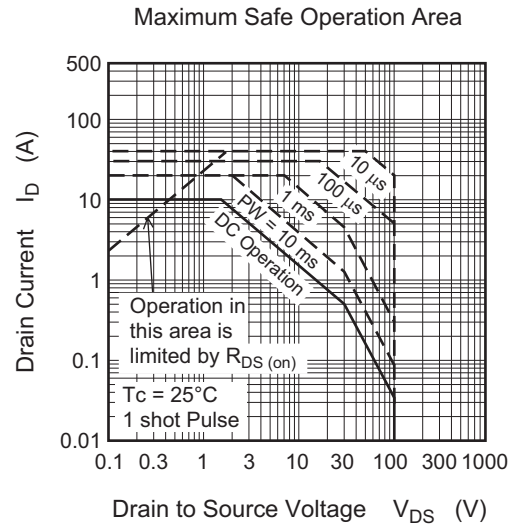
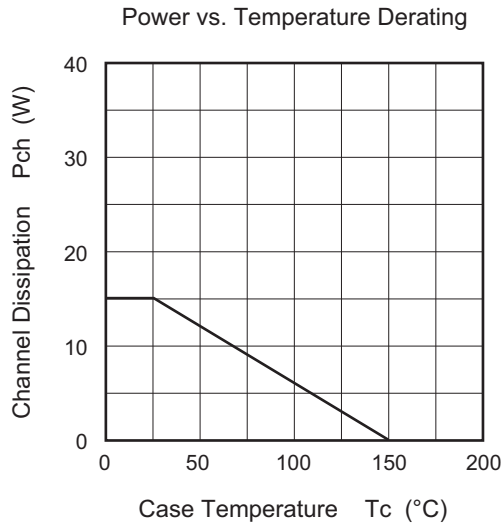
Electrical Characteristics

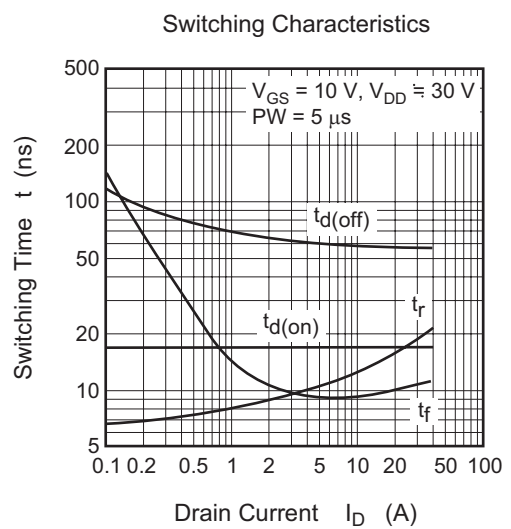
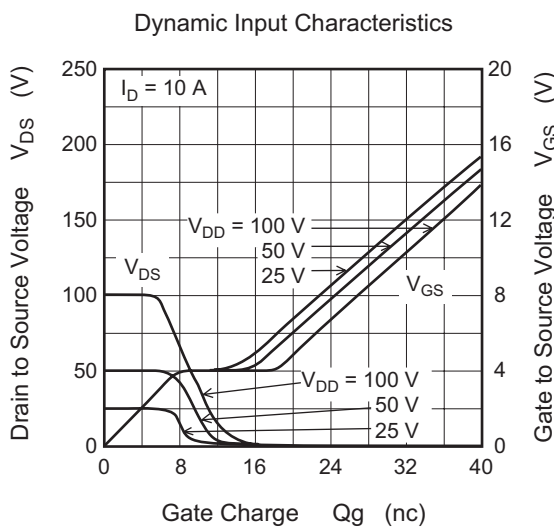
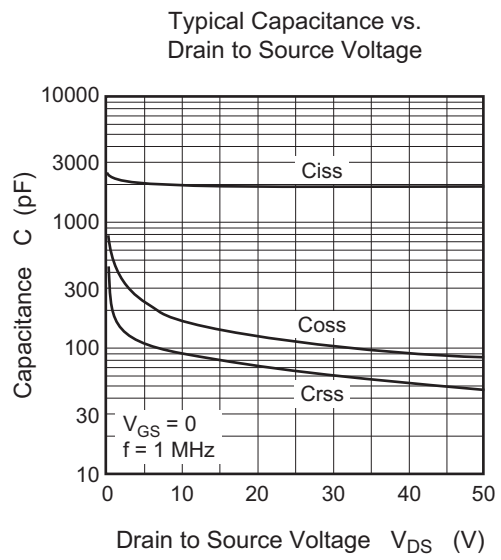
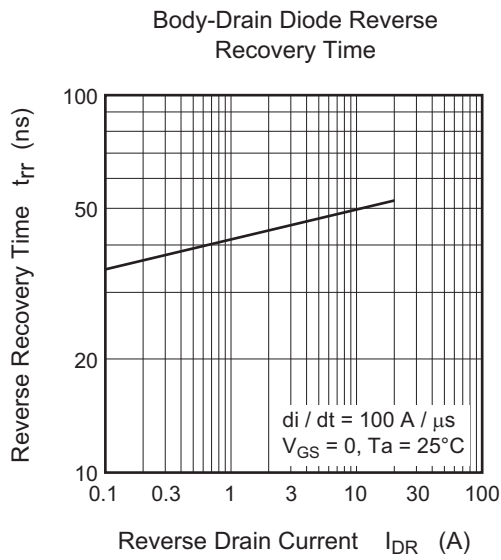
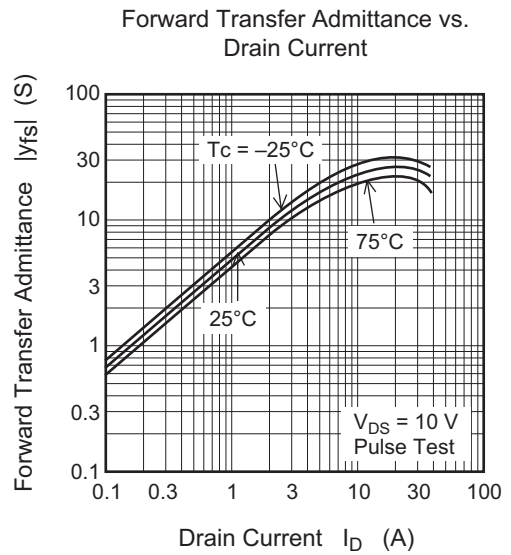
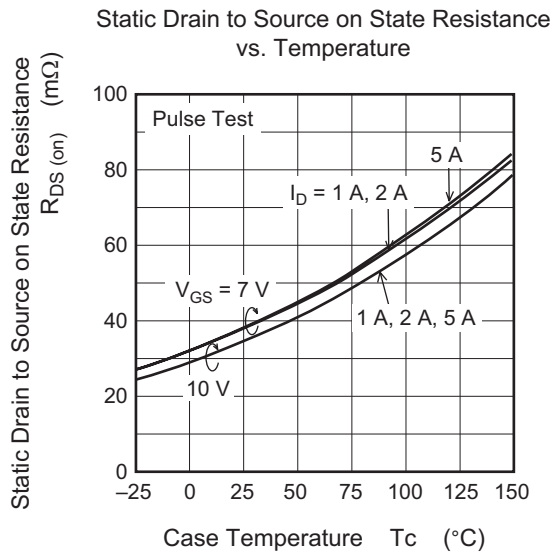
(Ta = 25°C)

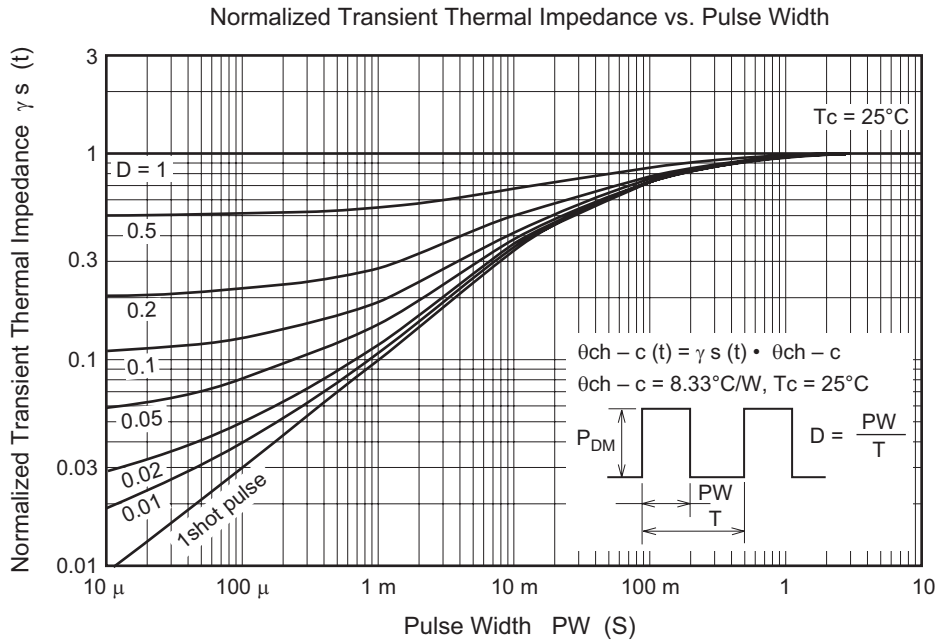
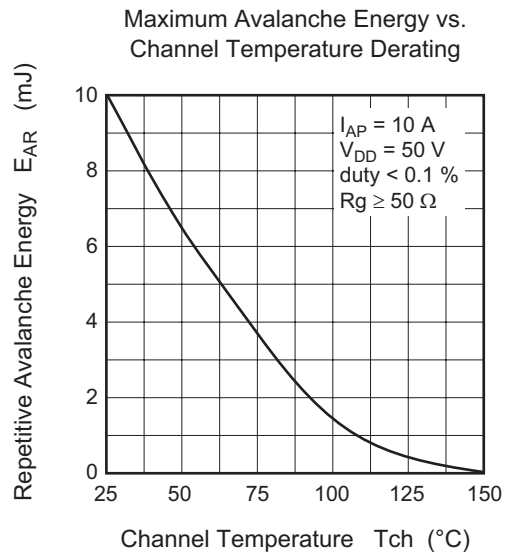
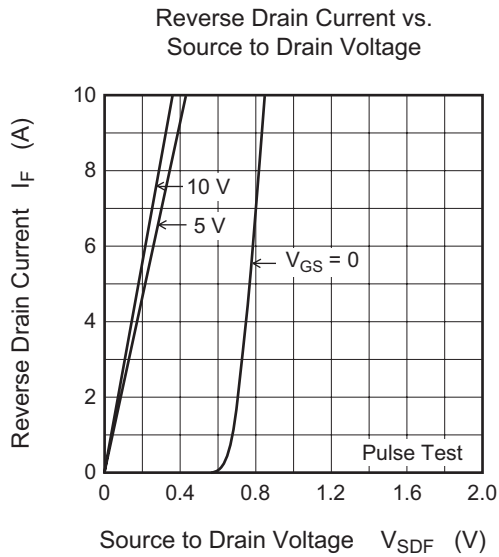
| Item | Symbol | Min | Typ | Max | Unit | Test Conditions |
|--|---------------|----------|------|----------|------------------|--|
| Drain to source breakdown voltage | $V_{(BR)DSS}$ | 100 | — | — | V | $I_D = 10 \text{ mA}$, $V_{GS} = 0$ |
| Gate to source breakdown voltage | $V_{(BR)GSS}$ | ± 20 | — | — | V | $I_G = \pm 100 \mu\text{A}$, $V_{DS} = 0$ |
| Gate to source leak current | I_{GSS} | — | — | ± 10 | μA | $V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$ |
| Zero gate voltage drain current | I_{DSS} | — | — | 1 | μA | $V_{DS} = 100 \text{ V}$, $V_{GS} = 0$ |
| Gate to source cutoff voltage | $V_{GS(off)}$ | 2.0 | — | 3.5 | V | $V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$ |
| Static drain to source on state resistance | $R_{DS(on)}$ | — | 35 | 44 | $\text{m}\Omega$ | $I_D = 5 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note 4} |
| | $R_{DS(on)}$ | — | 38 | 51 | $\text{m}\Omega$ | $I_D = 5 \text{ A}$, $V_{GS} = 7 \text{ V}$ ^{Note 4} |
| Forward transfer admittance | $ y_{fs} $ | 9 | 15 | — | S | $I_D = 5 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note 4} |
| Input capacitance | C_{iss} | — | 2000 | — | pF | $V_{DS} = 10 \text{ V}$ |
| Output capacitance | C_{oss} | — | 175 | — | pF | $V_{GS} = 0$ |
| Reverse transfer capacitance | C_{rss} | — | 90 | — | pF | $f = 1 \text{ MHz}$ |
| Total gate charge | Q_g | — | 32 | — | nC | $V_{DD} = 50 \text{ V}$ |
| Gate to source charge | Q_{gs} | — | 8.0 | — | nC | $V_{GS} = 10 \text{ V}$ |
| Gate to drain charge | Q_{gd} | — | 7.5 | — | nC | $I_D = 10 \text{ A}$ |
| Turn-on delay time | $t_{d(on)}$ | — | 18 | — | ns | $V_{GS} = 10 \text{ V}$, $I_D = 5 \text{ A}$ |
| Rise time | t_r | — | 11 | — | ns | $V_{DD} \cong 30 \text{ V}$ |
| Turn-off delay time | $t_{d(off)}$ | — | 60 | — | ns | $R_L = 6 \Omega$ |
| Fall time | t_f | — | 9 | — | ns | $R_g = 4.7 \Omega$ |
| Body-drain diode forward voltage | V_{DF} | — | 0.82 | 1.07 | V | $I_F = 10 \text{ A}$, $V_{GS} = 0$ ^{Note 4} |
| Body-drain diode reverse recovery time | t_{rr} | — | 50 | — | ns | $I_F = 10 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$ |

Note: 4. Pulse test

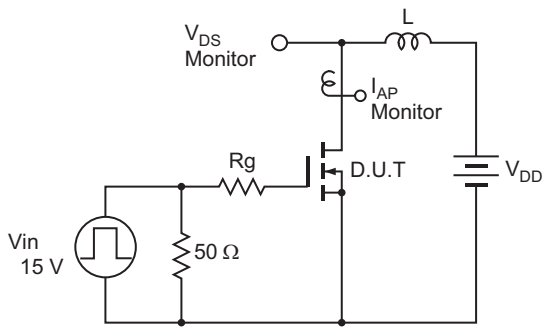
Main Characteristics



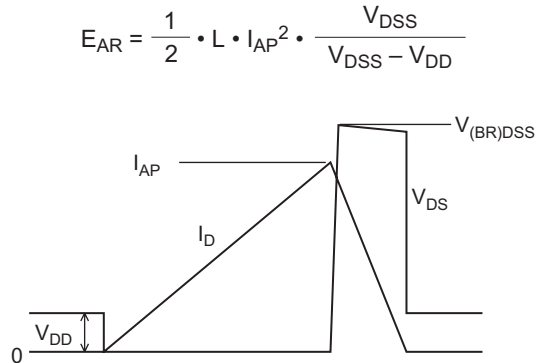


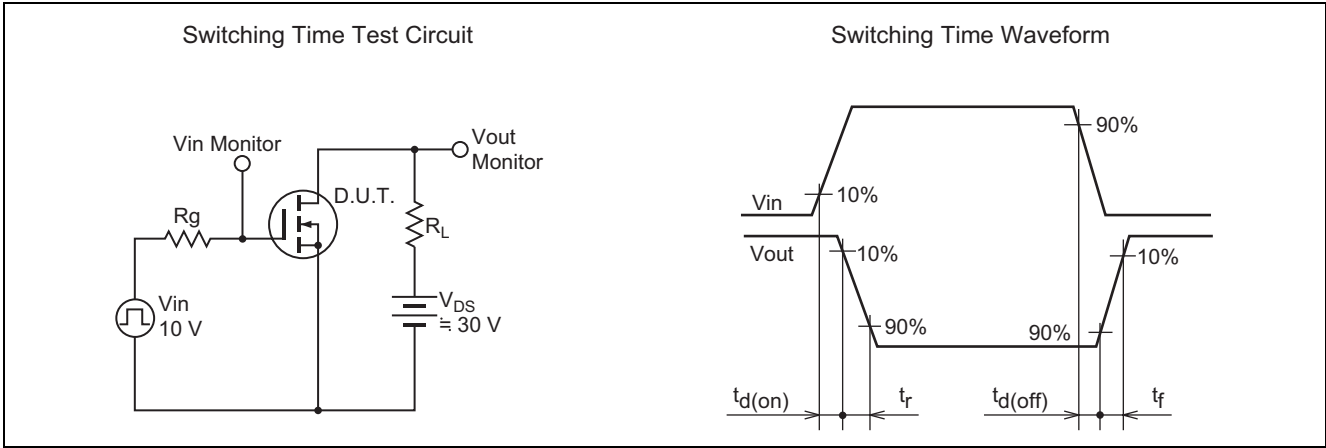


Avalanche Test Circuit

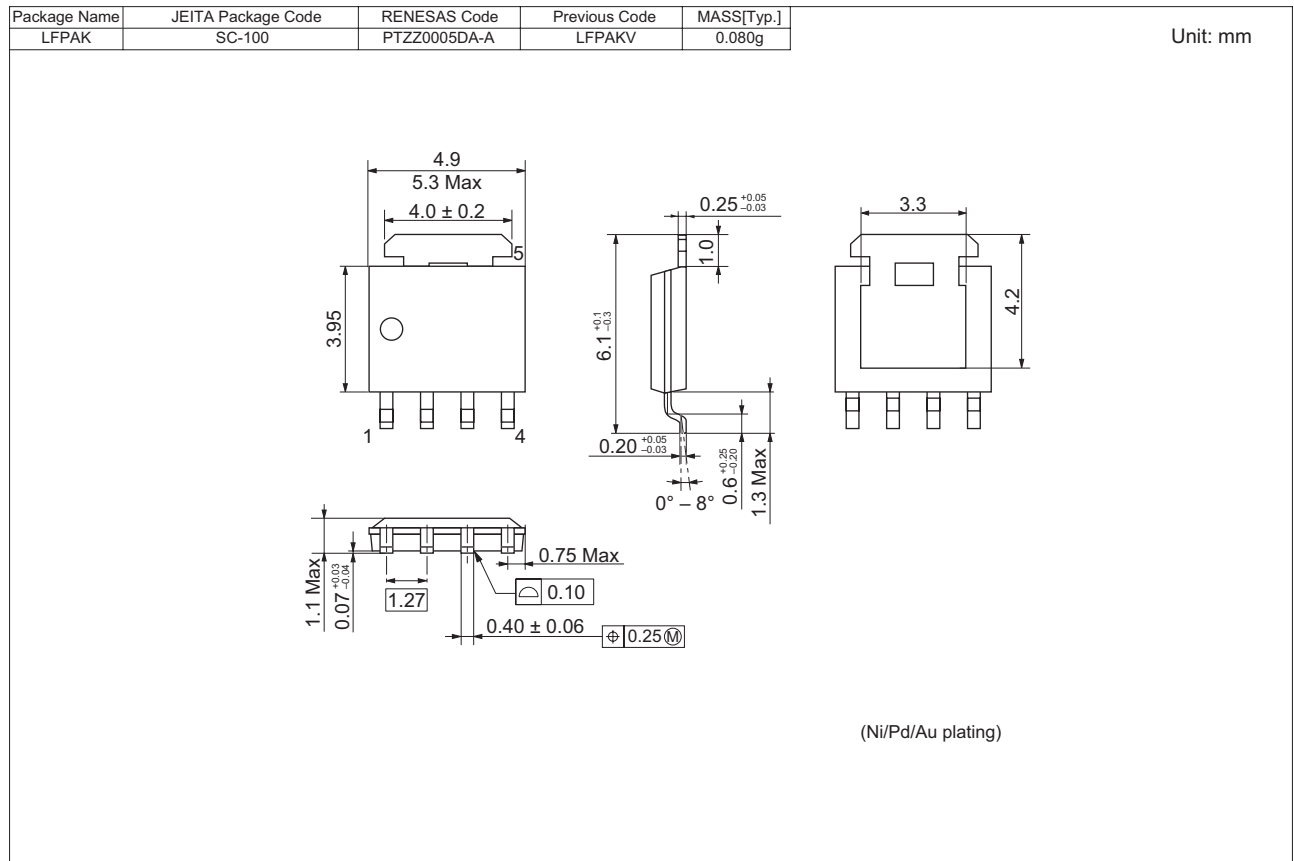


Avalanche Waveform





Package Dimensions



Ordering Information

| Part Name | Quantity | Shipping Container |
|---------------|----------|--------------------|
| HAT2142H-EL-E | 2500 pcs | Taping |

Notes:

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