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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HC4022

Octal Counters/Dividers

REJ03D0646-0200 (Previous ADE-205-532) Rev.2.00 Mar 30, 2006

Description

The HD74HC4022 is a four-stage Johnson octal counter with built-in code converter. High speed operation and spike-free outputs are obtained by use of a Johnson octal counter design. The eight decoded outputs are normally low, and go high only at their appropriate octal time period. The output changes occur on the positive-going edge of the clock pulse. This part can be used in frequency division application as well as octal counter or octal decode display applications.

Features

• High Speed Operation

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
UD74UC4022D	DILD 16 pin	PRDP0016AE-B	P		
HD74HC4022P	DILP-16 pin	(DP-16FV)		_	
HD74HC4022FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B	FP	EL (2,000 pcs/reel)	
TID74HO4022HT LL	301 - 10 pili (3L11A)	(FP-16DAV)	11	LL (2,000 pcs/leel)	
HD74HC4022RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)	

Note: Please consult the sales office for the above package availability.

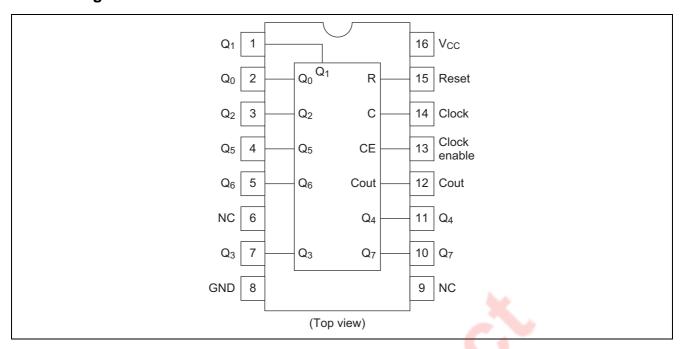
Function Table

Clock	Clock Enable	Reset	Output n
L	X	L	n
X	Н	L	n
	L	L	n + 1
	X	L	n
Н		L	n + 1
X	\int	L	n
X	X	Н	Q_0

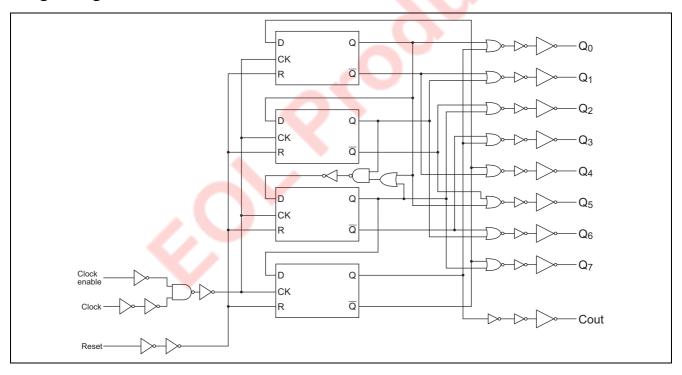
X: Irrelevant n < 4 ··· Carry = H

 $n \ge 4 \cdots Carry = L$

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	–0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	I _{OUT}	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V_{IN}, V_{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		$V_{CC} = 2.0 \text{ V}$
Input rise / fall time*1	t_r , t_f	0 to 500	ns	V _{CC} = 4.5 V
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

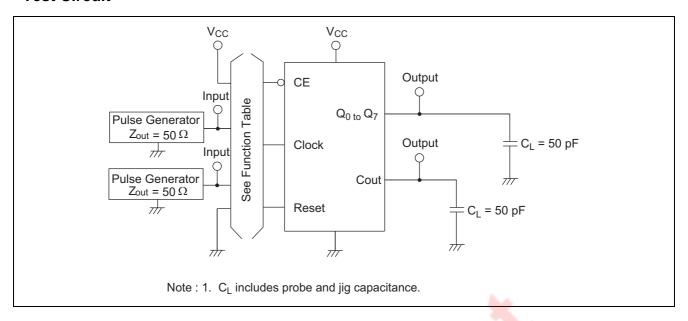
Electrical Characteristics

			Ta = 25°C		$Ta = -40 \text{ to} +85^{\circ}\text{C}$					
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Con	ditions
Input voltage	V _{IH}	2.0	1.5	-)	1.5	_	V		
		4.5	3.15			3.15				
		6.0	4.2	_	-	4.2	_			
	V_{IL}	2.0	4	_	0.5	_	0.5	V		
		4.5	1		1.35	_	1.35			
		6.0	_		1.8	_	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9		V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5		4.4				
		6.0	5.9	6.0	_	5.9				
		4.5	4.18	_		4.13	_			$I_{OH} = -4 \text{ mA}$
	1	6.0	5.68	_		5.63				$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	_	0.33			$I_{OH} = 4 \text{ mA}$
		6.0	_	_	0.26	_	0.33			$I_{OH} = 5.2 \text{ mA}$
Input current	lin	6.0			±0.1		±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply current	Icc	6.0	_	_	4.0	_	40	μΑ	Vin = V _{CC} or GNI	D, lout = 0 μA

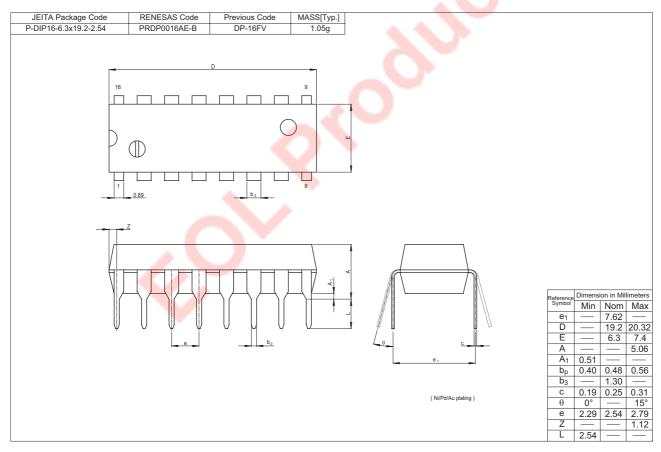
Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

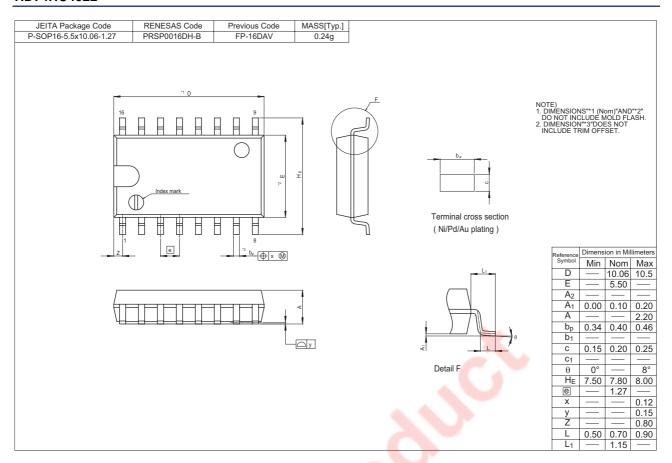
			Ta = 25°C Ta = -40 to +85°C						
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Maximum clock	f _{max}	2.0	_	_	6	_	5	MHz	
frequency		4.5	_	_	31	_	27		
		6.0	_	_	36	_	31		
Propagation delay	t _{PLH}	2.0	_	_	230	_	290	ns	Clock to Q
time	t _{PHL}	4.5	_	19	46	_	58		
		6.0	_	_	39	_	49		
	t _{PLH}	2.0	_	_	230	_	290	ns	Reset to Q
	t _{PHL}	4.5	_	16	46	_	58		
		6.0	_	_	39	_	49		
	t _{PLH}	2.0	_	_	230	_	290	ns	Clock to Cout
	t _{PHL}	4.5	_	_	46	_	58		
		6.0	_	_	39	_	49		
	t _{PLH}	2.0	_	_	230	_	290	ns	Reset to Cout
	t _{PHL}	4.5	_	13	46	_	58		
		6.0	_	_	39	_	49	- 1	
Pulse width	t _w	2.0	80	_	_	100	_	ns	
		4.5	16	7	_	20	_		4
		6.0	14	_	_	17	- 1		
Setup time	t _{su}	2.0	50	_	_	65	- -	ns	
		4.5	10	_	_	13	7-7		
		6.0	9	_	_	11	-		
Hold time	t _h	2.0	75	_	_	95		ns	
		4.5	15	_	_	19	_		
		6.0	13	_	7	16	_		
Removal time	t_{rem}	2.0	75	7	7	95	_	ns	
		4.5	15	-3		19	_		
		6.0	13	_	J	16	_		
Output rise/fall	t _{TLH}	2.0	4	_	75	_	95	ns	
time	t_{THL}	4.5	-		15	_	19		
		6.0			13	_	16		
Input capacitance	Cin	7		5	10	_	10	pF	

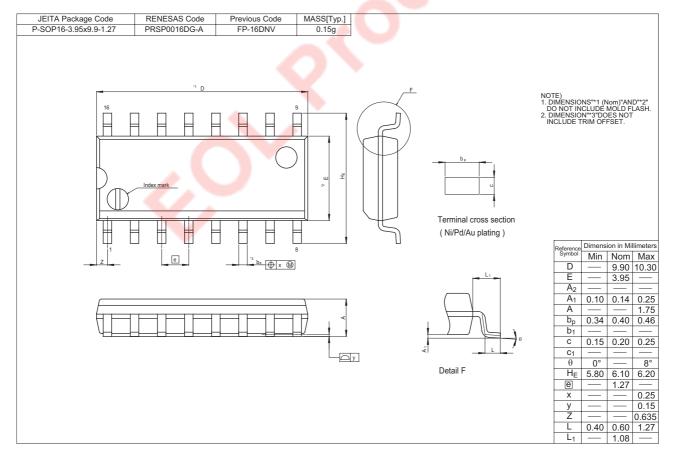
Test Circuit



Package Dimensions







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