# RENESAS

# HD74LV1G126A

Bus Buffer Gate with 3-state Output

# Description

The HD74LV1G126A has a bus buffer gate with 3–state output in a 5 pin package. Output is disabled when the associated output enable (OE) input is low. To ensure the high impedance state during power up or power down, OE should be connected to  $V_{CC}$  through a pull-down resistor; the minimum value of the resistor is determined by the current sourcing capability of the driver. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

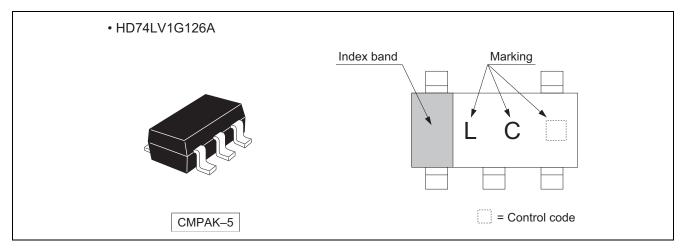
## Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV126A Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V to 5.5 V) All outputs  $V_0$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V, Output : Z)
- Output current  $\pm 6 \text{ mA}$  (@V<sub>CC</sub> = 3.0 V to 3.6 V),  $\pm 12 \text{ mA}$  (@V<sub>CC</sub> = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G126ACME	CMPAK–5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)
HD74LV1G126AVSE	VSON–5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

Note: Please consult the sales office for the above package availability.

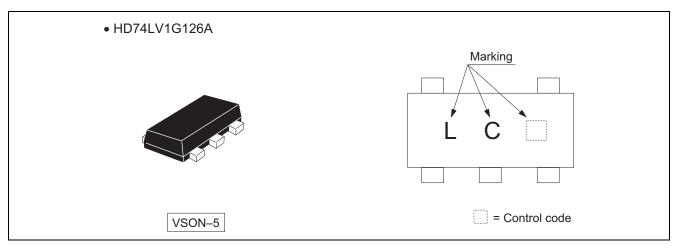
## **Outline and Article Indication**



R04DS0026EJ0800 Rev.8.00 Jan 10, 2014



## **Outline and Article Indication**



## **Function Table**

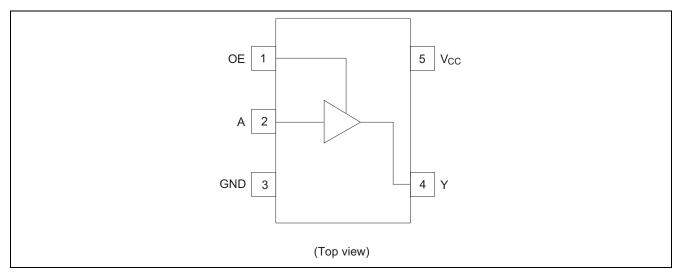
Inp	Inputs			
OE	А	Output Y		
Н	Н	Н		
Н	L	L		
L	Х	Z		

- H : High level
- L : Low level

X : Immaterial

Z : High impedance

## **Pin Arrangement**





## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V <sub>cc</sub>	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	M	-0.5 to V <sub>CC</sub> + 0.5	v	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	V	V <sub>CC</sub> : OFF or Output : Z
Input clamp current	I <sub>IK</sub>	-20	mA	V <sub>1</sub> < 0
Output clamp current	Ι <sub>ΟΚ</sub>	±50	mA	$V_0 < 0$ or $V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_{\rm O} = 0$ to $V_{\rm CC}$
Continuous current through $V_{CC}$ or GND	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) $^{*3}$	P <sub>T</sub>	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

## **Recommended Operating Conditions**

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	V	0	V <sub>CC</sub>	- V	
Output voltage range	Vo	0	5.5	v	Output : Z
		—	1		$V_{CC} = 1.65$ to 1.95 V
		—	2	mA	$V_{CC} = 2.3$ to 2.7 V
	I <sub>OL</sub>	—	6		$V_{CC} = 3.0$ to 3.6 V
Output summark		—	12		$V_{CC} = 4.5$ to 5.5 V
Output current	I <sub>он</sub>	—	-1		V <sub>CC</sub> = 1.65 to 1.95 V
		—	-2		$V_{CC} = 2.3$ to 2.7 V
		—	-6		$V_{CC} = 3.0$ to 3.6 V
		—	-12		$V_{CC} = 4.5$ to 5.5 V
		0	300		$V_{CC} = 1.65$ to 1.95 V
Input transition rise or fall rate	Δt / Δv	0	200	ns / V	$V_{CC} = 2.3$ to 2.7 V
Input transition rise or fall rate	$\Delta t / \Delta v$	0	100	115 / V	$V_{CC} = 3.0$ to 3.6 V
		0	20	]	$V_{CC}$ = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.



## **Electrical Characteristics**

## • Ta = -40 to $85^{\circ}C$

Item	Symbol	V <sub>cc</sub> (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V <sub>CC</sub> ×0.75	_	_		
	V	2.3 to 2.7	V <sub>CC</sub> ×0.7	—	—		
	VIH	3.0 to 3.6	V <sub>CC</sub> ×0.7	—	—		
Innut voltogo		4.5 to 5.5	V <sub>CC</sub> ×0.7	_	—	V	
Input voltage		1.65 to 1.95	—	_	V <sub>CC</sub> ×0.25	v	
	V	2.3 to 2.7	—		V <sub>CC</sub> ×0.3		
	VIL	3.0 to 3.6	—	_	V <sub>CC</sub> ×0.3		
		4.5 to 5.5	—	_	V <sub>CC</sub> ×0.3		
		1.8	—	0.25	—		
	V	2.5	—	0.30	—	V	$V_{T}^{+} - V_{T}^{-}$
Hysteresis voltage	V <sub>H</sub>	3.3	—	0.35	—	v	$v_{T} - v_{T}$
		5.0	—	0.45	—		
		Min to Max	V <sub>CC</sub> -0.1	_	—		I <sub>OH</sub> = -50 μA
		1.65	1.4	_	—		$I_{OH} = -1 \text{ mA}$
	V <sub>OH</sub>	2.3	2.0				$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	—		I <sub>OH</sub> =6 mA
Output valtage		4.5	3.8	_	—	V	I <sub>OH</sub> = -12 mA
Output voltage		Min to Max	—	_	0.1	v	I <sub>OL</sub> = 50 μA
		1.65	—	_	0.3		I <sub>OL</sub> = 1 mA
	V <sub>OL</sub>	2.3	—	_	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	—	_	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	—	—	0.55		I <sub>OL</sub> = 12 mA
Input current	I <sub>IN</sub>	0 to 5.5	—	—	±1	μA	$V_{IN} = 5.5 V \text{ or GND}$
Off state output current	I <sub>OZ</sub>	Min to Max	—	_	±5	μΑ	$V_0 = 5.5 V \text{ or GND}$
Quiescent supply current	I <sub>CC</sub>	5.5	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I <sub>OFF</sub>	0	—	_	5	μΑ	$V_{IN}$ or $V_O = 0$ to 5.5 V
Input capacitance	CIN	3.3	_	3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



## **Switching Characteristics**

### $\bullet \quad V_{CC} = 1.8 \pm 0.15 \ V$

ltem	Symbol		Ta = 25°C		Ta = -40	) to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	onit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	13.5	23.5	1.0	26.0		C <sub>L</sub> = 15 pF	А	v
delay time	t <sub>PHL</sub>	_	19.0	33.0	1.0	36.0	ns	$C_L = 50 \text{ pF}$	A	ř
Enable time	t <sub>ZH</sub>	_	13.7	26.5	1.0	29.0		$C_L = 15 \text{ pF}$	OE	v
Enable lime	t <sub>ZL</sub>	_	20.5	36.0	1.0	38.0	ns	$C_L = 50 \text{ pF}$	UE	ř
Disable time	t <sub>HZ</sub>	_	8.3	20.0	1.0	22.5		$C_L = 15 \text{ pF}$	OE	v
Disable time	t <sub>LZ</sub>	_	13.0	29.5	1.0	32.0	ns	$C_L = 50 \text{ pF}$	UE	ř

## • $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol		Ta = 25°C		Ta = -40	) to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	7.1	13.0	1.0	15.5		$C_L = 15 \text{ pF}$	А	V
delay time	t <sub>PHL</sub>	_	9.2	16.5	1.0	18.5	ns	$C_L = 50 \text{ pF}$	А	T
Enable time	t <sub>ZH</sub>	_	7.4	13.0	1.0	15.5		$C_L = 15 \text{ pF}$	OE	V
Enable lime	t <sub>ZL</sub>	_	9.5	16.5	1.0	18.5	ns	$C_L = 50 \text{ pF}$	UE	r
Disable time	t <sub>HZ</sub>	_	5.7	14.7	1.0	17.0		$C_L = 15 \text{ pF}$	OE	V
	t <sub>LZ</sub>	_	8.1	18.2	1.0	20.5	ns	$C_L = 50 \text{ pF}$		r

## • $V_{CC} = 3.3 \pm 0.3 V$

ltem	Symbol		Ta = 25°C		Ta = -40	) to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	5.0	8.0	1.0	9.5		$C_L = 15 \text{ pF}$	А	V
delay time	t <sub>PHL</sub>	_	6.4	11.5	1.0	13.0	ns	$C_L = 50 \text{ pF}$	А	T
Enchle time	t <sub>ZH</sub>	_	5.1	8.0	1.0	9.5		$C_L = 15 \text{ pF}$		V
Enable time	t <sub>ZL</sub>	_	6.6	11.5	1.0	13.0	ns	$C_L = 50 \text{ pF}$	OE	Ť
Dischla time	t <sub>HZ</sub>	_	4.4	9.7	1.0	11.5		$C_L = 15 \text{ pF}$		V
Disable time	t <sub>LZ</sub>	-	6.1	13.2	1.0	15.0	ns	$C_L = 50 \text{ pF}$	OE	Ť

## • $V_{CC} = 5.0 \pm 0.5 V$

ltem	Symbol		Ta = 25°C		Ta = -40	to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	3.5	5.5	1.0	6.5		$C_L = 15 \text{ pF}$	А	V
delay time	t <sub>PHL</sub>	-	4.6	7.5	1.0	8.5	ns	C <sub>L</sub> = 50 pF	A	T
Enable time	t <sub>ZH</sub>	_	3.6	5.1	1.0	6.0		$C_L = 15 \text{ pF}$		V
Enable time	t <sub>ZL</sub>	_	4.6	7.1	1.0	8.0	ns	C <sub>L</sub> = 50 pF	OE	Ý
Disable time	t <sub>HZ</sub>	_	3.3	6.8	1.0	8.0		$C_L = 15 \text{ pF}$	OE	V
	t <sub>LZ</sub>	_	4.3	8.8	1.0	10.0	ns	$C_L = 50 \text{ pF}$	UE	ſ

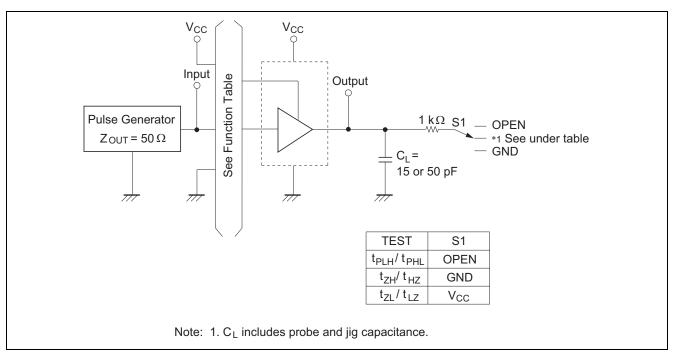
## **Operating Characteristics**

•  $C_L = 50 \text{ pF}$ 

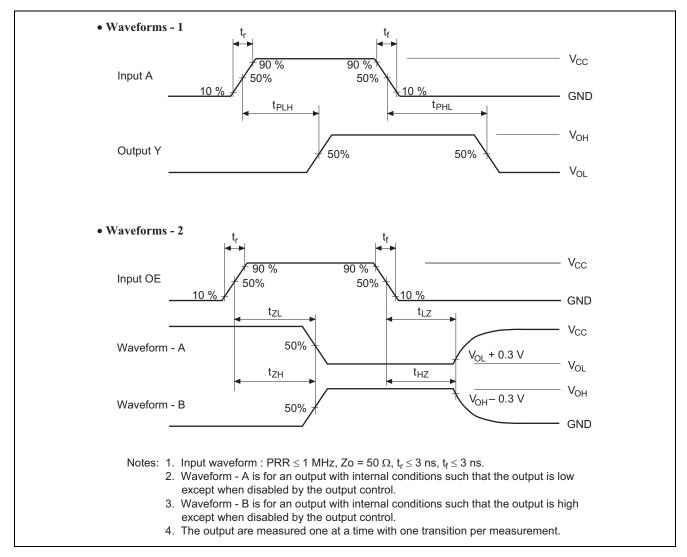
ltom	Symbol	V 00		Ta = 25°C		l lmit	Toot Conditions	
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Unit	Test Conditions	
Power dissipation	0	3.3	-	10.5	-	~ <b>F</b>	f = 10 MHz	
capacitance	C <sub>PD</sub>	5.0	-	11.5	-	pF		



## **Test Circuit**



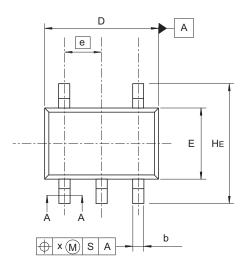
## Waveforms

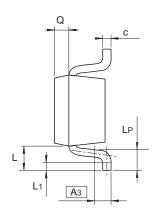


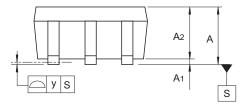


## Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-88A	PTSP0005ZC-A	CMPAK-5 / CMPAK-5V	0.006









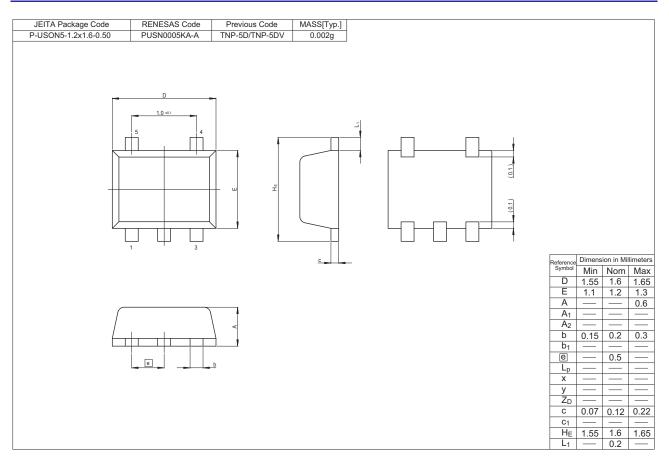
A-A Section

Reference	Dimensi	ons in mi	llimeters
Symbol	Min	Nom	Max
Α	0.8		1.1
A <sub>1</sub>	0		0.1
A <sub>2</sub>	0.8	0.9	1.0
A <sub>3</sub>		0.25	
b	0.15	0.22	0.3
С	0.1	0.13	0.15
D	1.8	2.0	2.2
E	1.15	1.25	1.35
е		0.65	
HE	1.8	2.1	2.4
L	0.3		0.7
L <sub>1</sub>	0.1		0.5
LP	0.2		0.6
Х			0.05
У			0.05
Q		0.25	

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### HD74LV1G126A





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