RENESAS

HD74LV1G66A

Analog Switch

R04DS0023EJ0800 Rev.8.00 Jan 10, 2014

Description

The HD74LV1G66A has an analog switch in a 5 pin package. Switch section has its enable input control (C). Highlevel voltage applied to C turns on the switch section. Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV4066A Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- Control inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
- Control inputs has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G66ACME	CMPAK–5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)
HD74LV1G66AVSE	VSON–5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Outline and Article Indication





Outline and Article Indication



Function Table

Control	Switch
L	OFF
Н	ON

H : High level

L : Low level

Pin Arrangement





Absolute Maximum Ratings

ltem	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to V _{CC} + 0.5	V	Output : H or L
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{ок}	±50	mA	$V_{\rm O}$ < 0 or $V_{\rm O}$ > $V_{\rm CC}$
Continuous output current	lo	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25° C (in still air) ^{*3}	PT	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

ltem	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Input / output voltage range	V _{I/O}	0	V _{CC}	V	
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	A 4 / A	0	200	nc / \/	V_{CC} = 2.3 to 2.7 V
	$\Delta t / \Delta v$	0	100	ns / v	$V_{CC} = 3.0$ to 3.6 V
		0	20		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating control inputs must be held high or low.



			٦	Γ _a = 25°0	c	Ta	= -40 to 85	°C		Test
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Тур	Max	Unit	Conditions
		1.65 to 1.95	—	—	—	V _{cc} ×0.75		_		
	.,	2.3 to 2.7	—	—	—	V _{cc} ×0.7		_		
	V _{IH}	3.0 to 3.6			_	V _{CC} ×0.7	_	_		
la a de calta da		4.5 to 5.5				V _{CC} ×0.7	_	_		Control in put only
Input voltage		1.65 to 1.95				_	_	V _{CC} ×0.25	V	Control input only
	N	2.3 to 2.7		_	_	_	_	V _{CC} ×0.3		
	VIL	3.0 to 3.6	—	_	—	_	_	V _{cc} ×0.3		
		4.5 to 5.5	_	_	_	_	-	V _{cc} ×0.3		
		1.8	_	_	_	_	0.25	—		
Hysteresis	V	2.5	_	_	_	_	0.30	—	V	<u> </u>
voltage	V _H	3.3	_	_	_	_	0.35	—	v	$v_{T} - v_{T}$
		5.0	_	_	_	_	0.45	—		
		1.65	—	120	360			450		
On-state switch	switch ce R _{on}	2.3	—	60	180			225	0	$V_{IN} = V_{CC}$ or GND
resistance		3.0	_	50	150	_	-	190	52	$V_{\rm C} = V_{\rm H}$
		4.5	_	40	75			100		II = 1 IIIA
		1.65	—	700	1100			1400		$V_{IN} = V_{CC}$ to GND
Peak on	Б	2.3	—	250	500			600	0	
resistance	RON (P)	3.0	—	100	180			225	52	$v_{\rm C} = v_{\rm H}$
		4.5	—	50	100			125		
Off-state switch leakage current	I _{s (OFF)}	5.5	_	_	±0.1			±1.0	μΑ	$\begin{split} V_{\text{IN}} &= V_{\text{CC}}, \\ V_{\text{OUT}} &= GND \\ \text{or } V_{\text{IN}} &= GND, \\ V_{\text{O}} &= V_{\text{CC}}, V_{\text{C}} &= V_{\text{IL}} \end{split}$
On-state switch leakage current	I _{s (ON)}	5.5	_	_	±0.1	_	_	±1.0	μA	$V_{IN} = V_{CC} \text{ or } GND$ $V_C = V_{IH}$
Input current	I _{IN}	0 to 5.5	—	—	±0.1	_	_	±1.0	μΑ	V_{IN} = 5.5 V or GND
Quiescent supply current	I _{cc}	5.5	_	_	_	—	—	10	μA	$V_{IN} = V_{CC}$ or GND
Control input capacitance	C _{IC}	_	_	3.5	_	_	_	_	pF	
Switch terminal capacitance	CIN/OUT	_	_	4.0	_	_	_	_	pF	
Feed through capacitance	CIN-OUT	—		0.5		_	_	_	pF	

Electrical Characteristics

Switching Characteristics

$\bullet \quad V_{CC} = 1.8 \pm 0.15 \ V$

ltom Symbol		Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		4.0	13.0		19.0	20	C∟ = 15 pF	IN/OUT	OUT/IN
delay time	t _{PHL}	_	11.0	23.0	_	29.0	ns	$C_L = 50 \text{ pF}$	or OUT/IN	or IN/OUT
Enchla time	t _{ZH}	_	11.0	24.0	_	29.0		$C_L = 15 \text{ pF}$	<u> </u>	IN/OUT
Enable lime	t _{ZL}	_	18.0	44.0	_	51.0	ns	$C_L = 50 \text{ pF}$	C	or OUT/IN
Dischla time	t _{HZ}	_	11.0	21.0	_	29.0		$C_L = 15 \text{ pF}$	<u> </u>	IN/OUT
Disable time	t _{LZ}	_	18.0	46.0	_	53.0	ns	$C_L = 50 \text{ pF}$	C	or OUT/IN

• $V_{CC} = 2.5 \pm 0.2 V$

léom	Symbol	Ta = 25°C			Ta = -40 to 85°C		l lmit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	2.0	10.0	—	16.0	20	C _L = 15 pF	IN/OUT	OUT/IN
delay time	t _{PHL}	—	5.0	12.0	_	18.0	ns	C _L = 50 pF	or OUT/IN	or IN/OUT
Enchle time	t _{ZH}	—	6.0	15.0	_	20.0		C _L = 15 pF	<u> </u>	IN/OUT
Enable lime	t _{ZL}	—	8.0	25.0	_	32.0	ns	C _L = 50 pF	C	or OUT/IN
Dischla time	t _{HZ}	—	7.0	15.0	_	23.0		C _L = 15 pF	<u> </u>	IN/OUT
Disable time	t _{LZ}	—	11.0	25.0	_	32.0	ns	C _L = 50 pF	C	or OUT/IN

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

ltom Symbol		Ta = 25°C			Ta = -40	Ta = -40 to 85°C		Test	FROM	то
nem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		1.5	6.0		10.0	20	$C_L = 15 \text{ pF}$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	4.0	9.0	-	12.0	ns	$C_L = 50 \text{ pF}$	or OUT/IN	or IN/OUT
Enchla time	t _{ZH}	_	4.0	11.0	-	15.0		$C_L = 15 \text{ pF}$	<u> </u>	IN/OUT
Enable lime	t _{ZL}	_	6.0	18.0	-	22.0	ns	$C_L = 50 \text{ pF}$	C	or OUT/IN
Dischla time	t _{HZ}	_	5.0	11.0	-	15.0		$C_L = 15 \text{ pF}$	<u> </u>	IN/OUT
Disable time	t _{LZ}	_	8.0	18.0	_	22.0	ns	C _L = 50 pF		or OUT/IN

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

Itom	Symbol		Ta = 25°C			Ta = -40 to 85°C		Test	FROM	то
nem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		1.0	4.0		7.0	20	$C_L = 15 \text{ pF}$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	3.0	6.0	_	8.0	ns	C _L = 50 pF	or OUT/IN	or IN/OUT
Enchla time	t _{zH}	_	3.0	7.0	_	10.0		C _L = 15 pF	<u> </u>	IN/OUT
Enable lime	t _{ZL}	_	5.0	12.0	_	16.0	ns	C _L = 50 pF	C	or OUT/IN
Dischla time	t _{HZ}	_	4.0	7.0	_	10.0		C _L = 15 pF	0	IN/OUT
	t _{LZ}	_	6.0	12.0	_	16.0	115	C _L = 50 pF		or OUT/IN

Operating Characteristics

• $C_L = 50 \ pF$

ltom	Symbol	V 00	Ta = 25°C			l lmit	Toot Conditions	
item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	Test conditions	
Power dissipation	<u> </u>	3.3	-	3.5	_	~ F	f = 10 MHz	
capacitance	CPD	5.0	-	4.0	_	рг		



Test Circuit

• R_{ON}



$\bullet \quad I_{S(off)},\,I_{S(on)}$



• t_{PLH}, t_{PHL}





• t_{ZH} , t_{ZL} / t_{HZ} , t_{LZ}



• C_{IN/OUT}, C_{IN-OUT}





Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-88A	PTSP0005ZC-A	CMPAK-5 / CMPAK-5V	0.006









A-A Section

Reference	Dimensi	ons in mi	llimeters
Symbol	Min	Nom	Max
A	0.8		1.1
A ₁	0		0.1
A ₂	0.8	0.9	1.0
A ₃		0.25	
b	0.15	0.22	0.3
С	0.1	0.13	0.15
D	1.8	2.0	2.2
E	1.15	1.25	1.35
е		0.65	
HE	1.8	2.1	2.4
L	0.3		0.7
L ₁	0.1		0.5
LP	0.2		0.6
Х			0.05
у			0.05
Q		0.25	

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HD74LV1G66A





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