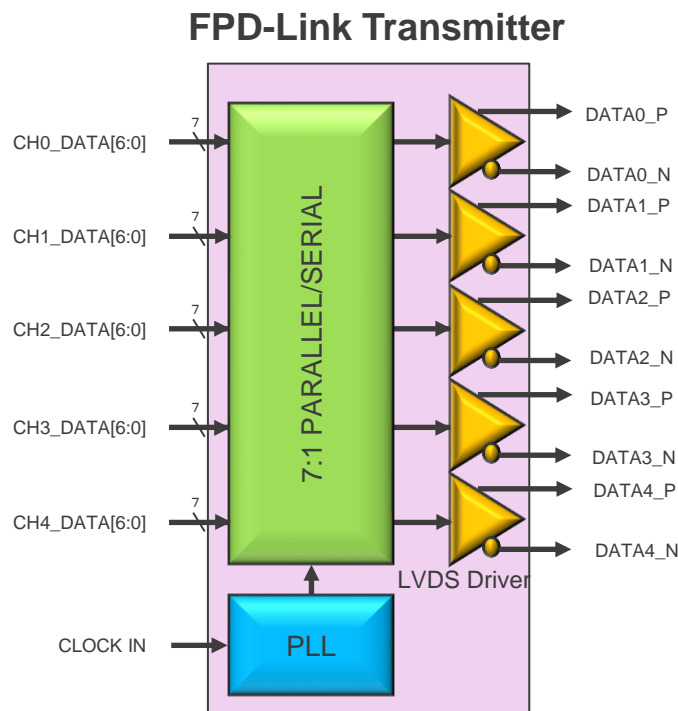Interface IP

FPD-Link Transmitter for TSMC 28nm HPC

Overview

The Renesas FPD-Link Transmitter is useful 5 Channel LVDS Driver and 7:1 PARALLEL to SERIAL Converting of TSMC 28nm HPC process. This macro can be select CLOCK Channel.
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Key Features

- Renesas FPD-Link Transmitter can be used for analog transmitter of following interface .
      - ANSI/TIA/EIA-644
- Technology is TSMC 28nm HPC 1p9M (4x2y2r).
- Supply voltage can be applied 1.0V for overdrive of core voltage, 1.8V for IO voltage.
- With an input clock at 75MHz and input data at 150Mbps, the maximum transmission data rate of each channel is 1.05Gbps.

Block Diagram

FPD-Link Transmitter

CTPD-24-062
R06PF0064EJ0200

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2024.07