

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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Not recommended  
for new design

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## M66223FP

4096 × 16-Bit Mail Box

REJ03F0248-0200  
Rev.2.00  
Sep 14, 2007

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### Description

The M66223 is a mail box that incorporates a complete CMOS shared memory cell of 4096 × 16-bit configuration using high-performance silicon gate CMOS process technology, and is equipped with two access ports of A and B.

Access ports A and B are equipped with independent addresses  $\overline{CS}$ ,  $\overline{WE}$  and  $\overline{OE}$  control pins and I/O pins to allow independent and asynchronous read/write operations from/to shared memory individually. This product also incorporates a port adjustment arbitration function in address contention from both ports.

### Features

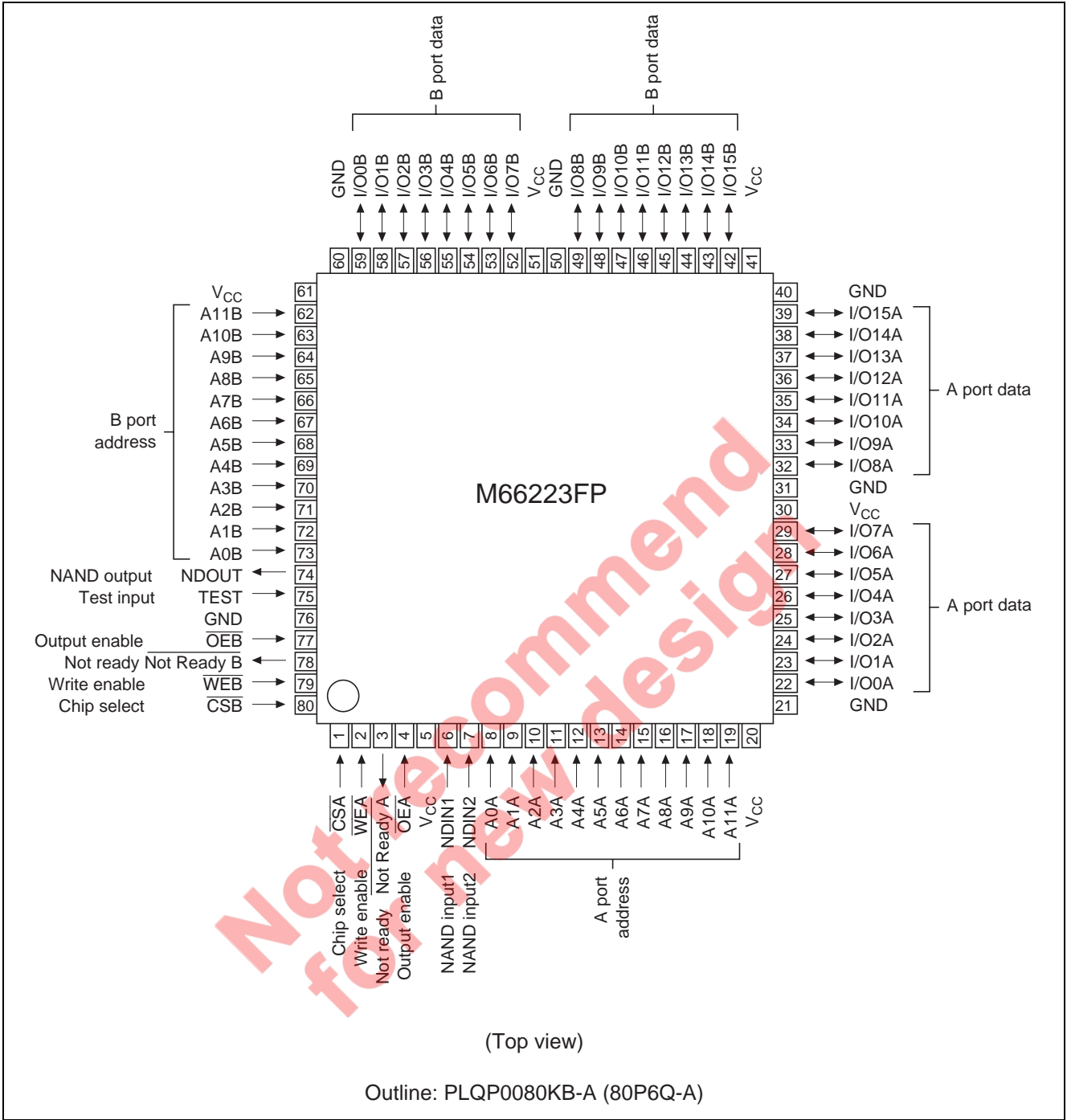
- Memory configuration of 4096 words × 16 bits
- Complete asynchronous accessibility from A and B
- Built-in port arbitration function
- 5 V single power supply
- Not Ready output pin is provided (open drain output)
- TTL direct-coupled I/O
- 3-state output for I/O pins
- Built-in 2-input NAND gate for public use

### Application

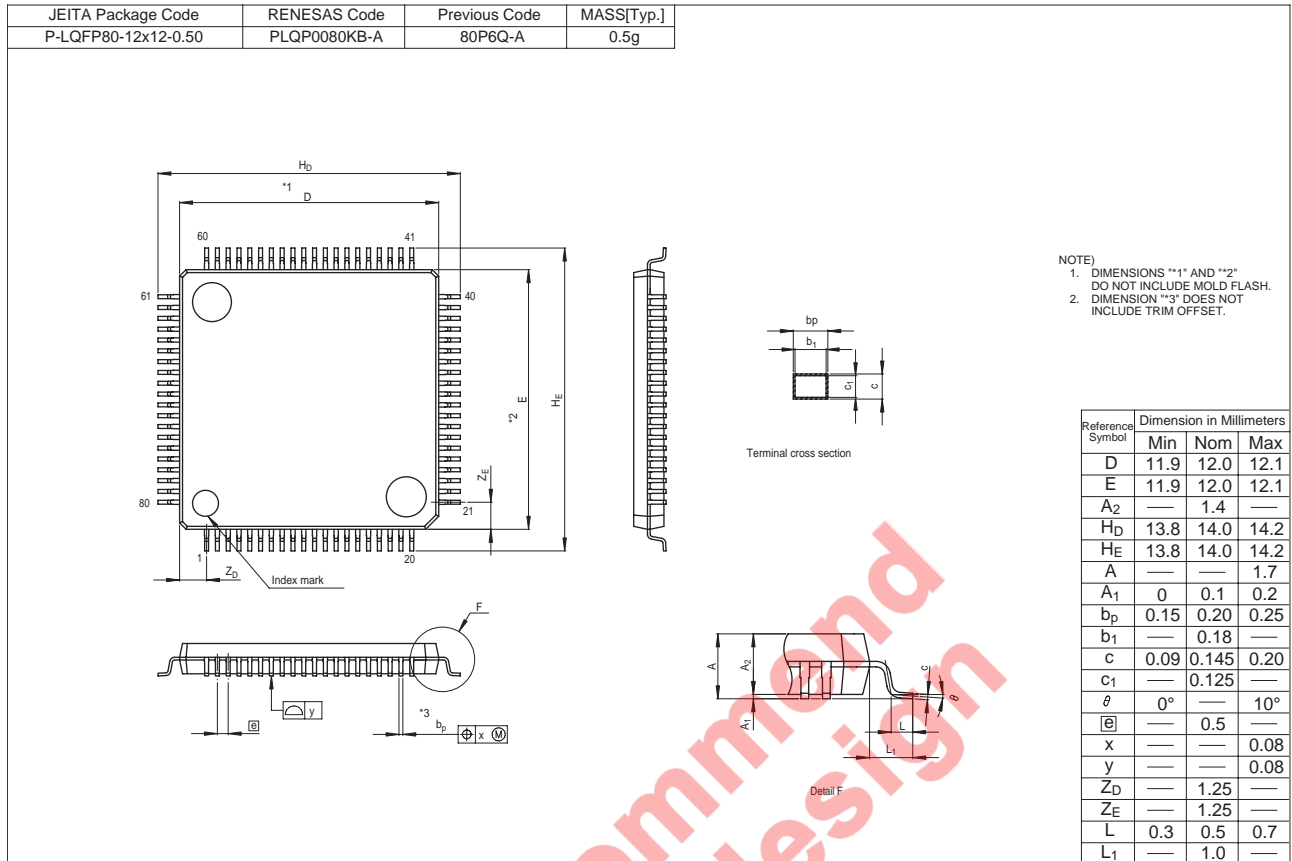
Inter-MPU data transfer memory, buffer memory for image processing system.

Not recommend  
for new design

Pin Arrangement



### Package Dimensions



Not recommended for new design

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