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April 1st, 2010 Renesas Electronics Corporation

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RD74LVC126B

Quad. Bus Buffer Gates with 3-state Outputs

REJ03D0499-0200 Rev.2.00 Dec. 10, 2004

Description

The RD74LVC126B has four bus buffer gates in a 14 pin package. The device requires the three state control input OE to be taken low to put the output into the high impedance condition. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 1.65 \text{ V}$ to 5.5 V
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
- All outputs V_{OUT} (Max.) = 5.5 V (@V_{CC} = 0 V or output off state)
- Typical V_{OL} ground bounce < 0.8 V (@V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@V_{CC} = 3.3 V, Ta = 25°C)
- High output current $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$
 - $\begin{array}{l} \pm 8 \mbox{ mA} (@V_{CC} = 2.3 \mbox{ V}) \\ \pm 12 \mbox{ mA} (@V_{CC} = 2.7 \mbox{ V}) \\ \pm 24 \mbox{ mA} (@V_{CC} = 3.0 \mbox{ V to } 5.5 \mbox{ V}) \end{array}$
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
RD74LVC126BFPEL	SOP-14 pin (JEITA)	FP–14DAV	FP	EL (2,000 pcs/reel)
RD74LVC126BTELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

Function Table

Inp		
OE	A	Outputs Y
L	Х	Z
Н	L	L
Н	Н	Н

H: High level

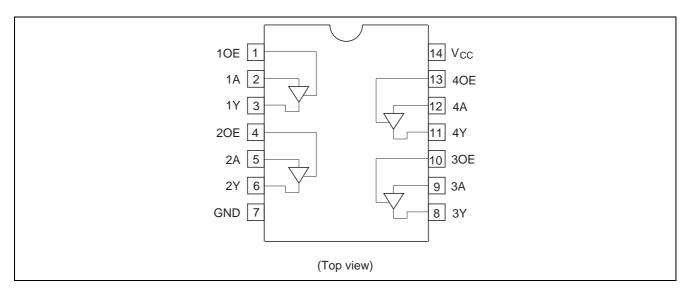
L: Low level

X: Immaterial

Z: High impedance



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	-0.5 to 7.0	V	
Input diode current	I _{IK}	-50	mA	$V_1 = -0.5 V$
Input voltage	VI	-0.5 to 7.0	V	
Output diode current	I _{ОК}	-50	mA	$V_{\rm O} = -0.5 \ V$
		50		$V_{\rm O} = V_{\rm CC} + 0.5 \rm V$
Output voltage	Vo	–0.5 to V _{CC} +0.5	V	Output "H" or "L"
		-0.5 to 7.0		Output "Z" or V _{CC} : OFF
Output current	lo	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	±100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	tage V _{CC} 1.5 to 5.5 V		V	Data hold
		1.65 to 5.5		At operation
Input / output voltage	VI	0 to 5.5	V	
	Vo	0 to V _{CC}	V	Output "H" or "L"
		0 to 5.5		Output "Z" or V _{CC} : OFF
Operating temperature	Та	-40 to 85	°C	
Output current	I _{OH}	-4	mA	V _{CC} = 1.65 V
		-8		V _{CC} = 2.3 V
		-12		V _{CC} = 2.7 V
		-24		V_{CC} = 3.0 V to 5.5 V
	I _{OL}	4	mA	V _{CC} = 1.65 V
		8		V _{CC} = 2.3 V
		12		V _{CC} = 2.7 V
		24		V_{CC} = 3.0 V to 5.5 V
Input rise / fall time *1	t _r , t _f	20	ns/V	V _{CC} = 1.65 V to 2.7 V
		10		V_{CC} = 3.0 V to 5.5 V

Note: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.



Electrical Characteristics

			Ta = -40 to 85°C			
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions
Input voltage	VIH	1.65 to 1.95	V _{CC} ×0.65	_	V	
		2.3 to 2.7	1.7	_		
		2.7 to 3.6	2.0	_		
		4.5 to 5.5	V _{CC} ×0.7	_		
	VIL	1.65 to 1.95	—	V _{CC} ×0.35	V	
		2.3 to 2.7	—	0.7		
		2.7 to 3.6	—	0.8		
		4.5 to 5.5	—	V _{CC} ×0.3		
Output voltage	V _{OH}	1.65 to 5.5	V _{CC} -0.2	—	V	I _{OH} = -100 μA
		1.65	1.2	_		$I_{OH} = -4 \text{ mA}$
		2.3	1.7	_		$I_{OH} = -8 \text{ mA}$
		2.7	2.2	—		$I_{OH} = -12 \text{ mA}$
		3.0	2.4	—		
		3.0	2.2	—		I _{OH} = -24 mA
		4.5	3.8	—		
	V _{OL}	1.65 to 5.5	—	0.2	V	I _{OL} = 100 μA
		1.65	—	0.45		I _{OL} = 4 mA
		2.3	—	0.7		I _{OL} = 8 mA
		2.7	—	0.4		I _{OL} = 12 mA
		3.0	—	0.55		I _{OL} = 24 mA
		4.5	—	0.55		
Input current	I _{IN}	0 to 5.5	—	±5.0	μA	$V_{IN} = 5.5 \text{ V} \text{ or GND}$
Output leak current	I _{OFF}	0	_	±5.0	μA	$V_{IN} / V_{OUT} = 5.5 V$
Off state output current	loz	2.7 to 5.5	—	±5.0	μA	$V_{IN} = V_{CC}$ or GND,
						V _{OUT} = 5.5 V or GND
Quiescent supply current	I _{CC}	2.7 to 3.6	—	±5.0	μA	$V_{IN} = 3.6$ V to 5.5 V
		2.7 to 5.5	—	5.0		$V_{IN} = V_{CC} \text{ or } GND$
	ΔI_{CC}	2.7 to 3.6	—	500	μA	V_{IN} = one input at (V _{CC} –0.6) V, other inputs at V _{CC} or GND



Switching Characteristics

			Ta = -40 to 85°C				From	То
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	1.8±0.15	1.0	—	9.8	ns	А	Y
	t _{PHL}	2.5±0.2	1.0	—	7.2			
		2.7	1.0	—	5.2			
		3.3±0.3	1.0	—	4.7			
		5.0±0.5	1.0	—	3.7			
Output enable time	t _{ZH}	1.8±0.15	1.0	—	10.0	ns	OE	Y
	t _{ZL}	2.5±0.2	1.0	—	8.3			
		2.7	1.0	—	6.3			
		3.3±0.3	1.0	—	5.7			
		5.0±0.5	1.0	—	4.7			
Output disable time	t _{HZ}	1.8±0.15	1.0	—	12.6	ns	OE	Y
	t _{LZ}	2.5±0.2	1.0	—	8.7			
		2.7	1.0	—	6.7			
		3.3±0.3	1.3	—	6.0			
		5.0±0.5	1.0	—	5.0			
Between output pins skew *	¹ t _{OSLH}	1.8±0.15	—	—	—	ns		
	t _{OSHL}	2.5±0.2	—	—	—			
		2.7	—	—	—			
		3.3±0.3	—	—	1.0			
		5.0±0.5	_	_	1.0]		
Input capacitance	CIN	3.3	—	4.0	—	pF		
Output capacitance	Co	3.3	_	7.0	_	pF		

Note: 1. This parameter is characterized but not tested.

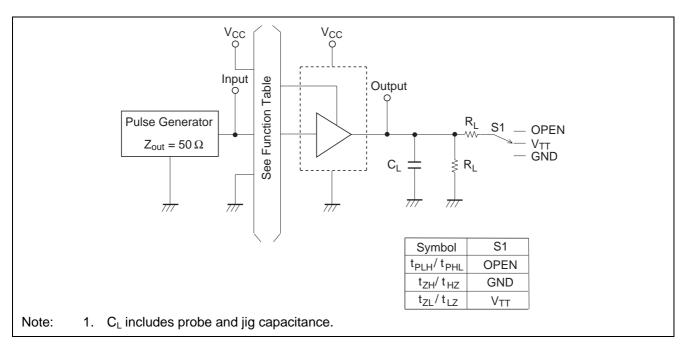
 $tos_{LH} = \mid t_{\mathsf{PLHm}} \text{-} t_{\mathsf{PLHn}} |, \ tos_{\mathsf{HL}} = \mid t_{\mathsf{PHLm}} \text{-} t_{\mathsf{PHLn}} |$

Operating Characteristics

 $Ta = 25^{\circ}C$

Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	CPD	1.8	_	21	—	pF	f = 10 MHz
capacitance		2.5	—	22	—		
		3.3	—	23	—		
		5.0	—	27	—		

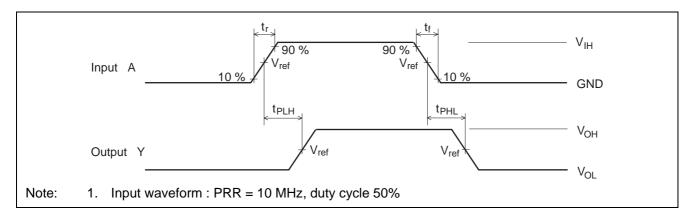
Test Circuit



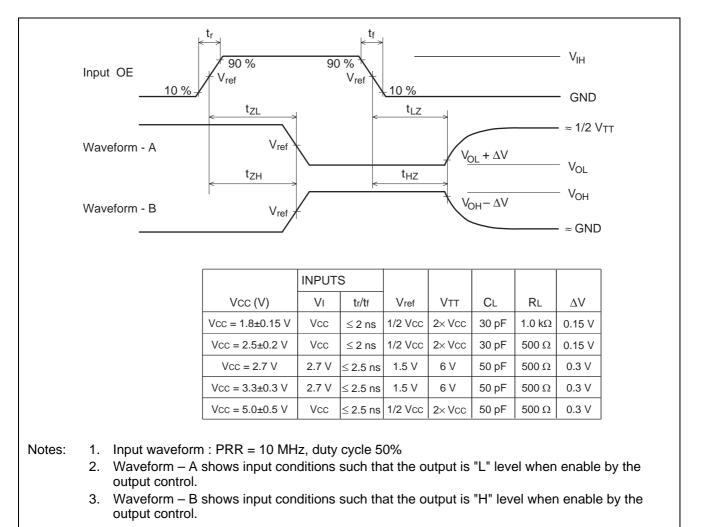


RD74LVC126B

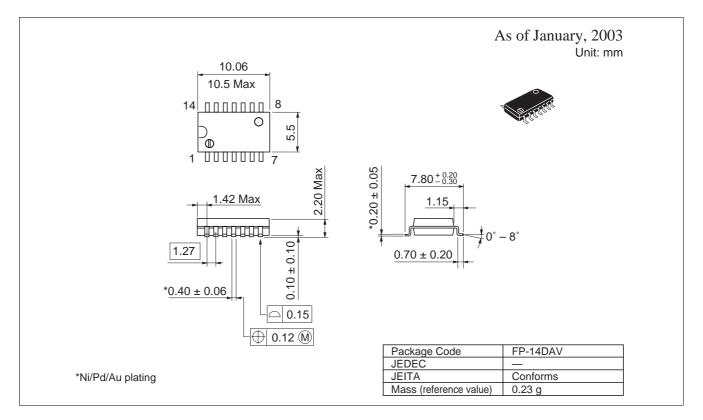
Waveforms - 1

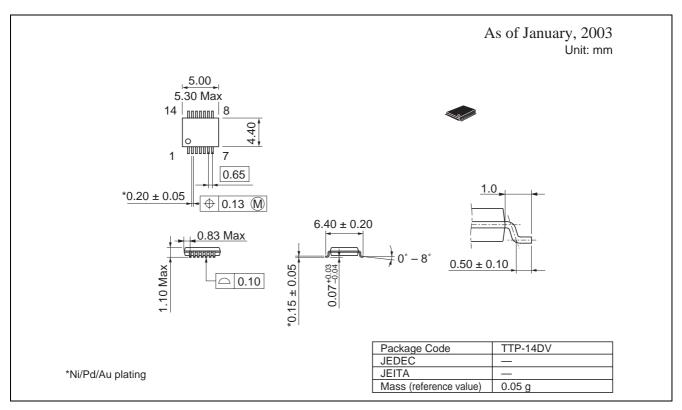


Waveforms – 2



Package Dimensions





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