READ4304G

High Drivability & High Slew Rate, Input Output Full Range CMOS Quad Operational Amplifier

V_{IO}≤±6mV, SR = 8V/μs , GBW=6MHz

Description

The READ4304G is input and output full range quad CMOS Operational Amplifier realizing high drivability and high slew rate. This IC can be used in minimum operating supply voltage from 2.5V, and in wide ambient temperature range from -40˚C to +105˚C.

Available in ultra-small 14 pins TSSOP package.

Features

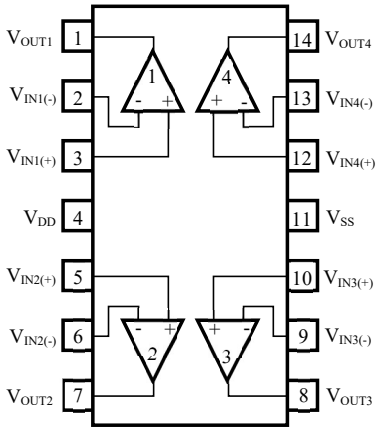
- Low voltage single supply operation — V_{DD} = 2.5V to 5.5V
- Low input offset voltage — V_{IO} ≤ ±6.0mV
- Low input bias current — I_B ≤ (1pA).
- Wide output voltage range — V_{OUT} : V_{SS}+0.1V to V_{DD}-0.1V(@Io=5mA)
- Supply current (per channel) — I_{DD} = 0.75mA Typ.
- High slew rate — SR = 8V/μs Typ.

( ) reference value of design

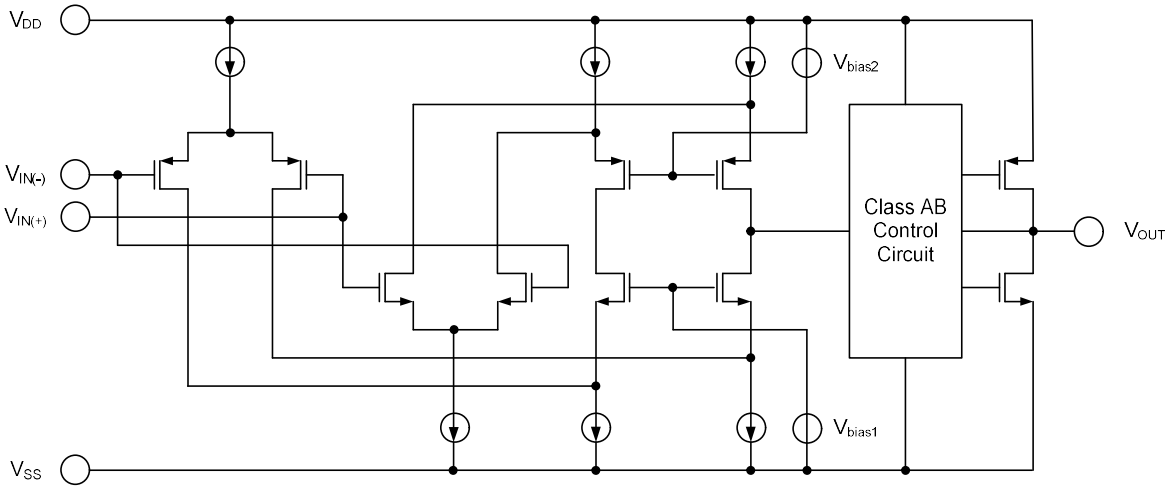
Product Line-up

Type name	Product type quality level	Package
READ4304GSP	High slew rate with Normal quality level	14 pins plastic TSSOP

Pin Arrangement



Equivalent Circuit (per one channel)



Absolute Maximum Ratings

<TA=25˚C>

Items	Symbol	Ratings	Unit
Supply voltage Note.1	V_{DD}	-0.3 to +6.5	V
Differential input voltage	V_{ID}	$-V_{DD}$ to $+V_{DD}$	V
Input voltage Note.2	V_I	-0.3 to V_{DD} +0.3	V
Maximum output current	I_O	20	mA
Power dissipation Note.3	P_T	550	mW
Junction temperature	T_j	+150	˚C
Operating temp. range	T_A	-40 to +105	˚C
Storage temp. range	T_{stg}	-55 to +150	˚C

Note 1. Please take note that reverse connection of a power supply may cause destruction.

2. Stresses above these ratings may cause permanent damage such as characteristics degradation or destruction. Please do not exceed voltage below of GND-0.3V as it is bottom limit. In addition, operation amplifier is operated as normal when input voltage for electrical characteristics is in common mode input voltage range.

3. The value is measured under mounted on a glass epoxy base board (size 100mm x 100mm, 1mm thickness, copper foiled surface base board area with 15% solid pattern).

Note that restrictions will be made to the following conditions for each product, and the derating ratio depending on the operating ambient temperature.

READ4304GSP: Derate at -7.0 mW/˚C when T_A > 71 ˚C

(Junction – ambient thermal resistance $R_{th(J-A)}$ = 144 ˚C/W)

Electrical Characteristics

<V_{DD}=5.0V, T_A= 25˚C>

Items	Symbol	MIN.	TYP.	MAX.	Unit	Test Condition
Supply voltage	V_{DD} − V_{SS}	2.5		5.5	V	
Input offset voltage	V_{IO}			±6.0	mV	
Input offset current	I_{IO}			(1)	pA	
Input bias current	I_B			(1)	pA	
Output high voltage	V_{OH}	V_{DD}-0.2			V	I_L = 10mA
Output low voltage	V_{OL}			V_{SS}+0.2	V	I_L = 10mA
Voltage gain	A_V	60	90		dB	R_L≥100kΩ
Channel supply current	I_{DD}/ch		0.75	1.5	mA	R_L=∞, I_O=0
Common mode rejection ratio	CMRR	60	80		dB	
Supply voltage rejection ratio	SVRR	60	80		dB	
Common mode input voltage range	V_{ICM}	V_{SS}		V_{DD}	V	
Gain bandwidth product	GBW		6		MHz	C_L=20pF
Slew rate	SR		8		V/us	C_L=20pF

( ) reference value of design

Notes

Output terminal: The over-current protection feature is not built in the output terminal of this product.

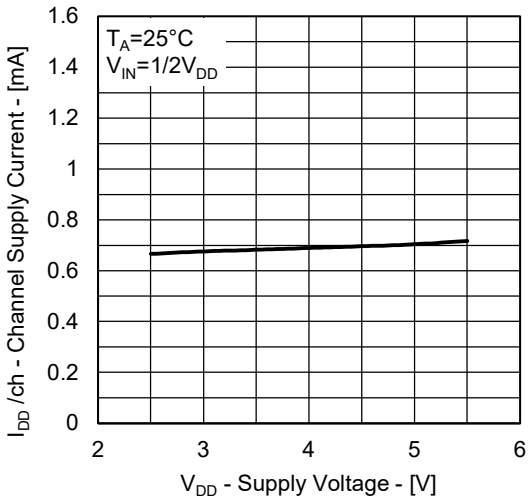
Therefore, please insert resistance to output port.

Input offset voltage : The amplifier circuit of the first block of operational amplifier.

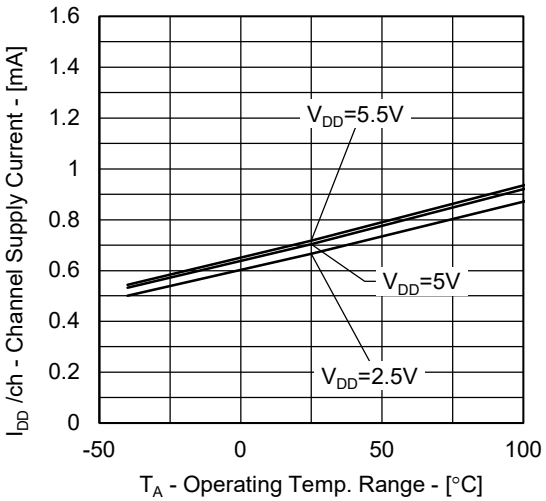
A circuit suitable for operation near GND, and a circuit suitable for operation near +power supply. In case of input voltage of overlap point output port has a minute voltage shift or distortion.

Electrical Characteristics

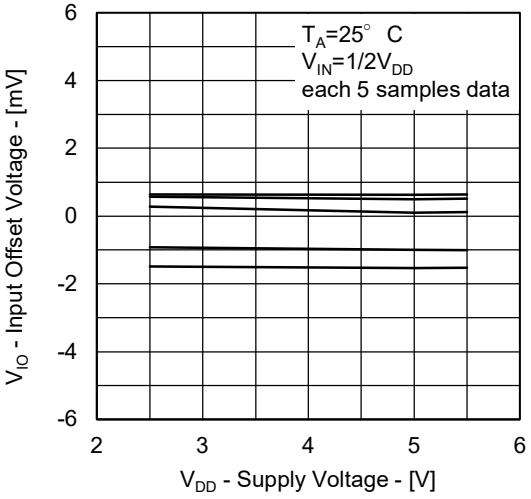
I_{DD} /ch vs. V_{DD}



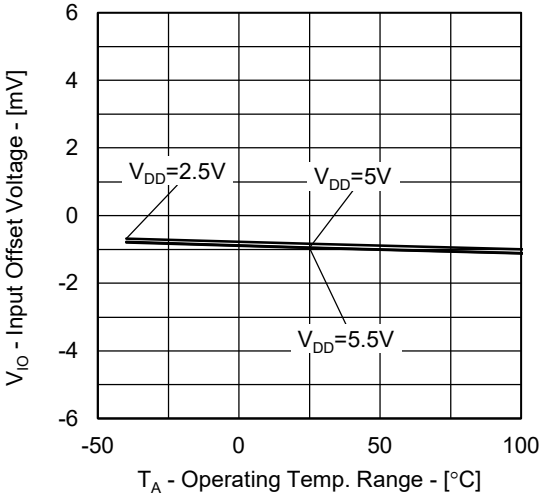
I_{DD} /ch vs. T_A



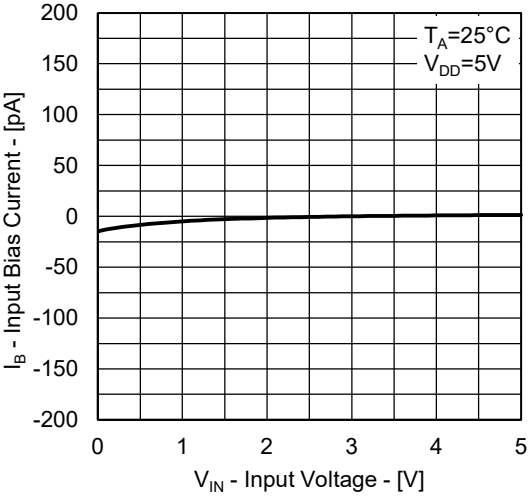
V_{IO} vs. V_{DD}



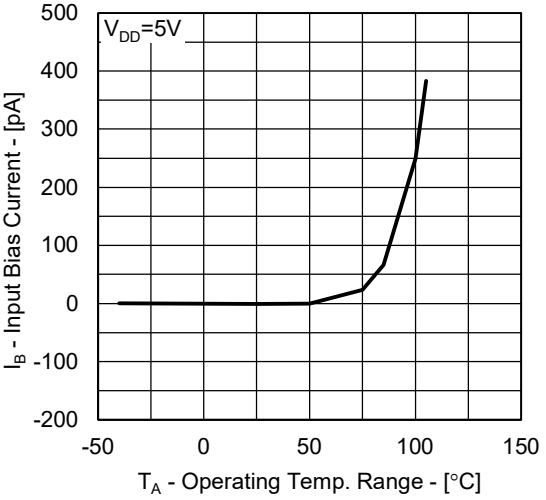
V_{IO} vs. T_A

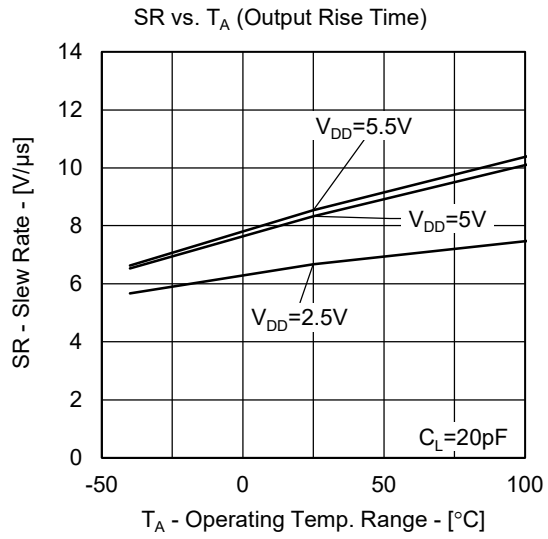


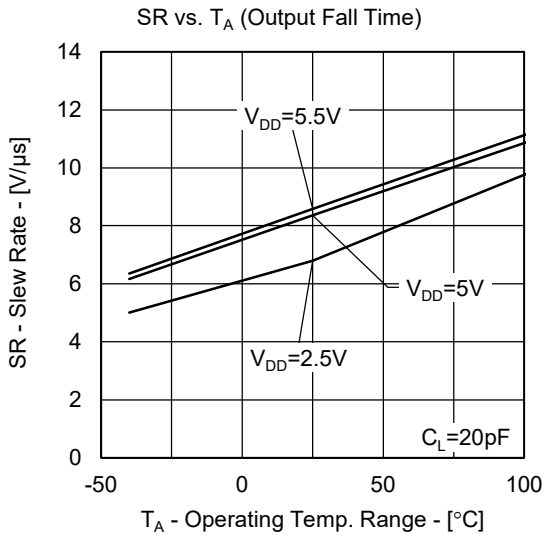
I_B vs. V_{IN}



I_B vs. T_A




SR vs. T_A (Output Rise Time)
SR - Slew Rate - [V/µs]
14
12
10
8
6
4
2
0
-50
0
50
100
T_A - Operating Temp. Range - [°C]
V_{DD}=5.5V
V_{DD}=5V
V_{DD}=2.5V
C_L=20pF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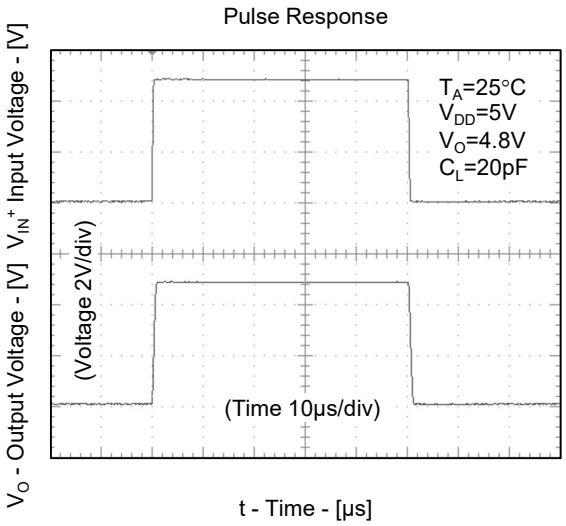
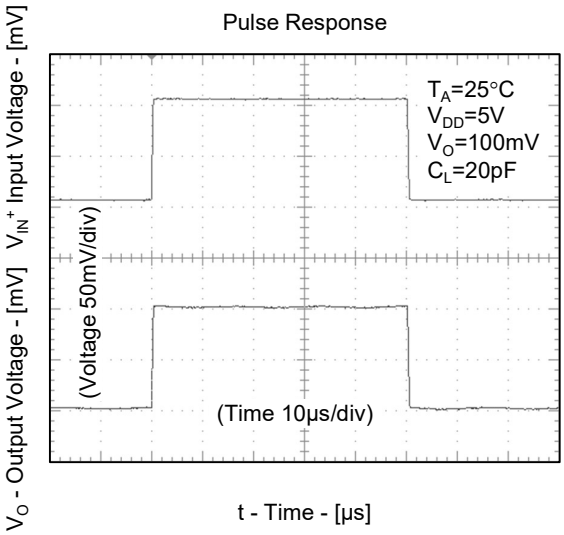
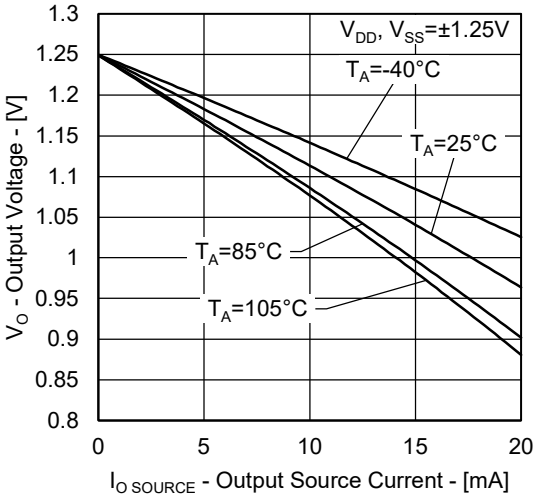
V_{DD}=5V
V_{DD}=2.5V
C_L=20pF

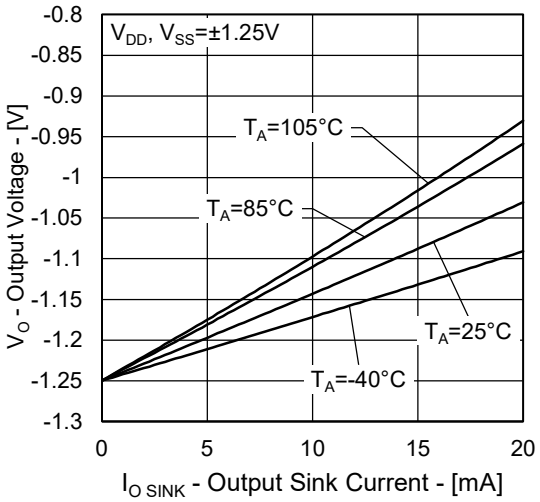

Pulse Response
V_O - Output Voltage - [V] V_{IN}^+ Input Voltage - [V]
T_A=25°C
V_{DD}=5V
V_O=4.8V
C_L=20pF
(Voltage 2V/div)
(Time 10µs/div)
t - Time - [µs]


Pulse Response
V_O - Output Voltage - [mV] V_{IN}^+ Input Voltage - [mV]
T_A=25°C
V_{DD}=5V
V_O=100mV
C_L=20pF
(Voltage 50mV/div)
(Time 10µs/div)
t - Time - [µs]

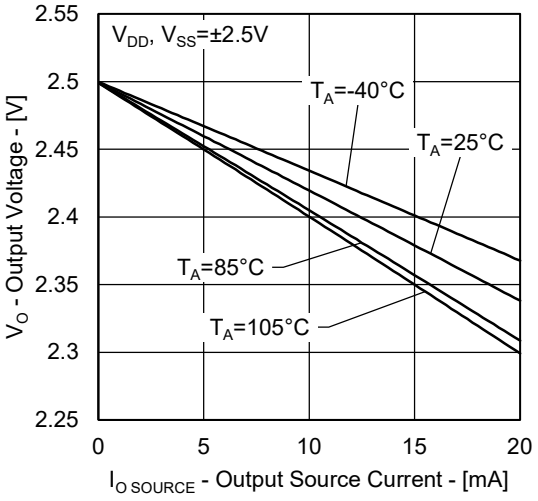
V_O vs. $I_{O\ SOURCE}$



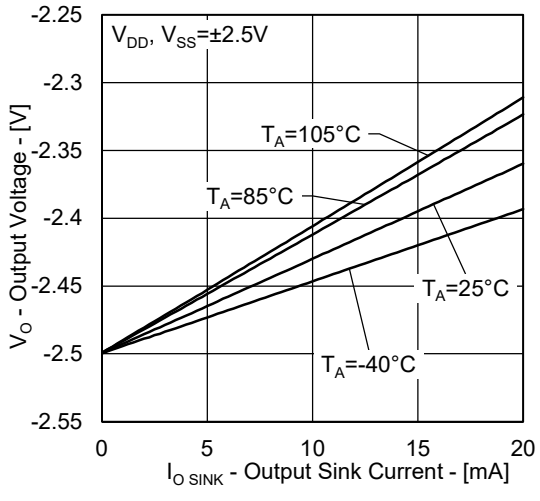
V_O vs. $I_{O\ SINK}$



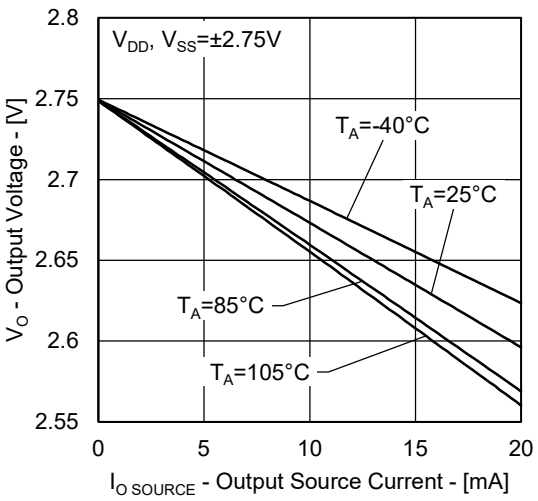
V_O vs. $I_{O\ SOURCE}$



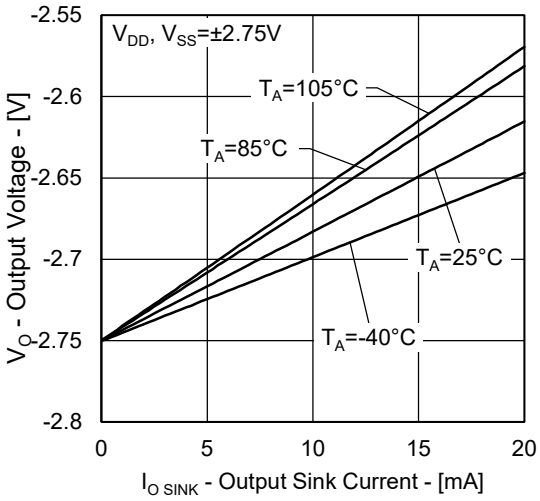
V_O vs. $I_{O\ SINK}$



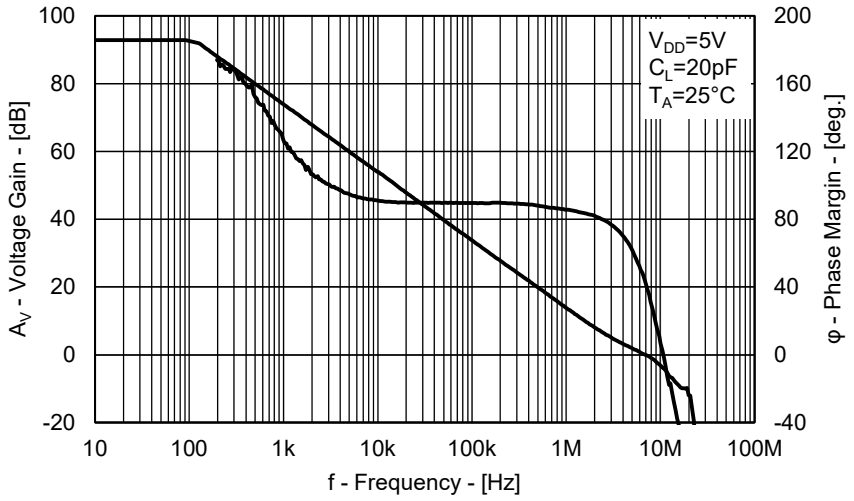
V_O vs. $I_{O\ SOURCE}$



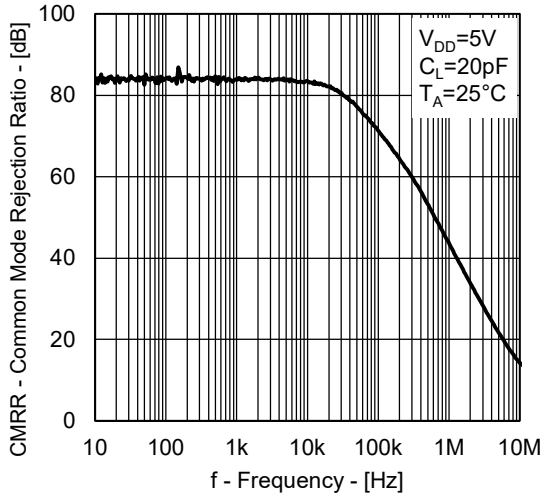
V_O vs. $I_{O\ SINK}$



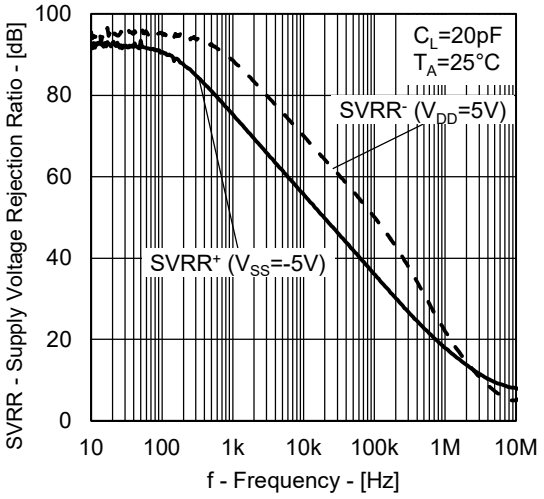
A_V, φ vs. f



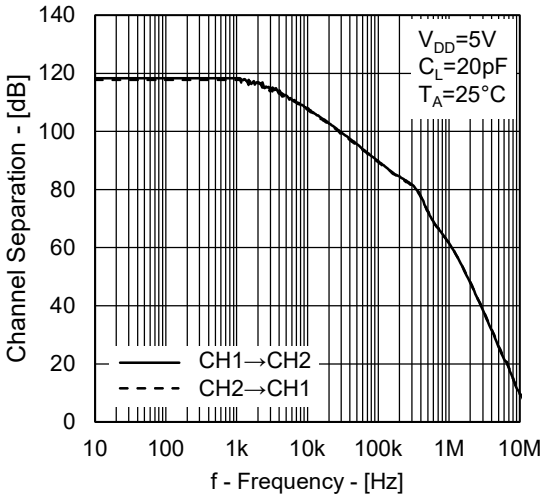
CMRR vs. f



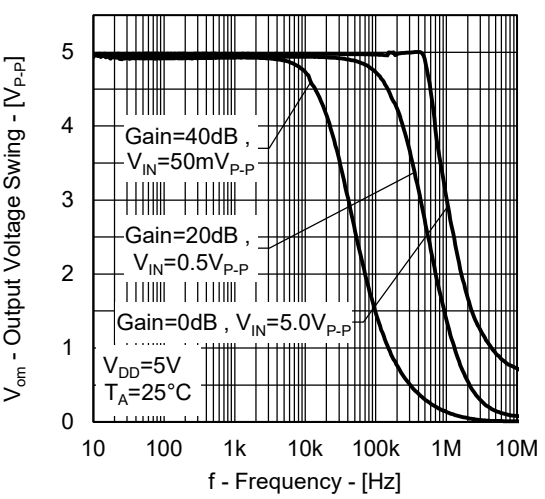
SVRR vs. f



Channel Separation vs. f



V_{om} vs. f

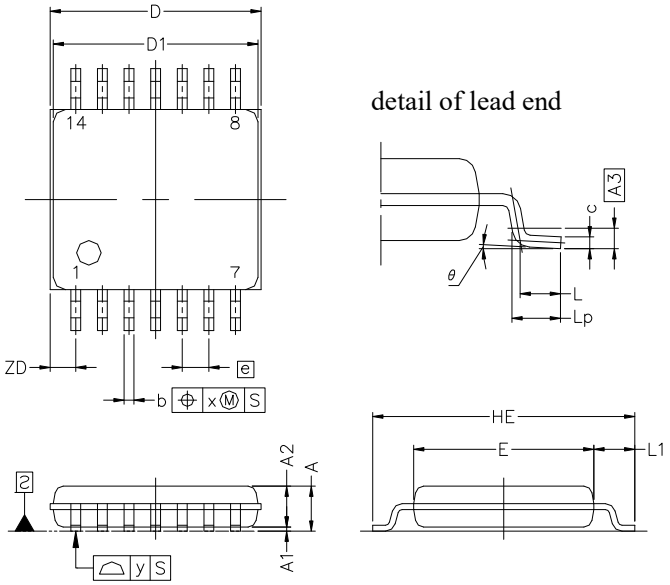


Package Dimensions

14-PIN PLASTIC TSSOP

JEITA Package Code	RENESAS Code	Previous Code	MASS(TYP.)[g]
P-TSSOP14-0225-0.65	PTSP0014JB-A	P14GR-65-9LG-1	—

Unit:mm



NOTE

Each lead centerline is located within 0.10 mm of its true position at maximum material condition.

ITEM	MILLIMETERS
D	5.15 ±0.15
D1	5.00 ±0.10
E	4.40 ±0.10
HE	6.40 ±0.20
A	1.20 MAX.
A1	0.10 ±0.05
A2	1.00 ±0.05
A3	0.25
b	0.24 +0.06 -0.05
c	0.145 ±0.055
L	0.5
Lp	0.60 ±0.15
L1	1.00 ±0.20
θ	3° +5° -3°
e	0.65
x	0.10
y	0.10
ZD	0.625

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