

**RZ/G3S Easy Download Guide**

Step-1	Information for engineers	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
1-1	RZ/G Series 32/64-bit MPU (Wiki)	Link summary	Introduction,Boards,Software and Documentation	<a href="#">Link</a>	-	Non limitation
1-2	Contact Support	QA Ticket	Technical Q&A	<a href="#">Link</a>	-	Non limitation

Step-2	Introduction	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
2-1	Hardware Information	Document	RZ/G3S Datasheet	<a href="#">Link</a>	-	Non limitation
2-2	Introduction for products	Document	RZ Family Microprocessors	<a href="#">Link</a>	-	Non limitation
		Document	Renesas RZ/G3S Group	<a href="#">Link</a>	-	Non limitation
		Video	60's Video	<a href="#">Link</a>	-	Non limitation
		Document	RZ Family Product Part Number Guide	<a href="#">Link</a>	-	Non limitation

Step-3	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
3-1	User's Manual	Document	User's Manual: Hardware	<a href="#">Link</a>	-	Non limitation
		Document	Additional document for User's Manual(SDHI)(Requires LoA)	Please contact Distl/Sales	LoA*1	Please contact Distl/Sales
3-2	How to start up the board	Document	SMARC Module Board User's Manual	<a href="#">Link</a>	-	Non limitation
		Document	SMARC Carrier Board User's Manual	<a href="#">Link</a>	-	Non limitation
		Document	Evaluation Board Kit(EVK) Quick Start Guide	<a href="#">Link</a>	-	Non limitation
		Document	RZ/G Series Performance Guide	<a href="#">Link</a>	-	Non limitation
3-3	Board information / how to get	Info/purchase	Evaluation Kit WEB Page	<a href="#">Link</a>	-	Non limitation
3-4	Evaluation environment start-up method Verified Linux Package (VLP)	Document	RZ/G Verified Linux Package V3.0.6-update3 Release Note	<a href="#">Link</a>	-	Non limitation
		Document	Linux Start-up Guide	<a href="#">Link</a>	-	Non limitation
		Document	RZ/G3S Group BSP Manual Set	<a href="#">Link</a>	EULA*2	MyRenesas
		Document	RZ/G Proprietary SW Package Handling	Please contact Distl/Sales	-	Please contact Distl/Sales
3-5	Evaluation environment start-up method Multi-OS Package	Document	Release Note for Multi-OS Package v2.0.2	<a href="#">Link</a>	-	Non limitation
		Document	Getting Started with Flexible Software Package	<a href="#">Link</a>	-	Non limitation
		Document	Setting GPIO with Flexible Software Package	<a href="#">Link</a>	-	Non limitation
		Document	FSP Documentation	<a href="#">Link</a>	-	Non limitation
		Document	Release Note for Flexible Software Package	<a href="#">Link</a>	-	Non limitation
3-6	RZ Security Solution Overview	Document	RZ Security Solution Overview	<a href="#">Link</a>	-	Non limitation
3-7	How to get various software	Software	RZ/G3S Pre-built Images	<a href="#">Link</a>	-	Non limitation
		Web Link	RZ/G Verified Linux Package [5.10-CIP]	<a href="#">Link</a>	-	Non limitation
		Web Link	RZ/G3S Group Multi-OS Package	<a href="#">Link</a>	EULA*2	MyRenesas
		Web Link	Security Package	<a href="#">Link</a>	SLA*3	Secure Access
		Tool	Smart Configurator	<a href="#">Link</a>	-	Non limitation
		Document	Smart Configurator for RZ User's Guide	<a href="#">Link</a>	-	Non limitation

Step-4	Custom board development	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
4-1	Documents and Simulation Models required for custom board design and development	Document	PCB Design and Verification Flow (for DDR-IF)	Please contact Distl/Sales	-	Please contact Distl/Sales
		Document	Board circuit design checklist	<a href="#">Link</a>	-	Non limitation
		Document	PCB Design Guidelines for MIPI-CSI, MIPI-DSI, USB2.0 and PCI Express Gen2	<a href="#">Link</a>	-	Non limitation
		Document,Tool	PCB Design Guideline for LDDR4, DDR Config Generation Tool	<a href="#">Link</a>	TCU*5	Secure Access
		Document,Model	PCB verification guide for Core VDD, PDN model	<a href="#">Link</a>	-	Non limitation
		Document,Model	PCB verification guide for LDDR4, PKG model	<a href="#">Link</a>	TCU*5	Secure Access
		Design Data	SMARC Module Board Design Data(Schematics,Layout,BOM)	<a href="#">Link</a>	Disclaimer*4	MyRenesas
		Design Data	Carrier Board Design Data(Schematics,Layout,BOM)	<a href="#">Link</a>	Disclaimer*4	MyRenesas
		Model	IBIS	<a href="#">Link</a>	TCU*5	Secure Access
		File	BSDL	<a href="#">Link</a>	Disclaimer*4	MyRenesas
		Document	Guide to Using RGMII in Making an Ethernet-IF Connection	<a href="#">Link</a>	-	Non limitation
		Document	Power Consumption Measurement	<a href="#">Link</a>	-	Non limitation
		Document	RZ Series Current Measurement Program(Application Note)	<a href="#">Link</a>	-	MyRenesas
		Tool	RZ Series Current Measurement Program(Tool)	<a href="#">Link</a>	Disclaimer*4	MyRenesas
		Document	<a href="#">Reference power consumption guide</a>	<a href="#">Link</a>	-	Non limitation
		Document	Thermal Management Guideline	<a href="#">Link</a>	-	Non limitation
		Document	Mechanical Handling Guideline	<a href="#">Link</a>	-	Non limitation
		Document	Mount Conditions Reflow Soldering Conditions	<a href="#">Link</a>	-	Non limitation
		Document	Mount Conditions Baking Conditions	<a href="#">Link</a>	-	Non limitation
		Document	Semiconductor Package Mount Manual	<a href="#">Link</a>	-	Non limitation
		Document	Lifetime Guideline	<a href="#">Link</a>	-	Non limitation
		Device Info	Power Management IC	<a href="#">Link</a>	-	Non limitation
		Device Info	Clock Generator IC	<a href="#">Link</a>	-	Non limitation
		Document	R9A08G045XXGBG Reliability Report	TBD	-	Non limitation
		Document	Power calculation sheet and guide	Please contact Distl/Sales	-	Please contact Distl/Sales

\*1: Letter of Acknowledgement  
 \*2: End User License Agreement  
 \*3: Software License Agreement  
 \*4: Disclaimer Letter  
 \*5: Terms and Conditions of Use

[Additional Information] Please refer to the software content archive list if necessary.

【補足情報 必要に応じてソフトウェアコンテンツ アーカイブ一覧をご参照ください。

RZ/G Software package archive list (2H2024)

5.10-CIP

Product	Libraly Nick Name	Version	Package Name
RZ/G3S	Board Support Package(BSP)	v1.0	<a href="https://www.renesas.com/en/document/swo/rzg3s-board-support-package-rtk0ef0045z95001zi-v100zip?r=25462026">https://www.renesas.com/en/document/swo/rzg3s-board-support-package-rtk0ef0045z95001zi-v100zip?r=25462026</a>