

			RZ/V2H
Step	Custom board development	Contents Type	Contents Name
	LPDDR4/4X Controller Configuration	Compressed file --- Document --- Document --- Tool	[NDA]DDRTOP Application Note Application Note LPDDR4/4X Controller Configuration Guide Setting tool Link
	PCB Design Guide	Document	[NDA]PCB Design Guidelines Link
	Thermal Design Guide	Document	Thermal Design Guide Link
	Lifetime estimation guide	Document	[NDA]Lifetime Guideline Link
	LSI Design Model (IBIS)	Model	[NDA]LSI IBIS Link
	Interface Design Model (Spara, IBIS)	Model	[NDA]Interface (Spara, IBIS) Link
	Reference Desing Data	Compressed file --- Desing Data --- Desing Data --- Desing Data --- Desing Data --- Desing Data	CPU Board Design Data --- Schamtics --- BOM List --- Circuit Data (.DSN) --- Layout Data (.brd) --- Gerber Data Link
	Reference Desing Data	Compressed file --- Desing Data --- Desing Data --- Desing Data --- Desing Data --- Desing Data	EXP Board Design Data --- Schamtics --- BOM List --- Circuit Data (.DSN) --- Layout Data (.brd) --- Gerber Data Link
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Step	Information for engineers	Contents Type	Contents Name
	RZ/V Series 64-bit MPU (Renesas info)	Link summary	Introduction,Boards,Software and Documentation Link
	FAQ	Link summary	Knowledgebase / FAQ Link
	Contact Support	QA Ticket	Technical Q&A Link