To our customers,

Old Company Name in Catalogs and Other Documents

   On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

RENESAS

REJ19B0777-0100

Mask ROM number	

RENESAS SINGLE-CHIP 16-BIT
MICROCOMPUTER M306NAMCT-XXXFP
MASK ROM CONFIRMATION FORM

Receipt	Date :	
	Section head signature	Supervisor signature

Note : Please complete all items marked ❀ .

❀ Customer	Company name	TEL ( )	Issuance signature	Submitted by	Supervisor
	Date issued	Date :			

❀ 1. Confirmation

Three EPROMs are required for each pattern if this order is performed by EPROMs.

One floppy disk is required for each pattern if this order is performed by a floppy disk.
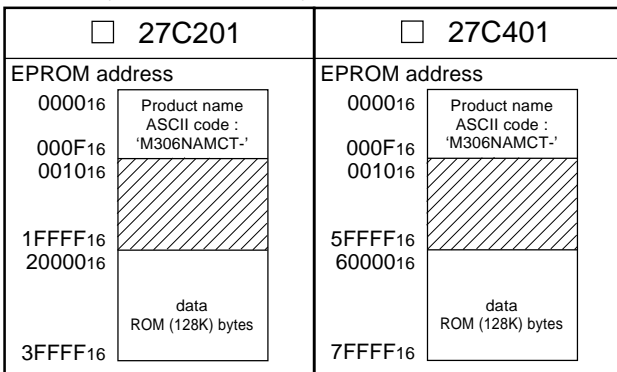
☐ Ordering by EPROMs

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM ☐☐☐☐ (hexadecimal notation)

EPROM type (indicate the type used)

☐ 27C201	☐ 27C401
EPROM address	EPROM address
0000_{16} — Product name ASCII code : 'M306NAMCT-'	0000_{16} — Product name ASCII code : 'M306NAMCT-'
$000F_{16}$ 0010_{16}	$000F_{16}$ 0010_{16}
$1FFFF_{16}$ 20000_{16}	$5FFFF_{16}$ 60000_{16}
data ROM (128K) bytes	data ROM (128K) bytes
$3FFFF_{16}$	$7FFFF_{16}$

(1) Set the data in the unused area (the shaded area of the diagram) to "FF_{16}".

(2) The ASCII codes of the product name "M306NAMCT–" must be entered in addresses 0000_{16} to $000F_{16}$. And set the data "FF_{16}" in addresses $000A_{16}$ to $000F_{16}$.

The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address		Address	
0000_{16}	'M' = $4D_{16}$	0008_{16}	'T' = 54_{16}
0001_{16}	'3' = 33_{16}	0009_{16}	'–' = $2D_{16}$
0002_{16}	'0' = 30_{16}	$000A_{16}$	FF_{16}
0003_{16}	'6' = 36_{16}	$000B_{16}$	FF_{16}
0004_{16}	'N' = $4E_{16}$	$000C_{16}$	FF_{16}
0005_{16}	'A' = 41_{16}	$000D_{16}$	FF_{16}
0006_{16}	'M' = $4D_{16}$	$000E_{16}$	FF_{16}
0007_{16}	'C' = 43_{16}	$000F_{16}$	FF_{16}

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We recommend the use of the following pseudo-command to set the start address of the assembler source program.

EPROM type	27C201	27C401
The pseudo-command	△.SECTION△ASCIICODE,ROMDATA △.ORG△0C0000H △.BYTE△' M306NAMCT-'	△.SECTION△ASCIICODE,ROMDATA △.ORG△080000H △.BYTE△' M306NAMCT-'

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

☐ Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file inthe submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and IBM format. And the number of mask files must be 1 in a floppy disk.

File code : ☐☐☐☐☐☐☐☐ (hex)

Mask file name : ☐☐☐☐☐☐☐☐ .MSK (alpha-numeric 8-digit)

✴ 2. Mark specification
Mark specification must be submitted using the correct form for the package being ordered. Fill out the appropriate mark specification form (100P6S) and attach it to the mask ROM confirmation form.

✴ 3.Usage conditions
For our reference to test the part, please fill out the following questions about the usage of the products you ordered.

(1) Type of X_{IN}-X_{OUT} oscillation circuit

☐ Ceramic resonator   ☐ Quartz-crystal oscillator

☐ External clock input   ☐ Other (   )

Frequency

$f(X_{IN})$ = ☐ MHz

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(2) Type of XCIN-XCOUT oscillation circuit

☐ Ceramic resonator ☐ Quartz-crystal oscillator ☐ Not Used

☐ External clock input ☐ Other ( )

Frequency

f(XCIN) = ☐ kHz

(3) Operation modes

☐ Single-chip mode ☐ Memory expansion mode

☐ Microprocessor mode

(4) Operating ambient temperature

☐ -10˚C~75˚C ☐ -20˚C~75˚C ☐ -40˚C~75˚C

☐ -10˚C~85˚C ☐ -20˚C~85˚C ☐ -40˚C~85˚C

(5) Operating supply voltage

☐ 4.2V~4.7V ☐ 4.7V~5.2V ☐ 5.2V~5.5V

(6) CAN controller setting

CAN0 ☐ Not used ☐ Used

Transmission rate ☐ bps

Synchronization Jump Width(SJW) ☐ Tq

Propagation Time Segment(PTS) ☐ Tq

Phase Buffer Segment1(PBS1) ☐ Tq

Phase Buffer Segment2(PBS2) ☐ Tq

CAN1 ☐ Not used ☐ Used

Transmission rate ☐ bps

Synchronization Jump Width(SJW) ☐ Tq

Propagation Time Segment(PTS) ☐ Tq

Phase Buffer Segment1(PBS1) ☐ Tq

Phase Buffer Segment2(PBS2) ☐ Tq

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(7) I^{2}C (Inter IC) bus function

☐ Not use   ☐ Use

(8)IE (Inter Equipment) bus function

☐ Not use   ☐ Use

Thank you for your cooperation.

※4. Comments (Indicate none if there is no specific)

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