

iW7039

32-Channel Internal Current Sink LED Backlighting Driver

The **iW7039** is a 32-channel, internal current sink, high precision, LED backlighting driver for high dynamic range (HDR) LCD TVs, monitors and notebook displays. The device combines adaptive DC/DC feedback control with Dialog's **BroadLED™** digital adaptive switch mode technology to enable best system efficiency and thermal performance.

The iW7039 integrates the ability to use either analog or digital dimming methods in one device. With a 10-bit/11-bit global or individual channel analog dimming and 12-bit PWM digital dimming, the device improved contrast ratio in HDR displays. The current sinks can drive up to 66mA per channel and have very tightly controlled current accuracy.

A full array or protection circuits are integrated, including built-in LED open/short detection, over temperature protection and protection during both start-up and normal operation.

Features

- 32 channel LED driver, pin map compatible with 16 channel iW7038
 - Internal Current sink
- Support 12V or 5V Single power supply
- 65V max. LED pin rating
- 66mA x 100% duty x 32ch output current, support channel grouping.
- 12-bit PWM dimming.
- 10-bit global and independent analog dimming (I-dimming), optional 66mA/44mA/22mA full range.
- Head mode with optional off time control.
- Optional 350/650/950/1600ns PWM output slew rate.
- Optional 0.5ms to 10ms (0.5ms per step), LED current rising and falling slop control in analog dimming.
- 44Hz to 32KHz V_{SYNC} input, 44Hz to 64KHz PWM output.
- 9-bit adaptive external DC/DC feedback control
- 3 PWM-generator clock options
 - External H_{SYNC} (up to 26MHz)
 - APLL (15MHz to 26.6MHz)
 - DPLL (10MHz), support gaming monitor VRR (Variable Refresh Rate)
- 26.6MHz Max SPI with Daisy chain
- Programmable LED open/short detection threshold and protection
- Programmable over temperature protection
- Optional Key registers write protection with password
- Optional data packet CRC/CHECKSUM for noisy SPI
- Fault interrupt output (open drain, need external pull up)
- -20 to +85°C operating ambient temperature range
- 145°C max. operating junction temperature
- Package: 7mm x 7mm QFN48-EP package

Applications

- TV
- Monitor
- Notebook

1. Pin Information

1.1 Pin Assignments

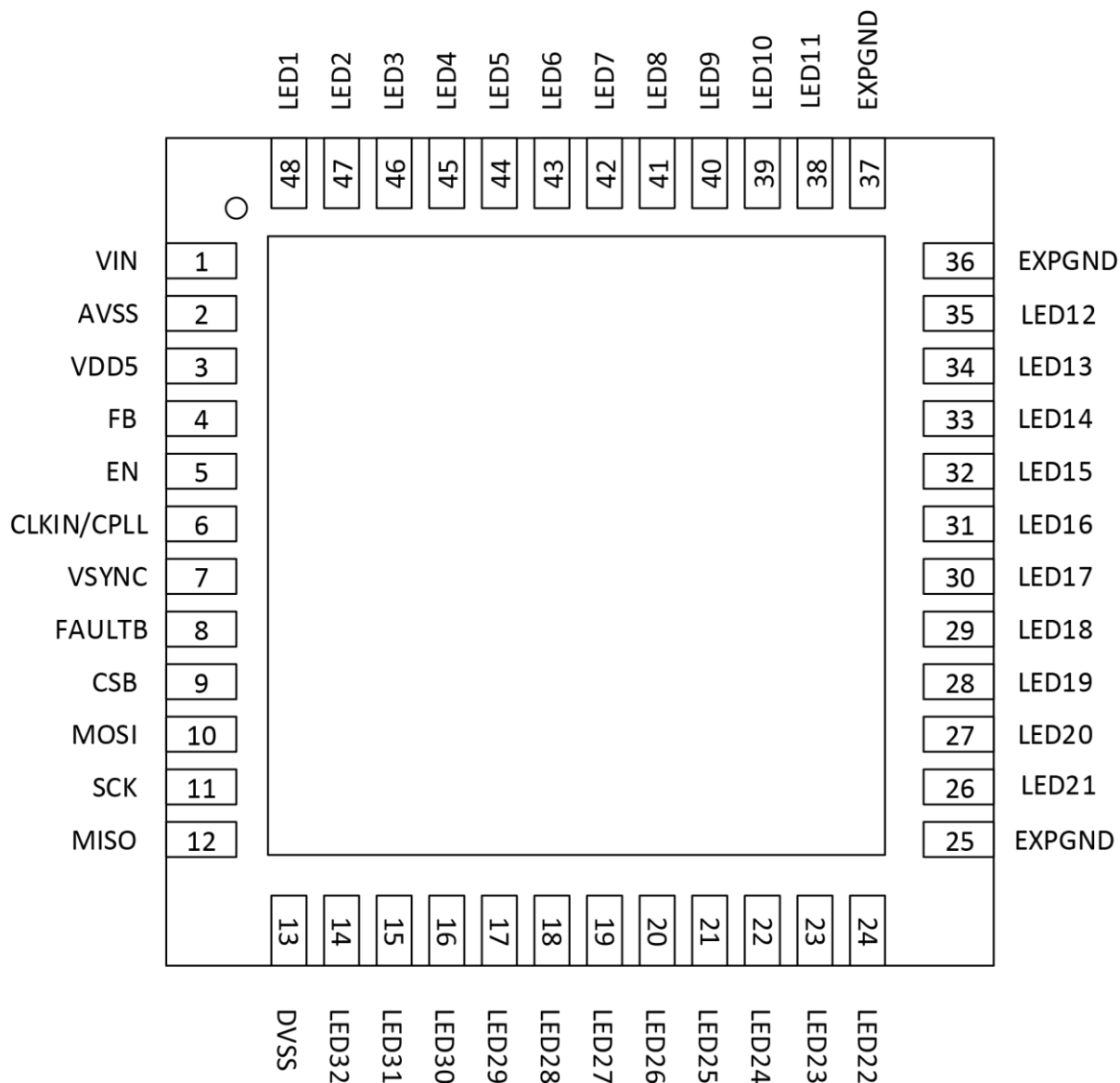


Figure 1. Pin Assignments – Top View

1.2 Pin Descriptions

Pin Number	Pin Name	Type	Description	If not use
1	VIN	AI	Power supply. Connect 4.7 μ F capacitor to GND.	
2	AVSS	GND	GND.	
3	VDD5	AIO	LDO output, connect 4.7 μ F capacitor to GND.	
4	FB	AO	Power supply feedback pin.	Leave open.
5	EN	DI	Chip enable pin.	
6	CLKIN/CPLL	DI/AI	H _{SYNC} signal input or connect PLL RC compensation circuit.	Leave open.
7	VSYNC	DI_PD	Frame synchronize signal input with internal programable filter, both high and low should be hold longer than 10 μ s.	
8	FAULTB	DO_OD	Fault status output, connect a 100K resister to external pull up.	Leave open.
9	CSB	DI_PU	SPI interface chip select.	
10	MOSI	DI_PD	SPI interface data input.	
11	SCK	DI_PD	SPI interface clock.	
12	MISO	DO	SPI interface data output.	Leave open.
13	DVSS	GND	GND.	
14	LED32	AI	Connect to the cathode of the LED string.	Leave open.
15	LED31	AI	Connect to the cathode of the LED string.	Leave open.
16	LED30	AI	Connect to the cathode of the LED string.	Leave open.
17	LED29	AI	Connect to the cathode of the LED string.	Leave open.
18	LED28	AI	Connect to the cathode of the LED string.	Leave open.
19	LED27	AI	Connect to the cathode of the LED string.	Leave open.
20	LED26	AI	Connect to the cathode of the LED string.	Leave open.
21	LED25	AI	Connect to the cathode of the LED string.	Leave open.
22	LED24	AI	Connect to the cathode of the LED string.	Leave open.
23	LED23	AI	Connect to the cathode of the LED string.	Leave open.
24	LED22	AI	Connect to the cathode of the LED string.	Leave open.
25	EXPGND	GND	GND.	
26	LED21	AI	Connect to the cathode of the LED string.	Leave open.
27	LED20	AI	Connect to the cathode of the LED string.	Leave open.
28	LED19	AI	Connect to the cathode of the LED string.	Leave open.

iW7039 Product Summary

Pin Number	Pin Name	Type	Description	If not use
29	LED18	AI	Connect to the cathode of the LED string.	Leave open.
30	LED17	AI	Connect to the cathode of the LED string.	Leave open.
31	LED16	AI	Connect to the cathode of the LED string.	Leave open.
32	LED15	AI	Connect to the cathode of the LED string.	Leave open.
33	LED14	AI	Connect to the cathode of the LED string.	Leave open.
34	LED13	AI	Connect to the cathode of the LED string.	Leave open.
35	LED12	AI	Connect to the cathode of the LED string.	Leave open.
36	EXPGND	GND	GND.	
37	EXPGND	GND	GND.	
38	LED11	AI	Connect to the cathode of the LED string.	Leave open.
39	LED10	AI	Connect to the cathode of the LED string.	Leave open.
40	LED9	AI	Connect to the cathode of the LED string.	Leave open.
41	LED8	AI	Connect to the cathode of the LED string.	Leave open.
42	LED7	AI	Connect to the cathode of the LED string.	Leave open.
43	LED6	AI	Connect to the cathode of the LED string.	Leave open.
44	LED5	AI	Connect to the cathode of the LED string.	Leave open.
45	LED4	AI	Connect to the cathode of the LED string.	Leave open.
46	LED3	AI	Connect to the cathode of the LED string.	Leave open.
47	LED2	AI	Connect to the cathode of the LED string.	Leave open.
48	LED1	AI	Connect to the cathode of the LED string.	Leave open.

A_I/O: Analog pin

P: Power pin

DO: Digital Output

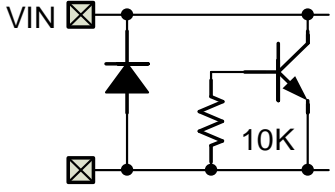
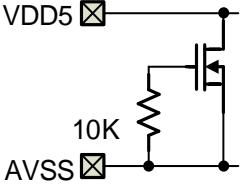
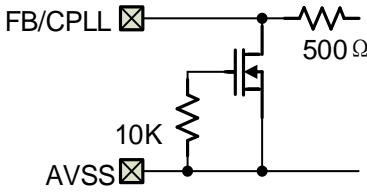
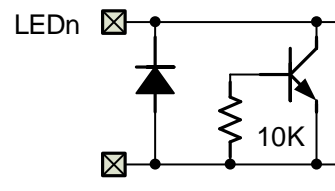
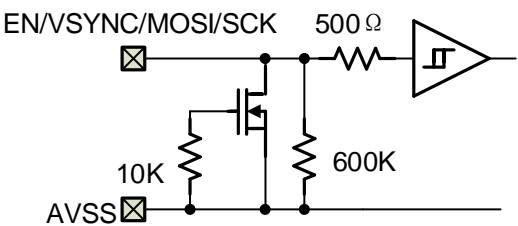
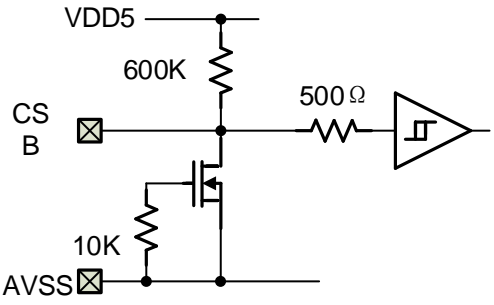
DO_OD: Digital Output Open Drain

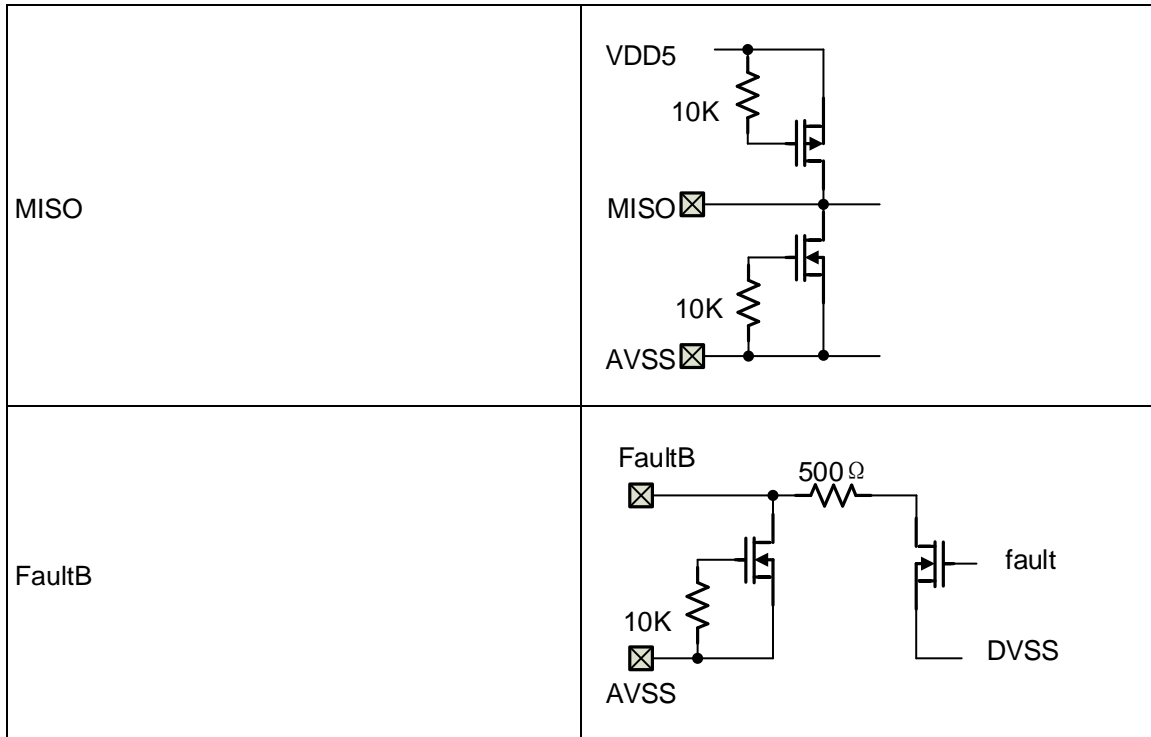
DI: Digital Input

DI_PU: Digital Input with Pull Up resistor

DI_PD: Digital Input with Pull Down resistor

1.3 Pin Equivalent Circuits

<p>VIN</p>	
<p>VDD5</p>	
<p>FB/CPLL</p>	
<p>LEDn</p>	
<p>EN VSYNC MOSI SCK</p>	
<p>CS B</p>	



2. Specifications

2.1 Absolute Maximum Ratings

Caution: Do not operate at or near the maximum ratings listed for extended periods of time. Exposure to such conditions can adversely impact product reliability and result in failures not covered by warranty.

Parameter	Symbol	Minimum	Maximum	Unit
VIN supply voltage	VIN	-0.3	20	V
5V LDO output or input (VIN short to VDD5)	VDD5	-0.3	7	V
LEDn pin voltage	LEDn	-0.3	65	V
Logic I/O	CSB, SCK, MISO, MOSI, VSYNC, CLKIN/CPLL, EN	-0.3	7	V
Human Body Model (Tested per JS-001-2017)	V_{ESDHBM}	-2000	+2000	V

2.2 Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Unit
VIN supply voltage	VIN	9	12	16	V
VDD5 as power supply, VIN pin short to VDD5 pin	VDD5	4.5	-	5.5	V
LEDn pin operating voltage rating or maximum LED string voltage	LEDn	-	-	65	V
VIN supply voltage	Operating Temperature	-20	-	85	°C

2.3 Thermal Specifications

Parameter	Package	Symbol	Condition	Typical Value	Unit
Thermal Resistance	48-QFN, 7mm x 7mm	θ_{JA}	Junction to ambient	TBD	°C/W
		θ_{JC}	Junction to case	TBD	

3. Package Outline Drawings

The package outline drawings are located at the end of this document and are accessible from the Renesas website. The package information is the most current data available and is subject to change without revision of this document.

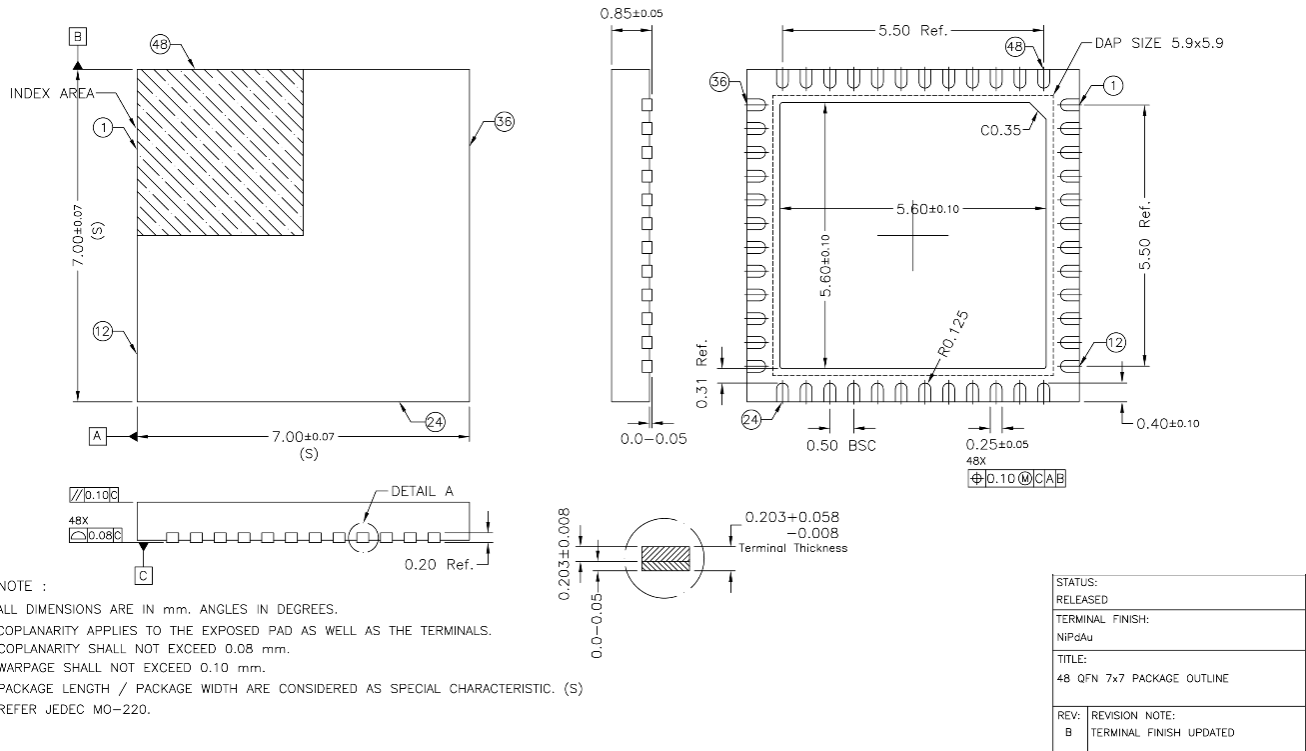


Figure 2: 48-Lead 7mm x 7mm QFN Package Outline Drawing

4. Ordering Information

Part Number	Package Description	Carrier Type	Temperature Range
iW7039-00-QFN4	QFN-7mm x 7mm 48-pin with EP	Tape and Reel ¹	-20°C to +85°C

1. Tape & Reel packing quantity is 4,000/reel. Minimum ordering quantity is 4,000.

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