

September 29, 2010

**Product Specifications of the μ PD48011318, μ PD48011336,
 μ PD48011418 and μ PD48011436**

Product Name		μ PD48011318	μ PD48011336	μ PD48011418	μ PD48011436
Memory Capacity		1.1Gb			
Bit Width		x18	x36	x18	x36
Number of Banks		8			
Burst Length		2		4	
Supply Voltage	VDD	1.5V +/-7%			
	VDDQ	1.0V +/-0.05V			
	VEXT	2.5V +/-0.2V			
Operating Frequency		525MHz/600MHz		667MHz/800MHz	
Latency	tRC	13.3ns		13.75ns	
	tRL	tRC+2 (No data Inversion)			
	tWL	tRL+1			
Termination		VDDQ (High Side)			
Package		180-pin BGA (14 x 18.5mm)			
Address Input Scheme		DDR			
Address Input Pin Count		14-pin (A[13:0])		7-pin (A[6:0])	
New Features		Programmable On Die Termination, Per Pin Deskew, Data Inversion			