

Separate Sheet

Product specifications of the RH850/P1L-C Group of microcontrollers

Group	RH850/P1L-C			
Product name	R7F701391EAFP	R7F701390EAFP	R7F701389EAFP	R7F701388EAFP
CPU	120 MHz (with Lockstep)			
Code flash	512 KB		1 MB	
Data flash (EEPROM)	32 KB		64 KB	
RAM	RAM 64 KB + ERAM 8 KB		RAM 100 KB + ERAM 8 KB	
DMA	8 (DMAC)/ 128 (DTS)			
MPU	16 areas			
Timer functions	System Timer: 1 unit WDT: 1 ch GTM: Input 8 ch/ Output 6 ch	System Timer: 1 unit WDT: 1 ch GTM: Input 16 ch/ Output 12 ch		
Communications	CAN: 2 ch LIN/UART: 2 ch CSI: 3 ch			
Sensor IF	SENT: 2 ch		SENT: 3 ch	SENT: 4 ch
12-bit ADC	8 ch x 1 (Include 2 ch T&H)	12 ch x 1 (Include 2 ch T&H)	12 ch x 1 (Include 4 ch T&H)	20 ch x 1 (Include 4 ch T&H)
Power supply	3.3 V or 5.0 V			
Other functions	Error Control Module, Data CRC, Core Voltage Monitor, Temperature Sensor, Security module (ICU-S)			
Package	80-pin LQFP (10mm x 10mm 0.4mm pitch)	100-pin LQFP (12mm x 12mm 0.4mm pitch)		144-pin LQFP (16mm x 16mm 0.4mm pitch)

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